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# Service Manual



ORDER NO. RRV2963

**DVD RECORDER** 

# **DVR-520H-S DVR-65H-S**

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Region No.	Serial No. Please confirm 3rd & 4th alphabetical letters.
DVR-520H-S	KU/CA	AC120V	1	
DVR-520H-S	KUXU/CA	AC120V	1	&&PG#####\$\$
DVR-65H-S	KCXU	AC120V	1	&&PG#####\$\$

- When servicing this model, some service procedures may reset the settings that customer set (\*) to the factory default settings. Make sure to explain this to the customer.
  - (\*): Initial Setup (Clock Setting, Remote Control Set, Channel settings, Video Out settings, Audio In settings, Audio Out settings, Language settings)

Refer to the chapter 13 of the Operating Instructions for more details.

An HDD (Hard Disc Drive) is mounted in this product.

The HDD is a precision instrument very vulnerable to shock and electrostatic charges. Please read "7.4 Cautions on Handling the HDD" in this manual and exercise sufficient caution when handling the HDD itself, as well as the product with the HDD built in.

When an HDD becomes defective and inoperable, restoration of the user's data recorded on the HDD, or copying of the user's recorded data to other media (such as a new HDD) is totally impossible. Before servicing, OBTAIN THE USER'S PRIOR CONSENT to that effect.

The user must be made aware that all recorded data are deleted if the HDD is intialized.







For details, refer to "Important symbols for good services".

PIONEER CORPORATION 4-1, Meguro 1-chome, Meguro-ku, Tokyo 153-8654, Japan PIONEER ELECTRONICS (USA) INC. P.O. Box 1760, Long Beach, CA 90801-1760, U.S.A. PIONEER EUROPE NV Haven 1087, Keetberglaan 1, 9120 Melsele, Belgium PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 253 Alexandra Road, #04-01, Singapore 159936

# SAFETY INFORMATION



This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

### WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

### NOTICE

### (FOR CANADIAN MODEL ONLY)

### REMARQUE

### (POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible — (fusible de type rapide) et/ou — (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

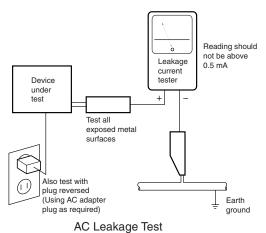
# - (FOR USA MODEL ONLY) -

### 1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

### LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60 Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5 mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

## 2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a  $\triangle$  on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

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DVR-520H-S

LASER DIODE CHARACTERISTICS – MAXIMUM OUTPUT POWER : 70 mw WAVELENGTH : 658 nm WARNING!

DEVICE INCLUDES LASER DIODE WHICH EMITS INVISIBLE INFRARED RADIATION WHICH IS DANGEROUS TO EYES. THERE IS A WARNING SIGN ACCORDING TO PICTURE 1 INSIDE THE DEVICE CLOSE TO THE LASER DIODE.



LASER
Picture 1
Warning sign for laser radiation

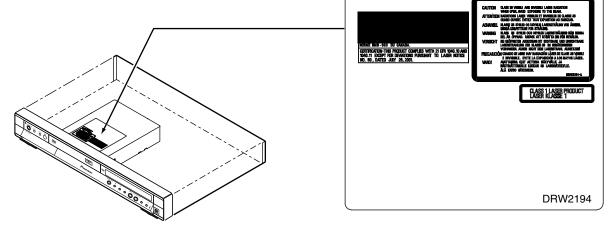
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# **■ LABEL CHECK**



### Additional Laser Caution -

- The ON/OFF(ON:low level,OFF:high level) status of the CLAMP signals for detecting the loading state are detected by the drive CPUs, and the design prevents laser diode oscillation when the CLAMP signal turns OFF.
  - In normal operation, if no disc is clamped, the laser diode oscillation is disabled.
  - However, the interlock does not always operate in the test mode.
- When the cover is opened, close viewing of the objective lens with the naked eye will cause exposure to a Class 3A laser beam.

■ 2 ■ 3 ■ 4

# [ Important symbols for good services ]

In this manual, the symbols shown-below indicate that adjustments, settings or cleaning should be made securely. When you find the procedures bearing any of the symbols, be sure to fulfill them:

### 1. Product safety

1



You should conform to the regulations governing the product (safety, radio and noise, and other regulations), and should keep the safety during servicing by following the safety instructions described in this manual.

### 2. Adjustments



To keep the original performances of the product, optimum adjustments or specification confirmation is indispensable. In accordance with the procedures or instructions described in this manual, adjustments should be performed.

# 3. Cleaning



For optical pickups, tape-deck heads, lenses and mirrors used in projection monitors, and other parts requiring cleaning, proper cleaning should be performed to restore their performances.

# 4. Shipping mode and shipping screws



To protect the product from damages or failures that may be caused during transit, the shipping mode should be set or the shipping screws should be installed before shipping out in accordance with this manual, if necessary.

# 5. Lubricants, glues, and replacement parts



Appropriately applying grease or glue can maintain the product performances. But improper lubrication or applying glue may lead to failures or troubles in the product. By following the instructions in this manual, be sure to apply the prescribed grease or glue to proper portions by the appropriate amount. For replacement parts or tools, the prescribed ones should be used.

4

Ε

# **CONTENTS**

SAFETY INFORMATION	
1. SPECIFICATIONS	
2. EXPLODED VIEWS AND PARTS LIST	8
2.1 PACKING	
2.2 EXTERIOR	10
2.3 FRONT PANEL	13
3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM	16
3.1.1 OVERALL BLOCK DIAGRAM	16
3.1.2 TUNB and JCKB ASSY BLOCK DIAGRAM	18
3.1.3 MAIN ASSY BLOCK DIAGRAM	20
3.1.4 POWER BLOCK DIAGRAM	22
3.2 OVERALL WIRING DIAGRAM	24
3.3 TUNB ASSY	26
3.4 JCKB ASSY(1/2)	
3.5 JCKB(2/2), ATWB and ATHB ASSYS	
3.6 MAIN ASSY(1/5)	
3.7 MAIN ASSY(2/5)	
3.8 MAIN ASSY(3/5)	
3.9 MAIN ASSY(4/5)	
3.10 MAIN ASSY(5/5)	
3.11 FLKY, KIRB, FRJB and DVJB ASSYS	
3.12 POWER SUPPLY UNIT	
3.13 WAVE FORMS	
4. PCB CONNECTION DIAGRAM	
4.1 TUNB ASSY	
4.2 JCKB, ATWB and ATHB ASSYS	52
4.3 MAIN ASSY	56
4.4 FLKY, KIRB, FRJB and DVJB ASSYS	60
4.5 POWER SUPPLY UNIT	
5. PCB PARTS LIST	
6. ADJUSTMENT	69
6.1 TUNB ASSY ADJUSTMENT	
6.2 MAIN ASSY ADJUSTMENT	
7. GENERAL INFORMATION	71
7.1 DIAGNOSIS	
7.1.1 CPRM ID NUMBER AND DATA SETTING	72
7.1.2 MODEL SETTING	
7.1.3 DOWNLOAD METHOD	
7.1.4 SERVICE MODE	
7.1.5 DV DEBUG MODE	91
7.1.6 ERROR RATE MEASUREMENT	
7.1.7 VIDEO ADJUSTMENT FOR SPECIFIC AREA	96
7.1.8 AGING MODE	
7.1.9 SETUP SEQUENCE	
7.1.10 DISASSEMBLY	
7.2 IC	
7.3 OUTLINE OF THE PRODUCT	
7.4 CAUTIONS ON HANDLING THE HDD.	
7.5 DISC/CONTENT FORMAT	
7.6 CLEANING	
8. PANEL FACILITIES	

5

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# 1. SPECIFICATIONS

	General
	System
١	Video-CD, CD,
	CD-R/RW (WMA, MP3, JPEG, CD-DA)
	Power requirements
	Power consumption DVR-65H
	DVR-65H
	Power consumption in standby mode 0.48 W
ı	(Front panel display: off)
•	Weight
	Dimensions
	Operating temperature +5∞Cto +35∞C
	Operating humidity5% to 85%
	(no condensation)
3	TV format
	Recording
	Recording formatDVD Video Recording DVD-VIDEO
	Recordable discs
	DVD-RW (DVD Re-recordable disc)
	DVD-R (DVD Recordable disc)
	2.2.1.(2.2.1.000144310-4.00)
	Video recording format
	Sampling frequency
	Compression format MPEG
	Audio recording format
	Sampling frequency
	Compression format Dolby Digital or Linear PCM
	(uncompressed)
	Recording time
	HDD
	Fine (FINE)
_	Long Play (LP)
	Extended Play (EP) Approx. 102 hours
	Manual Mode (MN) Approx. 17-102 hours
	DVD-R/DVD-RW
	Fine (FINE)
	Standard Play (SP) Approx. 2 hours
	Long Play (LP)
)	Extended Play (EP) Approx. 6 hours
	Manual Mode (MN) Approx. 1-6 hours
	Tuner
	Receivable channels
_	VHF
	UHF
	CATV C1-C125ch

# Timer

Programs 1 month/32 programs
ClockQuartz lock (12-hour digital display)
Power off memory Approx. 5 years (after manufacture)
Input/Output
VHF/UHF antenna input/output terminal VHF/UHF set
75 Ω (F-shape connector)
Video input Input 1 (rear), 3 (rear), 2 (front)
Input level
Jacks
Video outputOutput 1,2
Output level
JacksRCA jack
S-Video input
Y (luminance) - Input level 1 Vp-p (75 Ω)
C (color) - Input level
Jacks4 pin mini DIN
S-Video outputOutput 1,2
Y (luminance) - Output level1 Vp-p (75 Ω)
C (color) - Output level
Jacks
•
Component video output
Output level Y: 1.0 Vp-p ( $75\Omega$ )
PB, PR: 0.7 Vp-p (75Ω)
JacksRCA jacks
Audio input Input 1, 3 (rear), 2 (front) L/R
Input level
During audio input 2V rms
(Input impedance: more than 22 k $\Omega$ )
Jacks
Audio output Output 1,2 L/R
1 1
During audio output
(Output impedance: less than 1.5 k $\Omega$ )

# **Supplied accessories**

Remote control1
Dry cell batteries (AA/R6P)
Audio / Video cable (red/white/yellow)
RF antenna cable1
Power cable
Operating Instructions
Warranty card1

 Jacks
 RCA jacks

 Control input
 Mini jack

 DV input/output (DVR-520H only)
 4 pin

 (i.LINK/IEEE 1394 standard)

Note: The specifications and design of this product are subject to change without notice, due to improvement.

6

DVR-520H-S



• Dry cell batteries ×2 (AA/R6P)

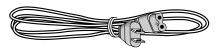


• RF antenna cable  $\times 1$  (VDE1025)



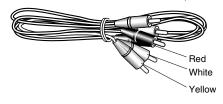
• Power cable ×1

(ADG7021 : DVR-520H-S/KU/CA) (ADG7075 : DVR-520H-S/KUXU/CA DVR-65H-S/KCXU)



 Audio / Video cable(L=1.5m) ×1 (red/white/yellow)

(XDE3049 : DVR-520H-S/KU/CA) (VDE1077 : DVR-520H-S/KUXU/CA DVR-65H-S/KCXU)



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DVR-520H-S

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# 2. EXPLODED VIEWS AND PARTS LIST

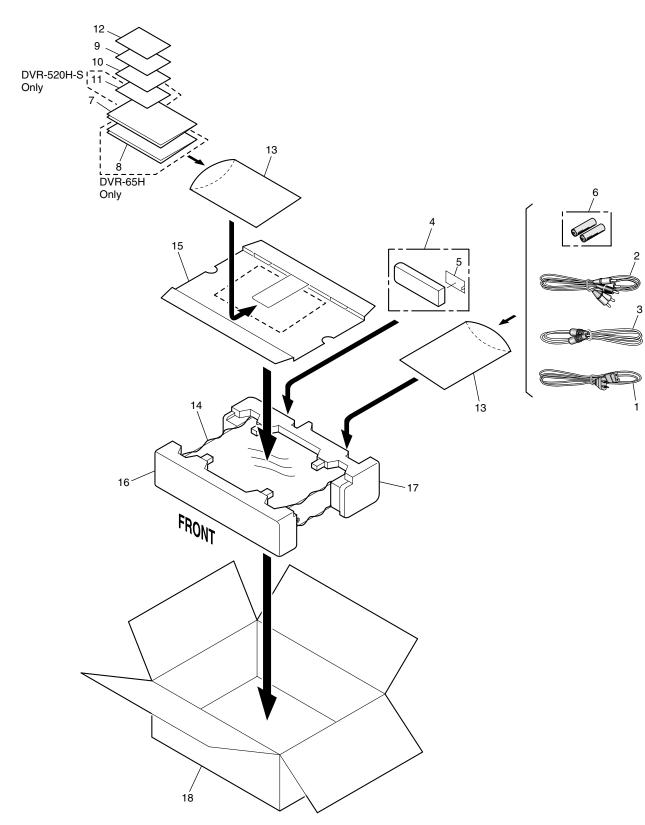
NOTES: ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- The  $\triangle$  mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Screws adjacent to ▼ mark on product are used for disassembly.
- For the applying amount of lubricants or glue, follow the instructions in this manual. (In the case of no amount instructions, apply as you think it appropriate.)

# 2.1 PACKING

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DVR-520H-S

# **PACKING parts List**

Mark No.	<u>Description</u>	Part No.	Mark No.	<u>Description</u>	Part No.	
<u> </u>	Power Cable	See Contrast table (2)	NSP 9	HDD Caution 8L B	VRR1046	
2	Audio / Video Cable	See Contrast table (2)	NSP 10	HDD Caution 8L	VRR1047	Δ
3	RF Antenna Cable	VDE1025	NSP 11	Card	See Contrast table (2)	-
4	Remote Control	VXX2932	NSP 12	Warranty Card	See Contrast table (2)	
5	Battery Cover	AZN7933	13	Polyethylene Bag	VHL1051	
NSP 6	Dry Cell Battery (R6P, AA)	See Contrast table (2)	14	Mirror Sheet	VHL1006	_
7	Operating Instructions	See Contrast table (2)	15	IM Holder	See Contrast table (2)	
	(English)		16	Front Pad	See Contrast table (2)	
8	Operating Instructions	See Contrast table (2)	17	Rear Pad	See Contrast table (2)	
	(French)	, ,	18	Packing Case	See Contrast table (2)	

(2) CONTRAST TABLE
DVR-520H-S/KU/CA, KUXU/CA and DVR-65H/KCXU are constructed the same except for the following:

Mark	No.	Symbol and Description	DVR-520H-S /KU/CA	DVR-520H-S /KUXU/CA	DVR-65H-S /KCXU
<u> </u>	1	Power Cable	ADG7021	ADG7075	ADG7075
	2	Audio / Video Cable	XDE3049	VDE1077	VDE1077
NSP	6	Dry Cell Battery (R6P, AA)	VEM1031	VEM1030	VEM1030
	7	Operating Instructions	VRB1338	VRB1338	VRB1343
		(English)			
	8	Operating Instructions (French)	Not used	Not used	VRC1210
NSP	11	Card	VRY1132	VRY1132	Not used
NSP	12	Warranty Card	ARY7045	ARY7045	ARY7007
	15	IM Holder	VHC1116	VHC1115	VHC1115
	16	Front Pad	VHA1368	VHA1364	VHA1364
	17	Rear Pad	VHA1369	VHA1365	VHA1365
	18	Packing Case	VHG2545	VHG2530	VHG2549

9

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DVR-520H-S

# 2 3 2.2 EXTERIOR 55 62 В 53 √ 62 Lithium Battery (CR2032) G F O 36 J 23 С 39 65 53 38 **%**-60 **`** 64 63 6361 0.00 Ε G) C (65H type) (520H type) 49 Refer to "2.3 FRONT PANEL SECTION". 10 DVR-520H-S

	5	6	7	
EXTERIOR pa	arts List			

Mark I	<u>No.</u>	<u>Description</u>	Part No.	Mark No.	<u>Description</u>	Part No.	
	1	TUNB Assy	VWV2010	34	FFC Protector	VEC2436	
	2	JCKB Assy	VWV2023	35	Gasket 20 x 8T	VEC2437	Α
	3	MAIN Assy	VWV2025				,,
	4	FRJB Assy	VWV2004	36	Gasket 75 x 7T	VEC2439	
	5	DVJB Assy	VWV2006	37	Gasket Sheet 2	VEC2440	
		•		38	Aluminum Tape	VEF1056	
	6	ATWB Assy	VWV2009	NSP 39	HDD Stay	VNE2334	
	7	ATHB Assy	VWV2011	NSP 40	Bonnet Angle	VNE2335	
<u> </u>	8	POWER SUPPLY Unit	VWR1380				
	9	DRIVE Assy R7 (for Service)	VXX2942	NSP 41	Writer Stay L	VNE2336	
	10	HDD	See Contrast table (2)	NSP 42	Writer Stay R	VNE2337	
			( )	NSP 43	PCB Base	VNE2339	
	11	DC Fan Motor	VXM1114	NSP 44	Heatsink	VNH1070	В
	12	Ferrite Core	ATX1048	45	SYPS Cover	VNK5426	
	13	Ferrite Core	See Contrast table (2)				
	14	Connector Assy (13P)	PF13PP-D47	46	Fan Duct	VNK5427	
	15	Flexible Cable (8P)	See Contrast table (2)	NSP 47	Base Chassis Assy	VXA2682	
		,	( )	NSP 48	Binder (BK-1)	ZCA-BK1	_
	16	Flexible Cable (24P)	See Contrast table (2)	49	HDD Badge	See Contrast table (2)	
	17	Flexible Cable (32P)	See Contrast table (2)	50	Tray Sheet	See Contrast table (2)	
	18	Flexible Cable (7P)	See Contrast table (2)				
	19	Flexible Cable (13P)	See Contrast table (2)	51	Tray Panel	See Contrast table (2)	
	20	Flexible Cable (40P)	See Contrast table (2)	52	Bonnet Label	VRW2104	
		` ,	( )	NSP 53	Tape	ZTA-156A-19	С
	21	Shield Flexible Cable (40P)	See Contrast table (2)	54	Rear Panel	See Contrast table (2)	
	22	Housing Assy (4P)	See Contrast table (2)	55	Bonnet Case S	VXX2925	
	23	Housing Assy (2P)	See Contrast table (2)				
	24	Housing Assy (4P)	See Contrast table (2)	56	Tray Panel Base	See Contrast table (2)	
NSP	25	P. Plate Holder	PNY-405	57	Tray Panel Lens	See Contrast table (2)	
				58	Screw	BPZ30P250FTC	_
	26	Rubber Foot	VEB1349	59	Screw	PBZ30P080FTC	
	27	Radiation Sheet	VEB1360	60	Screw	AMZ30P060FTC	
	28	Rubber Spacer	VEB1370				
NSP	29	PC Support	VEC1749	61	Screw	BBZ30P040FTC	_
	30	Heatsink Cushion	VEC2363	62	Screw	BCZ40P060FNI	D
				63	Screw	BPZ30P080FTC	
	31	Gasket Sheet	VEC2394	64	Screw	BBZ30P060FTC	
	32	Spacer	VEC2413	65	#6-32 Screw	DBA1125	
NSP	33	Clamp	VEC2418				
		•					

(2) CONTRAST TABLE DVR-520H-S/KU/CA, KUXU/CA and DVR-65H/KCXU are constructed the same except for the following :

Mark	No.	Symbol and Description	DVR-520H-S /KU/CA	DVR-520H-S /KUXU/CA	DVR-65H-S /KCXU
	10	HDD 80G 4R080L0 SV	VXF1010	VXF1010	Not used
	10	HDD 160G 4R160L0 SV	Not used	Not used	VXF1028
	13	Ferrite Core	VTH1051	VTH1050	VTH1050
	15	Flexible Cable (8P)	VDA2011	VDA1997	VDA1997
	16	Flexible Cable (24P)	VDA2012	VDA1998	VDA1998
	17	Flexible Cable (32P)	VDA2013	VDA1999	VDA1999
	18	Flexible Cable (7P)	VDA2014	VDA2000	VDA2000
	19	Flexible Cable (13P)	VDA2015	VDA2001	VDA2001
	20	Flexible Cable (40P)	VDA2034	VDA2032	VDA2032
	21	Shield Flexible Cable (40P)	VDA2033	VDA2031	VDA2031
	22	Housing Assy (4P)	VKP2330	VKP2327	VKP2327
	23	Housing Assy (2P)	VKP2332	VKP2315	VKP2315

11

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DVR-520H-S

		1 2	_	3	-
Mark	No.	Symbol and Description	DVR-520H-S /KU/CA	DVR-520H-S /KUXU/CA	DVR-65H-S /KCXU
	24	Housing Assy (4P)	VKP2348	VKP2335	VKP2335
	49	HDD Badge	VAM1141	VAM1141	Not used
	50	Tray Sheet B	VEC2409	VEC2409	Not used
	50	Tray Sheet	Not used	Not used	VEC2448
	51	Tray Panel	VNK5421	VNK5421	Not used
	54	Rear Panel	VNA2704	VNA2704	VNA2737
	56	Tray Panel Base	Not used	Not used	VNK5432
	57	Tray Panel Lens	Not used	Not used	VNK5592

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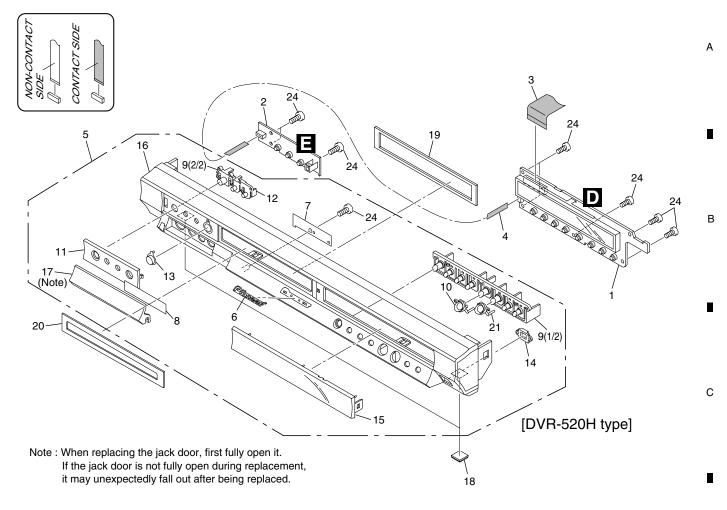
DVR-520H-S

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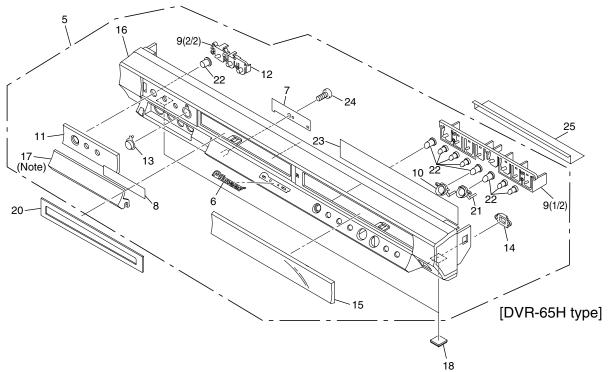
# 2.3 FRONT PANEL

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	Mark No.	<u>Description</u>	Part No.	Mark No.	<u>Description</u>	Part No.
Α	1	FLKY Assy	VWG2489	NSP 14	DV Cover	VNK5451
	2	KIRB Assy	VWG2493	NSP 15	FL Lens	See Contrast table (2)
	3	Flexible Cable (19P)	See Contrast table (2)			
	4	Flexible Cable (7P)	See Contrast table (2)	NSP 16	Front Panel	See Contrast table (2)
	5	Front Panel Assy	See Contrast table (2)	17	Jack Door	VNK5494
•				18	Rubber Foot	VEB1349
	6	Pioneer Name Plate	See Contrast table (2)	19	Drive Sheet	VEC2345
	7	Door Spring	VBK1151	20	Panel Sheet	See Contrast table (2)
	NSP 8	Mirror Sheet	VHL1082			
В	NSP 9	Main Key	See Contrast table (2)	NSP 21	Copy Ring	VNK5523
	NSP 10	REC Ring	VNK5425	NSP 22	Cap	See Contrast table (2)
				23	FL Filter	See Contrast table (2)
	NSP 11 NSP 12	Sub Panel Power Key	See Contrast table (2) VNK5480	24	Screw	BPZ30P080FTC
				25	Sheet S	See Contrast table (2)
	NSP 13	IR Window	See Contrast table (2)			

# $\hbox{\bf (2) CONTRAST TABLE} \\ \hbox{\rm DVR-520H-S/KU/CA, KUXU/CA and DVR-65H/KCXU are constructed the same except for the following:} \\$

	Mark	No.	Symbol and Description	DVR-520H-S /KU/CA	DVR-520H-S /KUXU/CA	DVR-65H-S /KCXU	
С		3	Flexible Cable (19P)	VDA2016	VDA2002	VDA2002	
		4 Flexible Cable (7P)		VDA2019	VDA2005	VDA2005	
		5	Front Panel Assy	VXA2645	VXA2645	VXA2649	
		6	Pioneer Name Plate	VAM1146	VAM1146	VAM1147	
	NSP	9	Main Key	VNK5420	VNK5420	VNK5436	
•	NSP	11	Sub Panel	VNK5483	VNK5483	VNK5526	
	NSP	13	IR Window	VNK5423	VNK5423	Not used	
	NSP	15	FL Lens	VNK5422	VNK5422	VNK5593	
	NSP	16	Front Panel	VNK5441	VNK5441	VNK5527	
		20	Panel Sheet	VEC2446	VEC2446	Not used	
D	NSP	22	Сар	Not used	Not used	VNK5437	
		23	FL Filter	Not used	Not used	VEC2421	
		25	Sheet S	Not used	Not used	VEC2426	

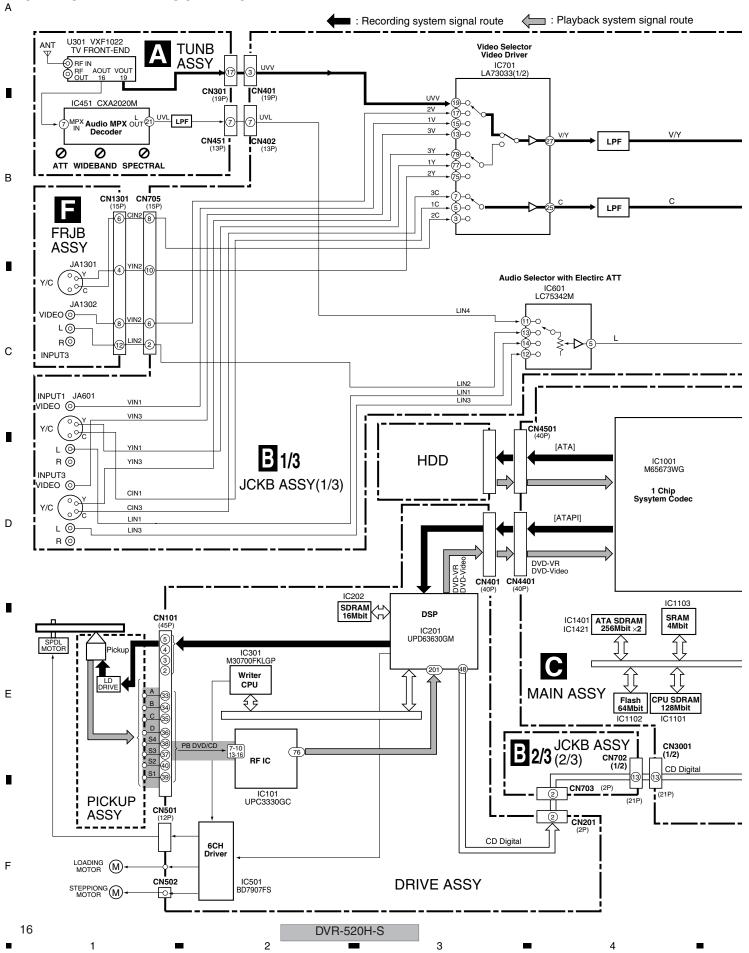
DVR-520H-S

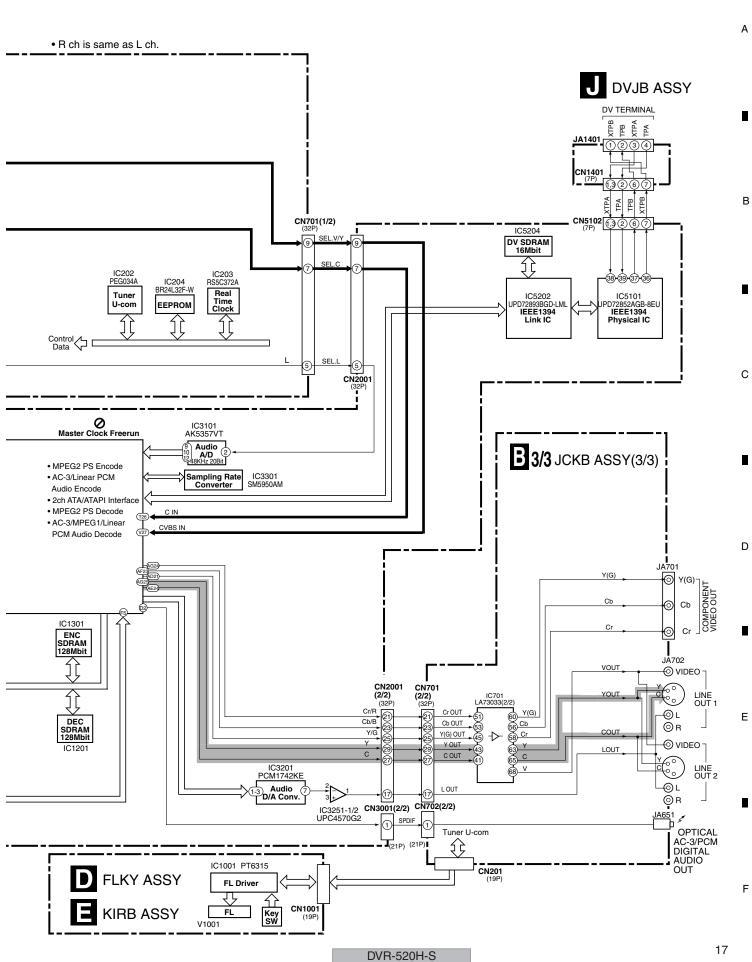
В С Ε 15 DVR-520H-S 5

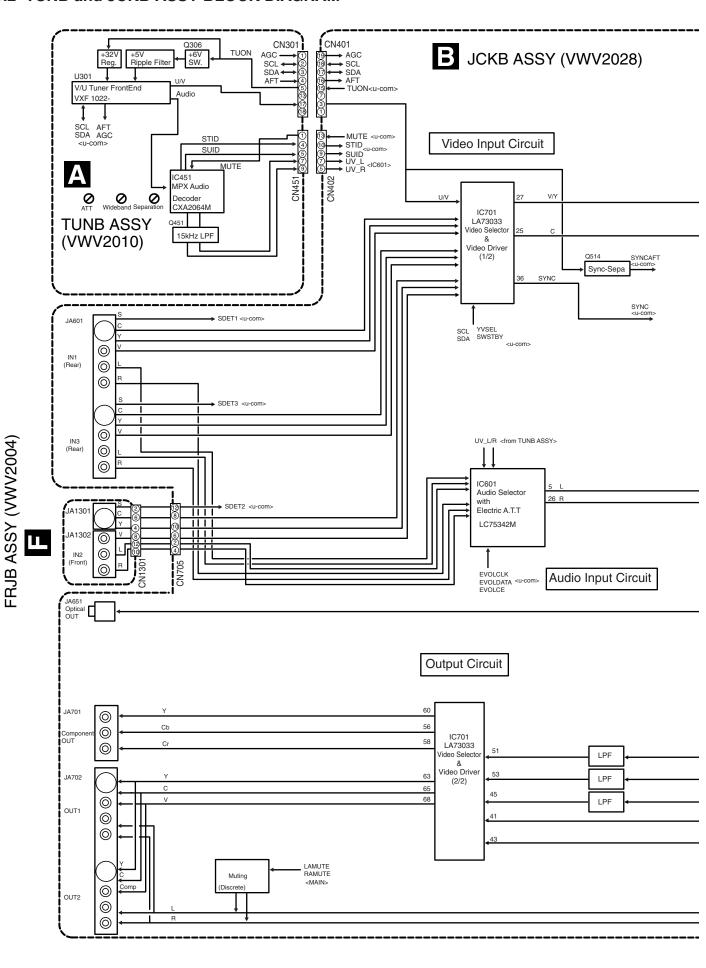
# 3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM

# 3.1 BLOCK DIAGRAM

# 3.1.1 OVERALL BLOCK DIAGRAM







18

Е

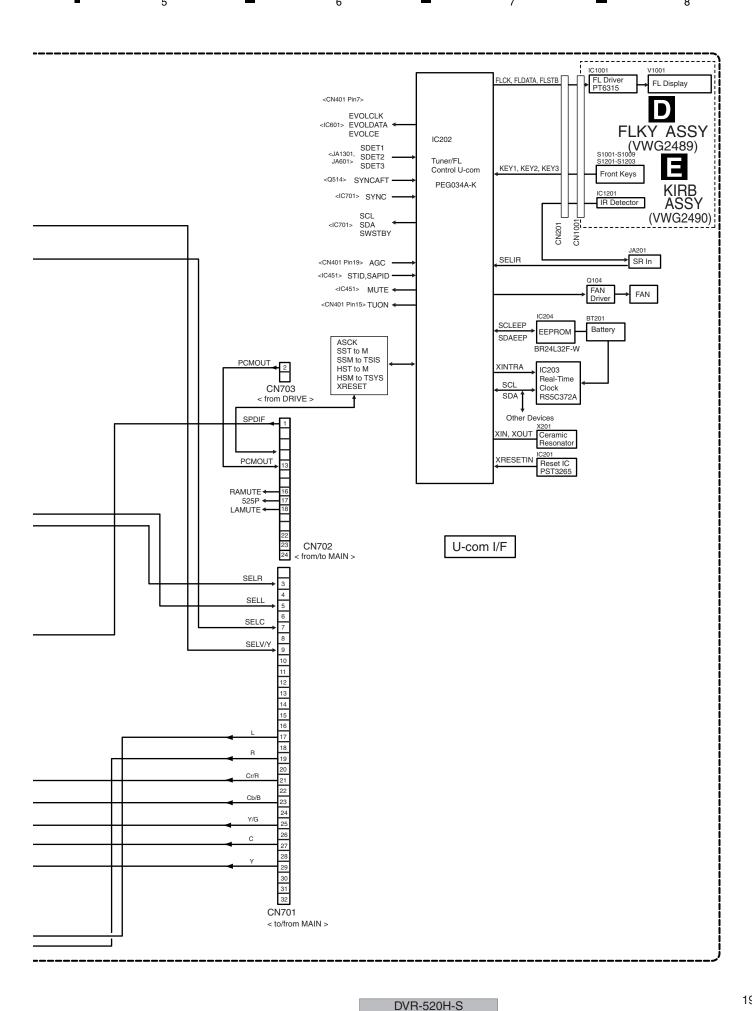
Α

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DVR-520H-S

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В

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DVR-520H-S

VSS1195 X4102 VCXO Input Mster clock IC4101 27M ADMCLKI 33/36M IC3402 SM8707KV PLL IC 24M SRC\*\*I SM5950AM IC3301 SRC SRC\*\*O DVPLLCK(ADMCLKI) AMCLK1 36M AMCLK2 33M VMCLKI 27M DVVPWM IC4205 TC7WHU04FU Play Mster clock IC4206 **DVVCLKO** IC5341 VCLKI HCLK0 IC5202 HOST BUS UPD72893BGD-LML UPD72852AGB-8EU LINK/DV CODEC MCLK **SCLK** IC1101 IC5204 IC5101 **CPU SDRAM** DV SDRAM Phy K4S281632E-TC75 K4S161622D-TC80 DV H

DVR-520H-S

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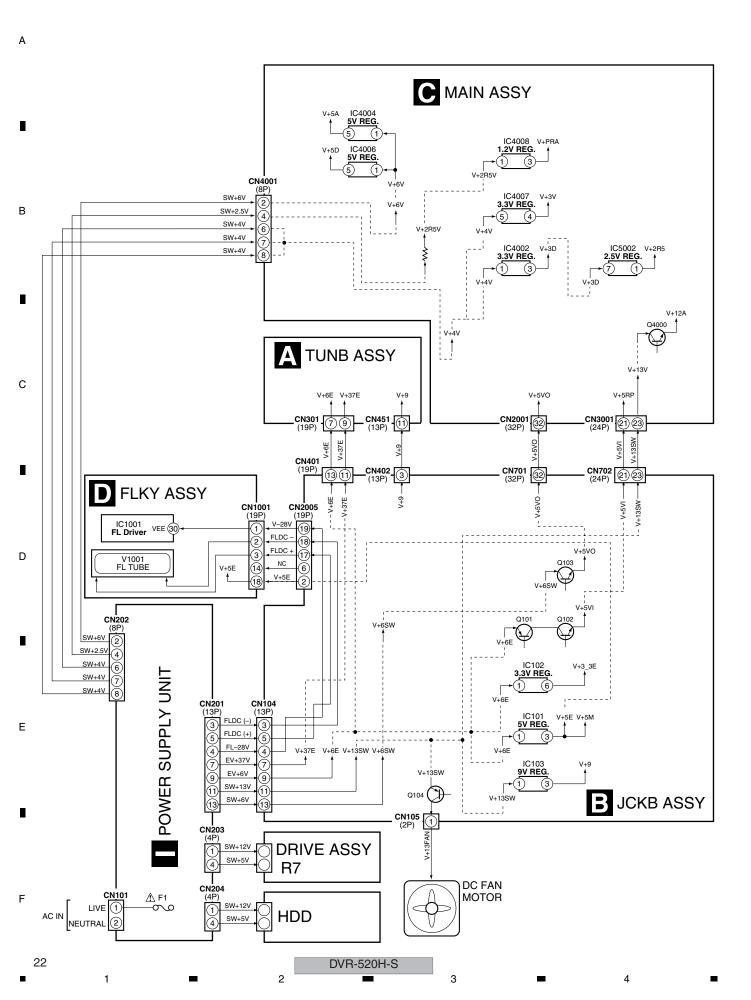
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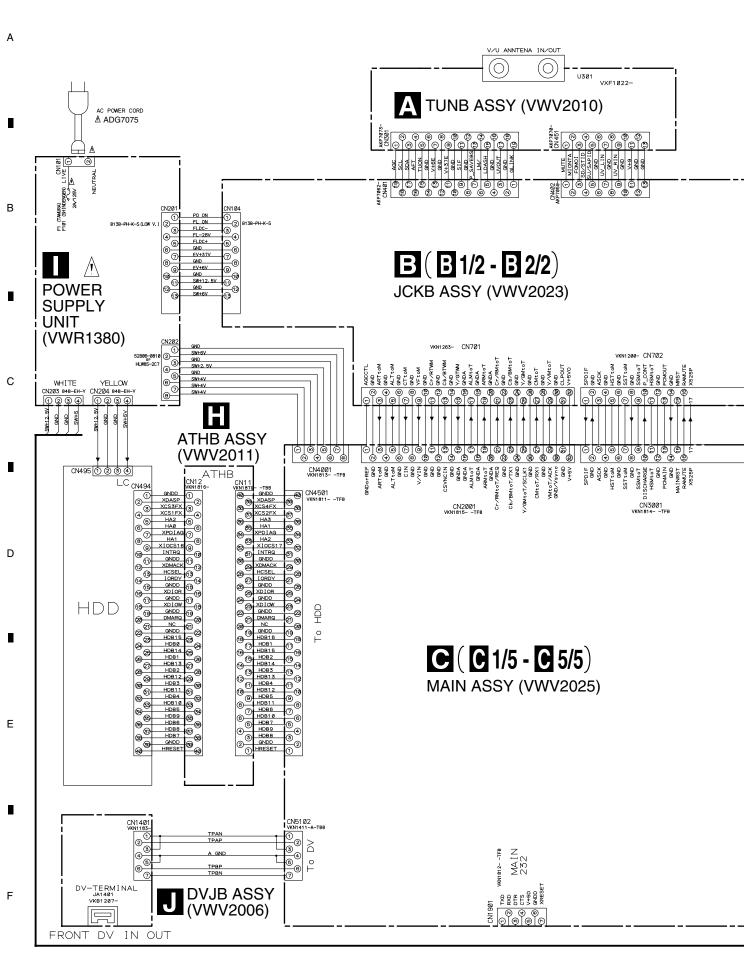
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В С Ε

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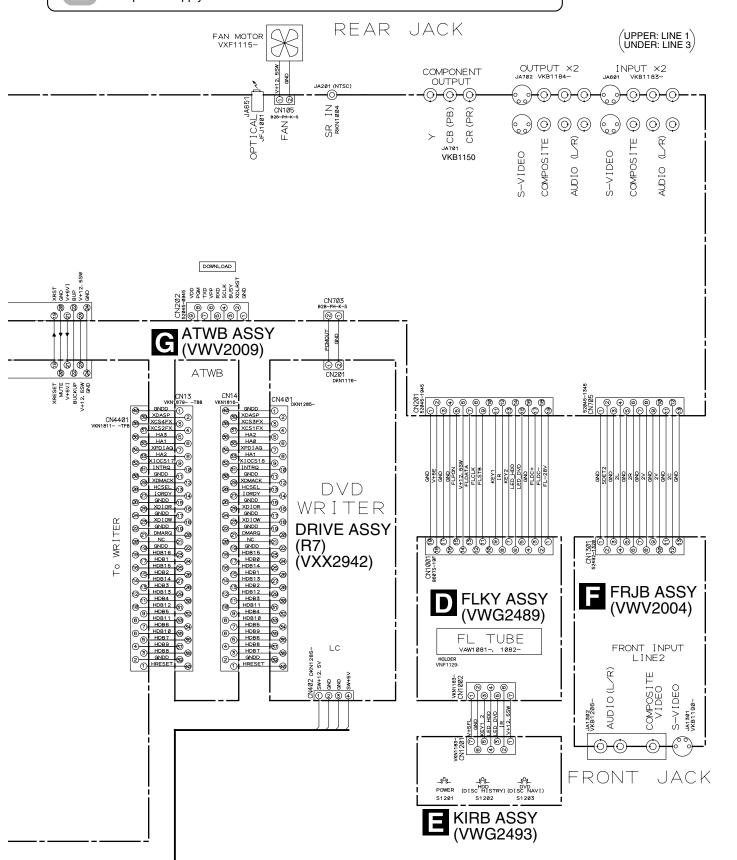
DVR-520H-S 7 8



DVR-520H-S

- When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".
- The ⚠ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- : The power supply is shown with the marked box.

5



DVR-520H-S

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8

В

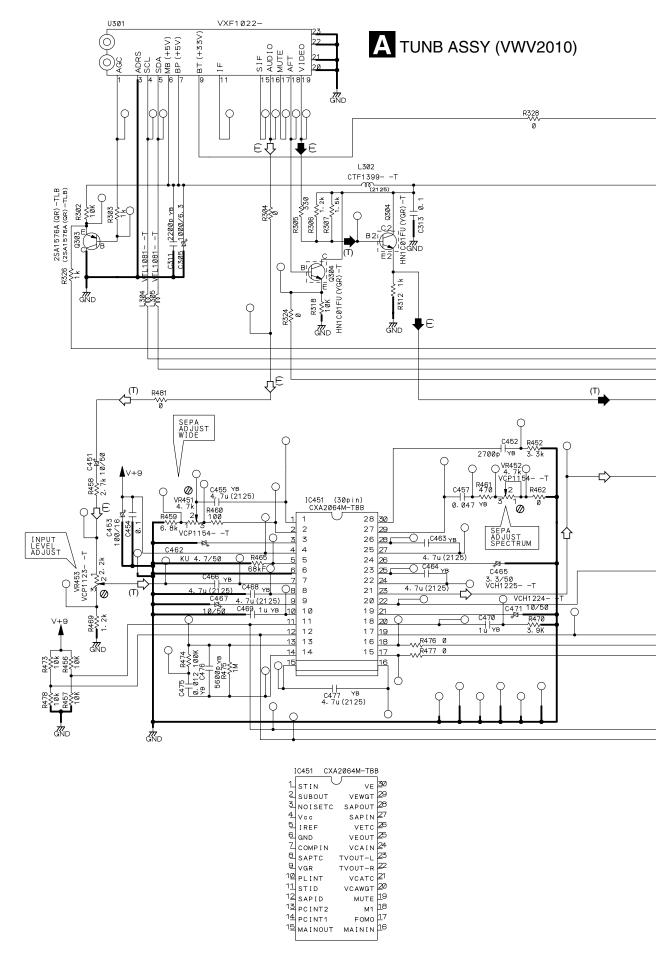
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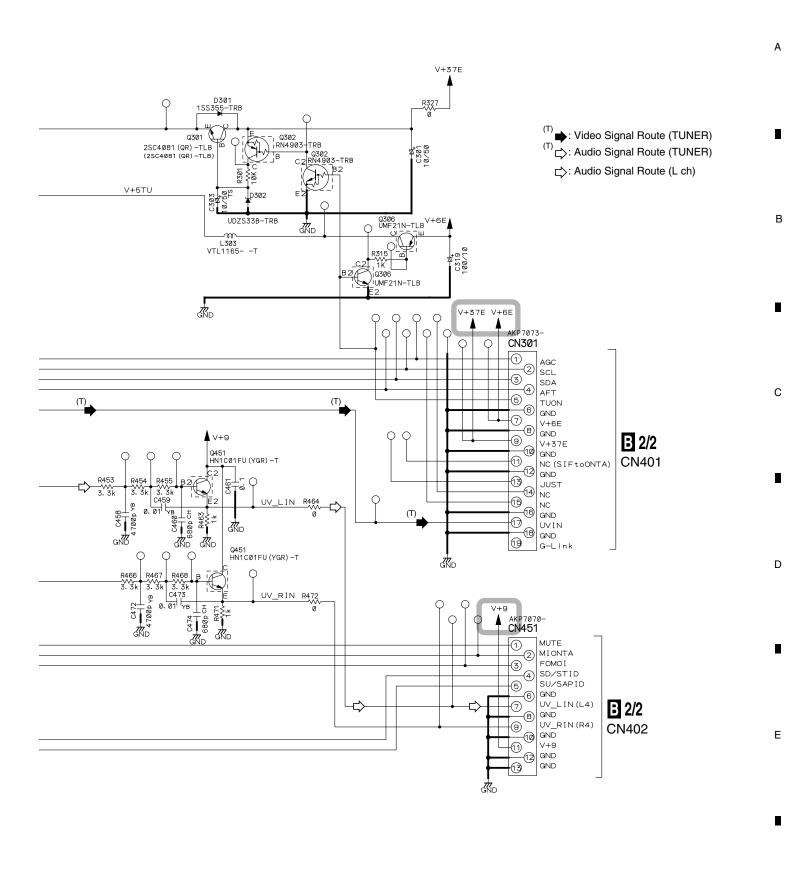
**A** 

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DVR-520H-S

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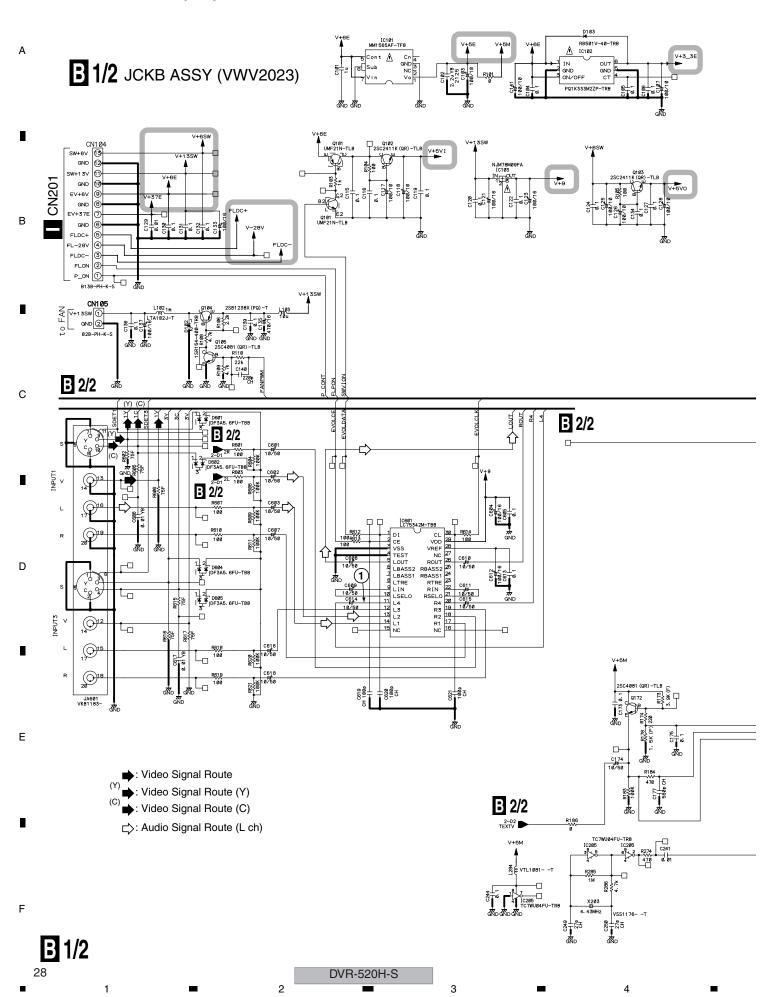
2

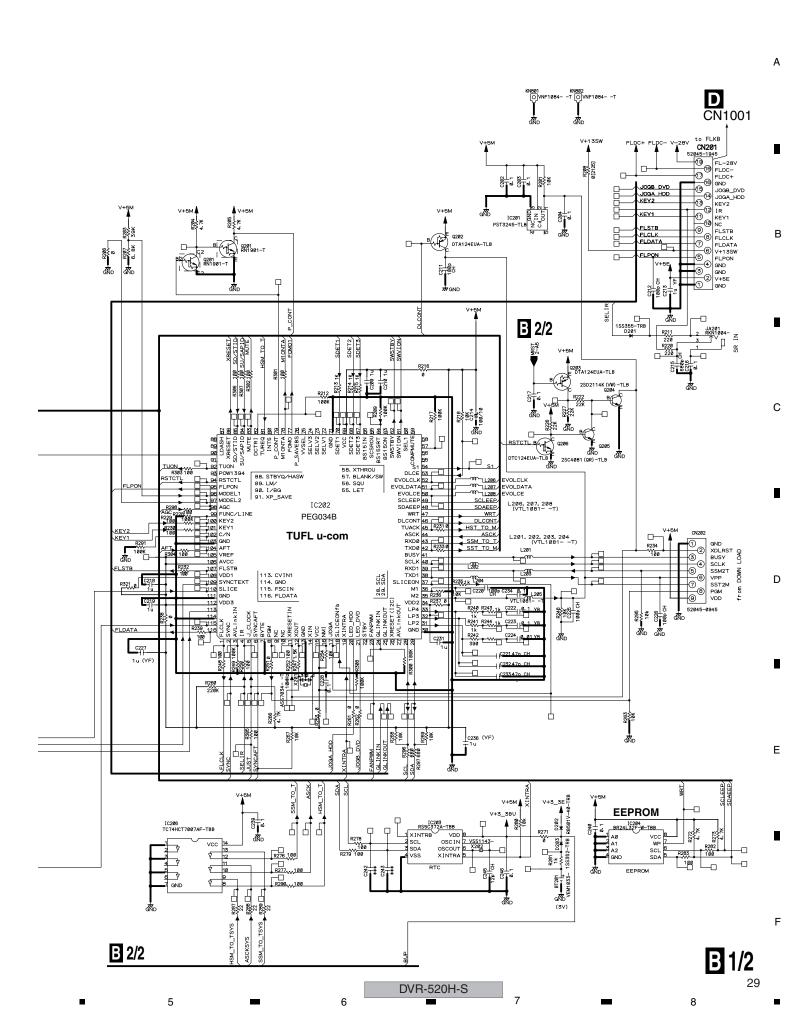


A

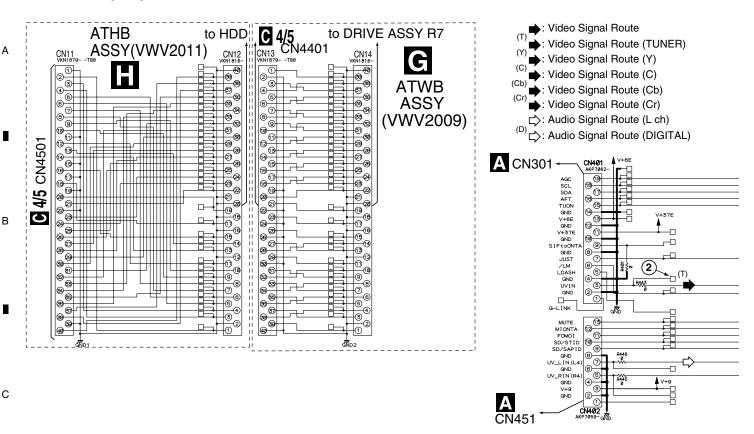
DVR-520H-S 7

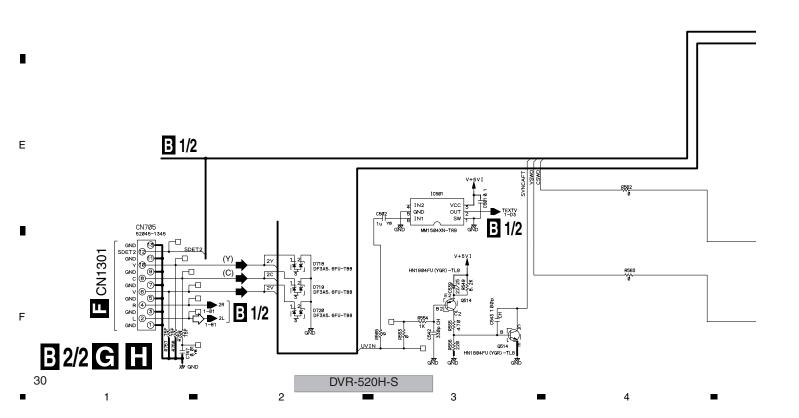
# 3.4 JCKB ASSY(1/2)

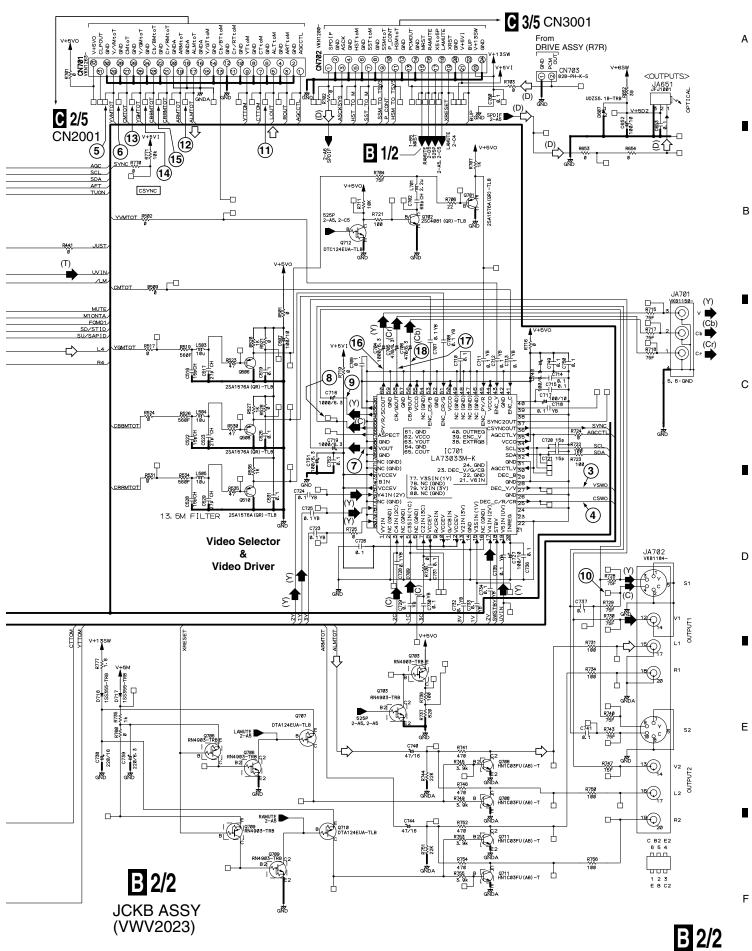




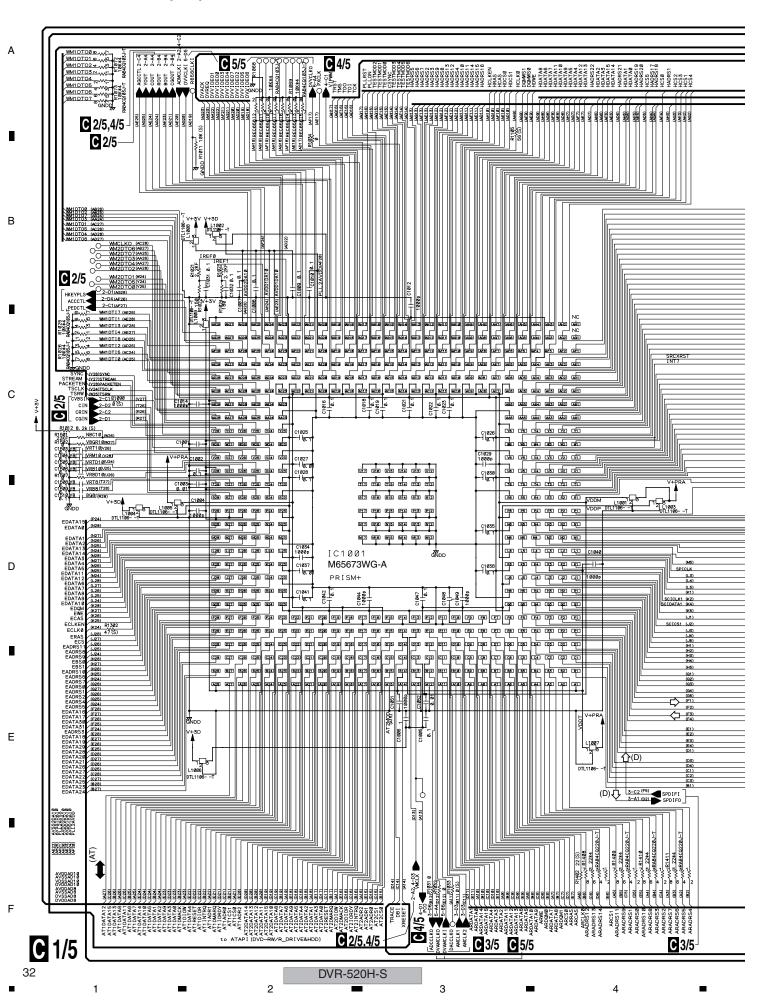
# 3.5 JCKB(2/2), ATWB and ATHB ASSYS

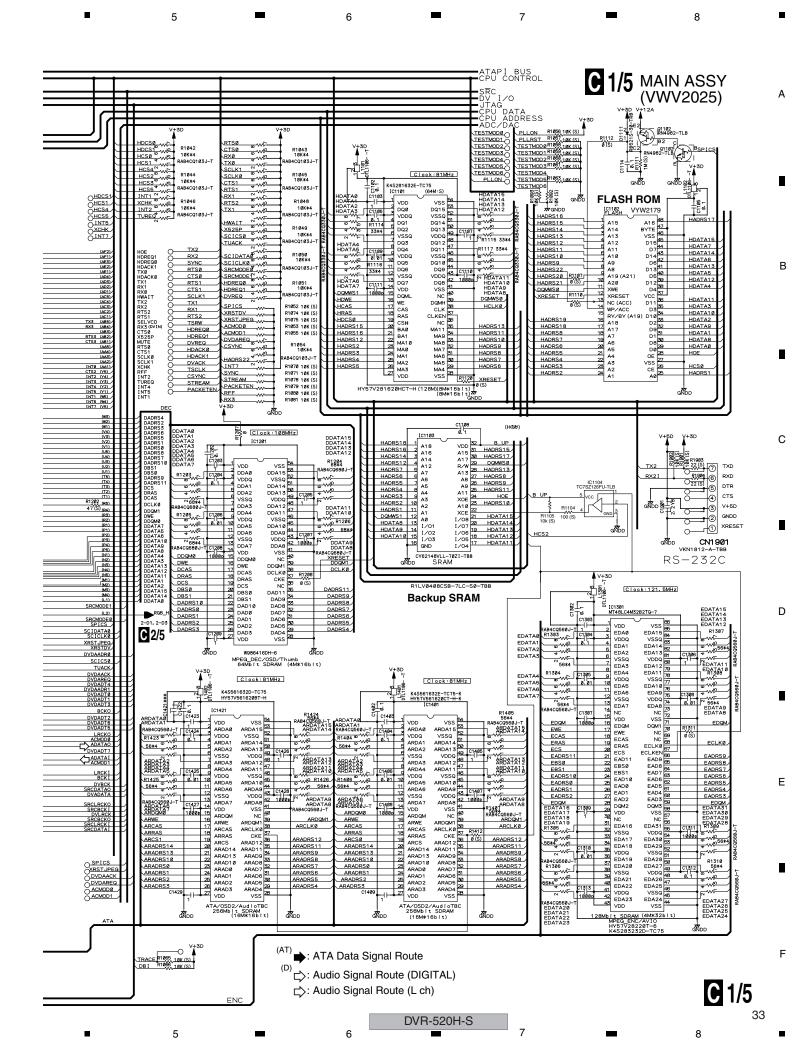






DVR-520H-S



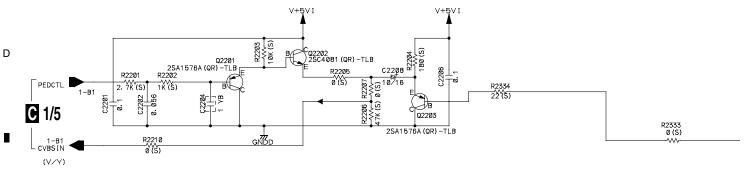


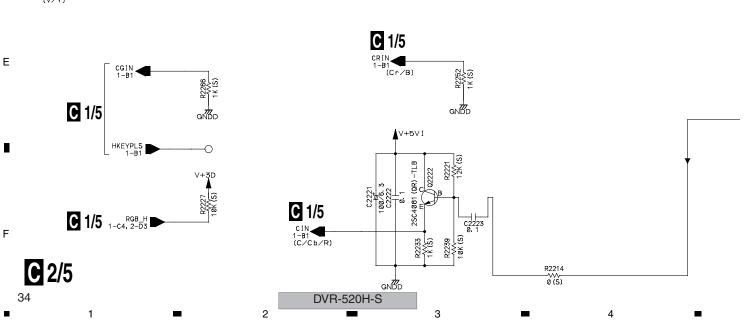
# 3.7 MAIN ASSY(2/5)

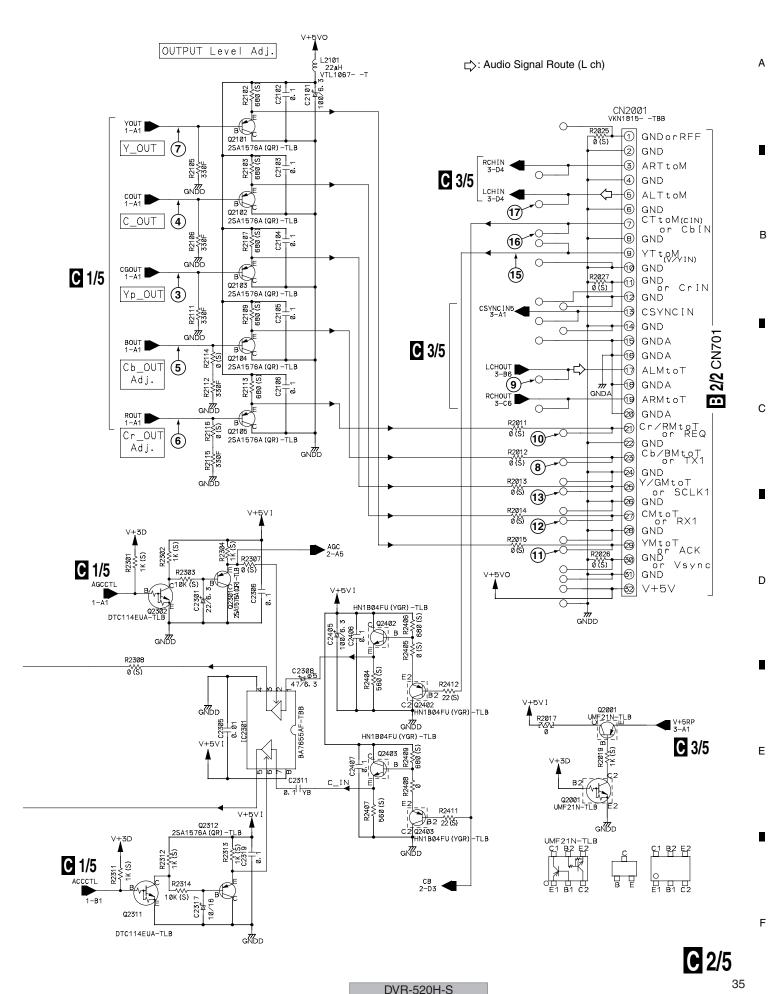
Α

В

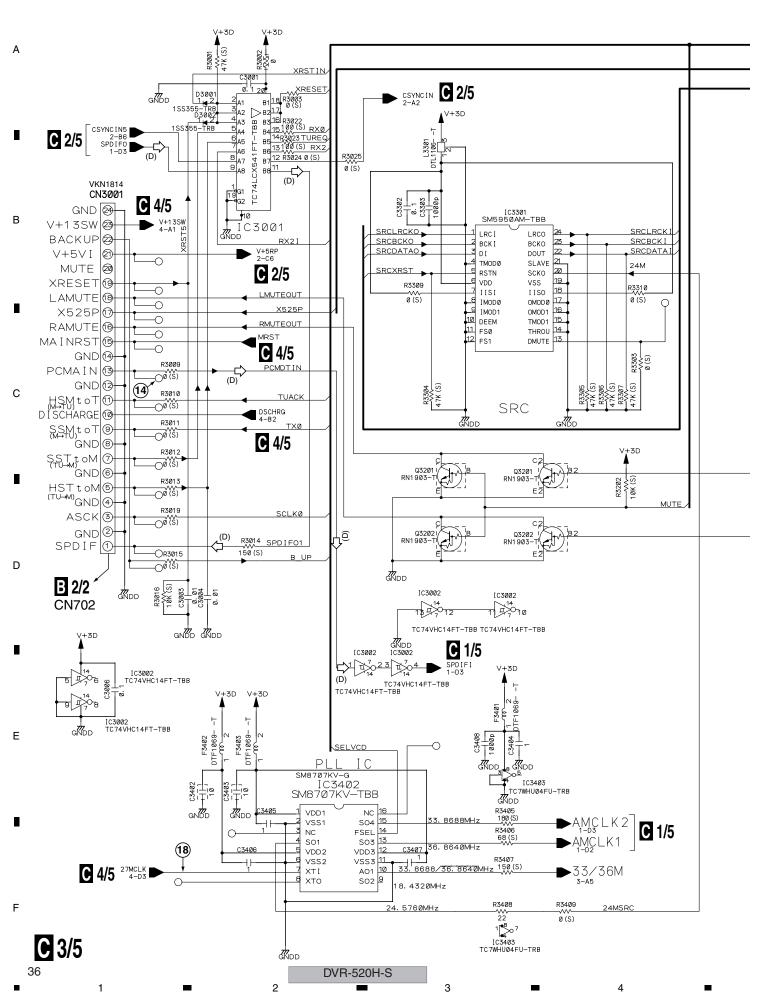
**C** 2/5 MAIN ASSY (VWV2025)

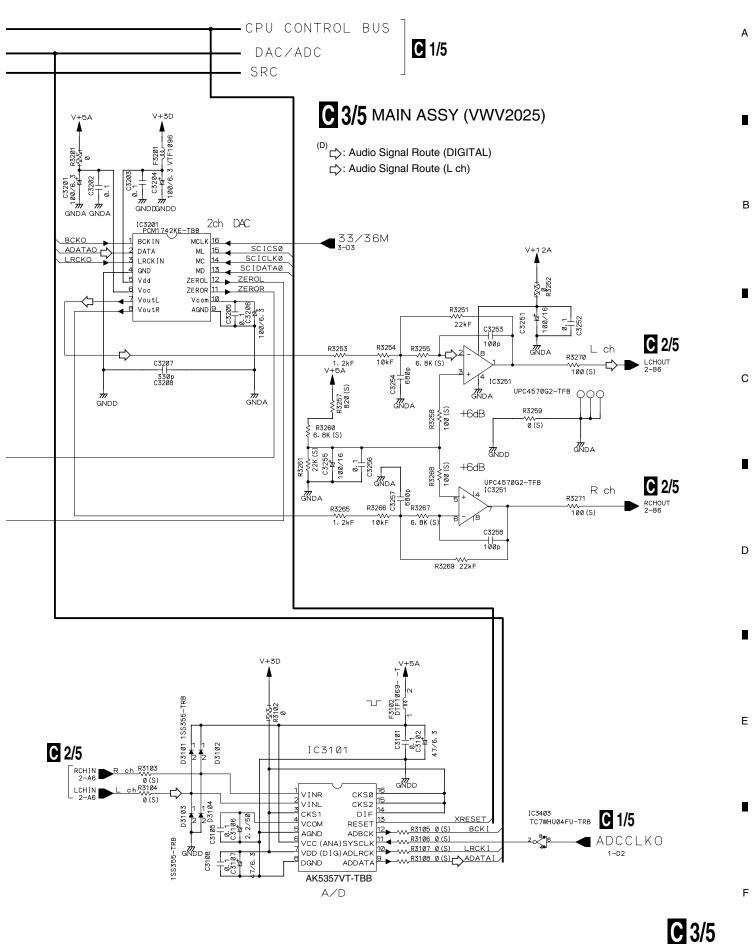






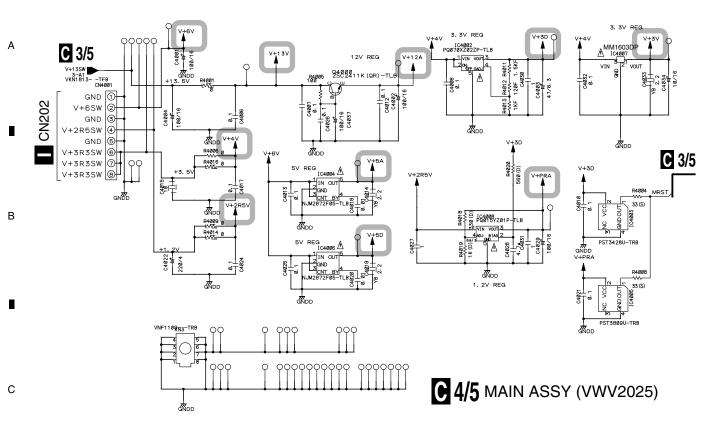
**D** VIII 02011

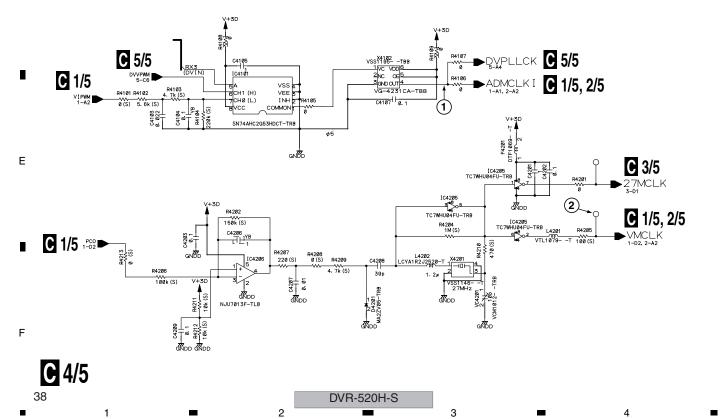


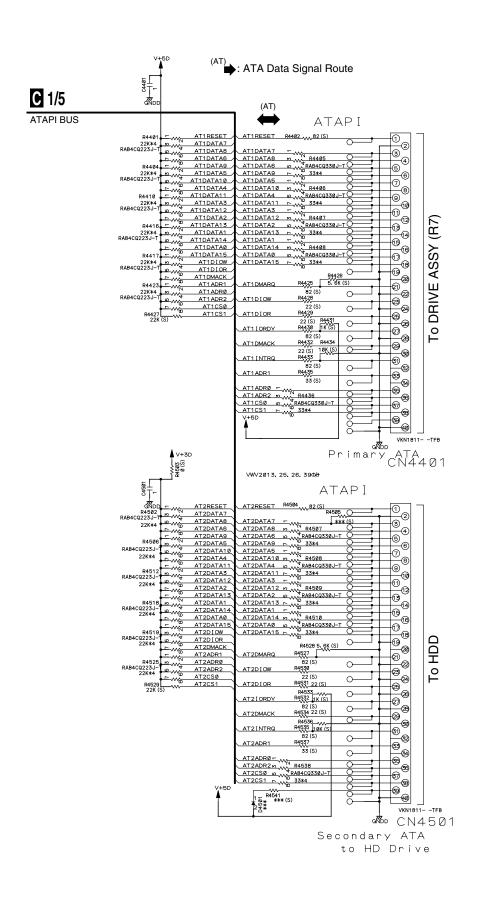


### 3.9 MAIN ASSY(4/5)

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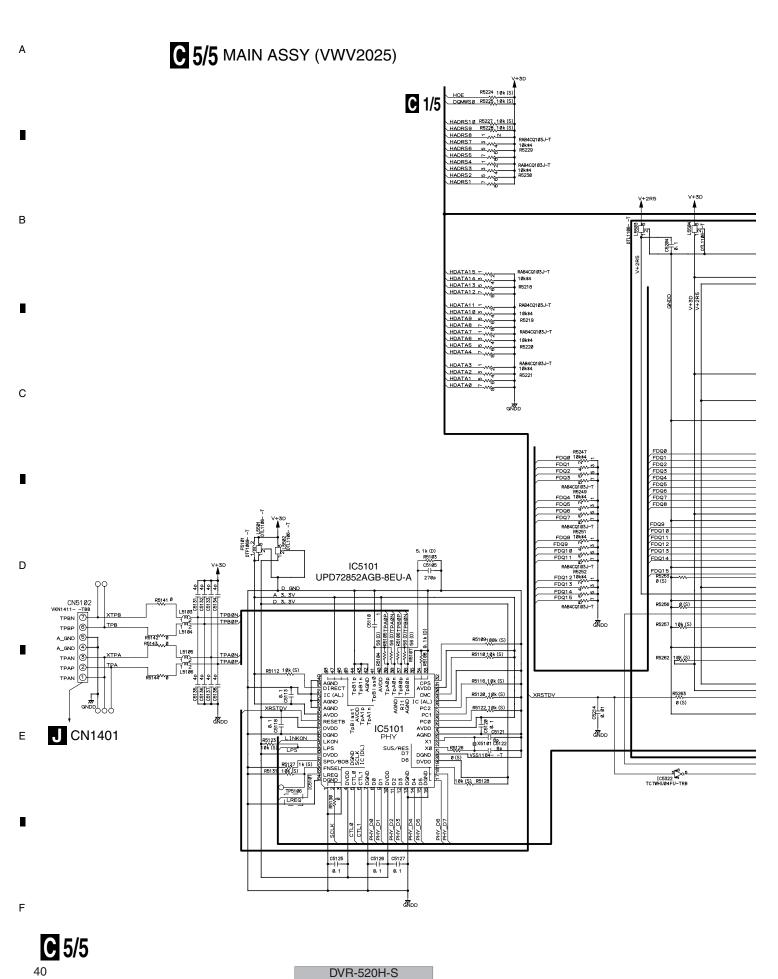
C 4/5 39

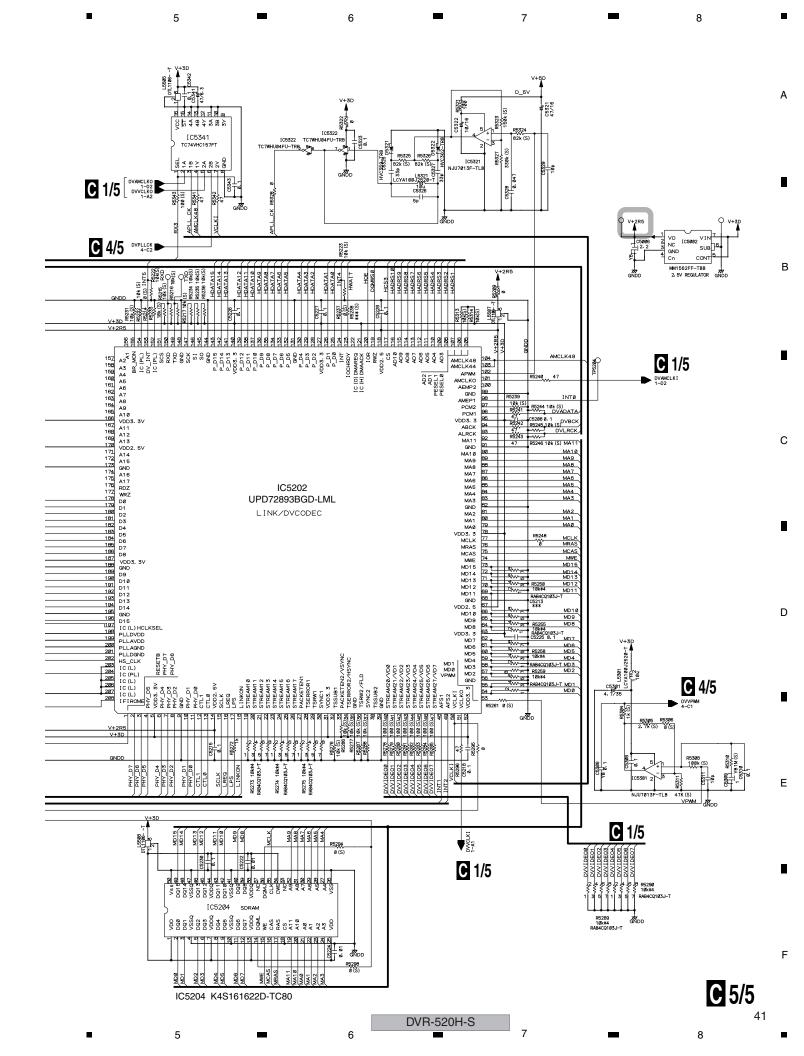
DVR-520H-S

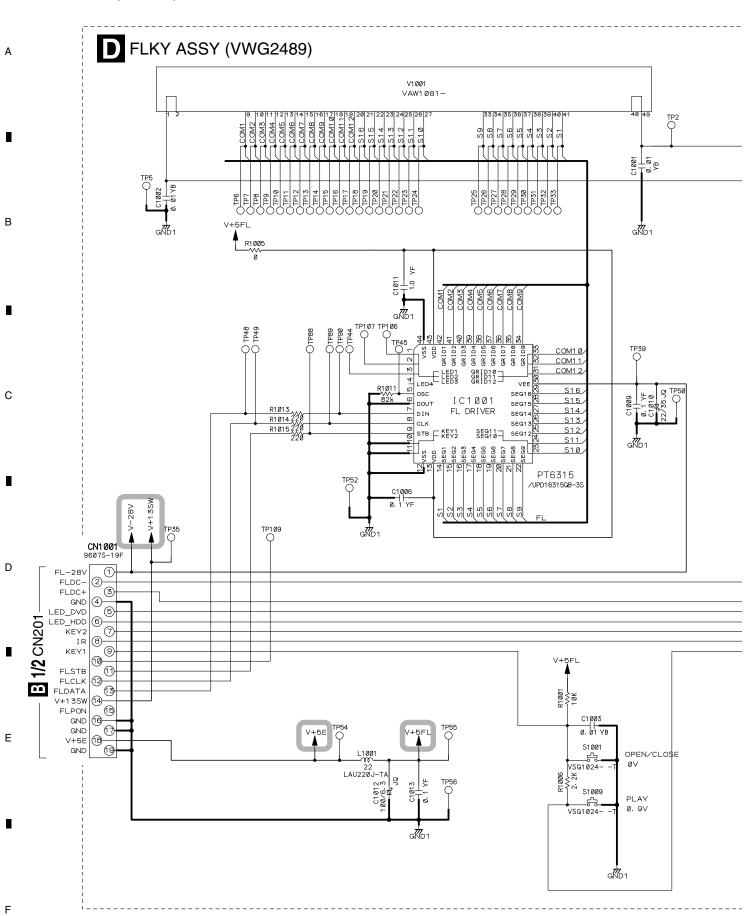
8

5

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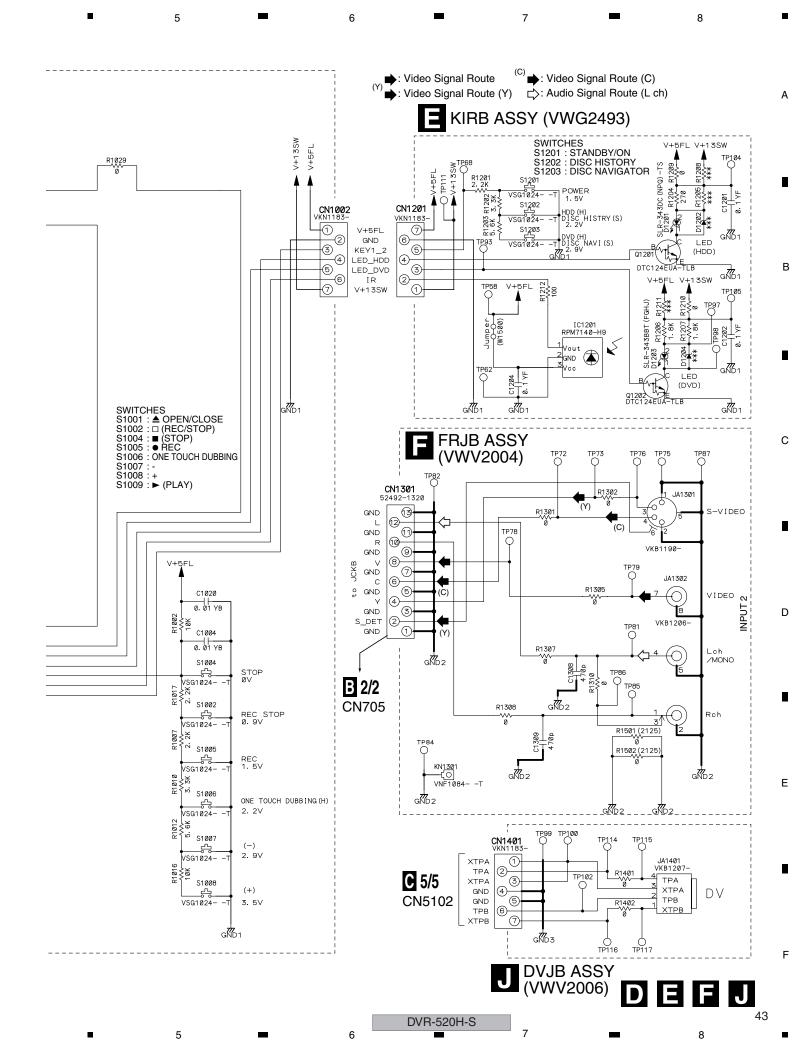






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D



Α

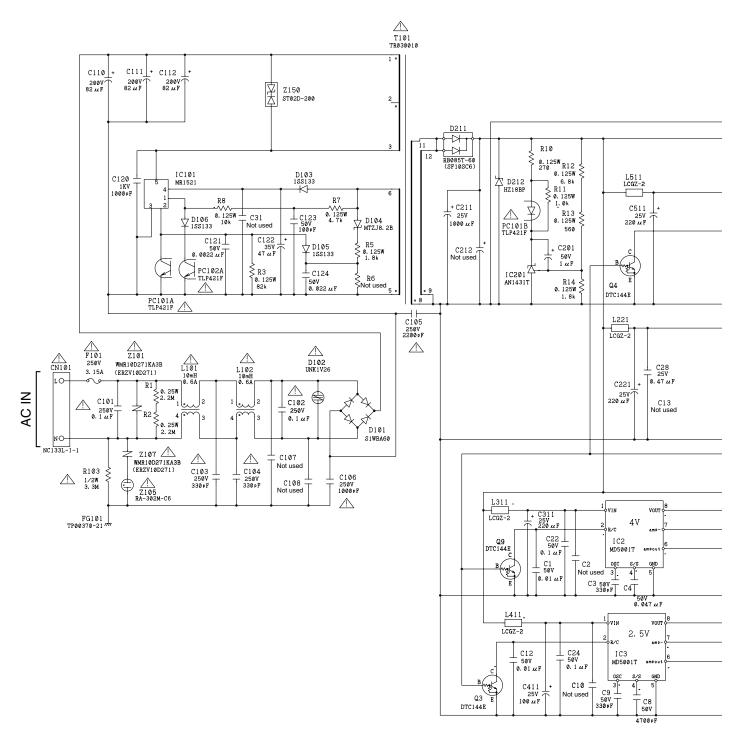
В

С

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Е

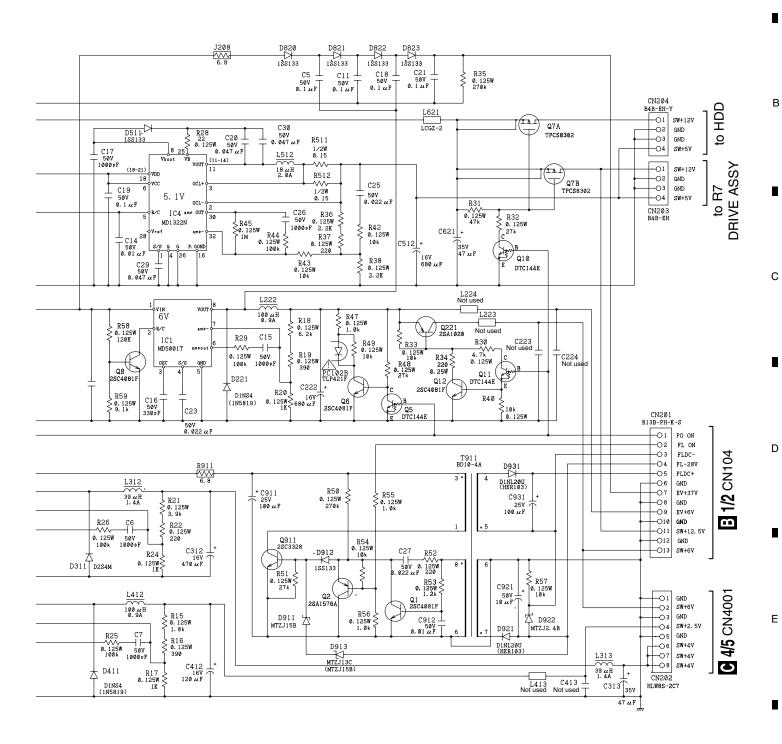
# POWER SUPPLY UNIT (VWR1380)



3

11

Note: No individual parts replacement for repair is accepted by Model Supplier due to the safety reasons. Replace whole ASSY.



### 3.13 WAVE FORMS

Note: The encircled numbers denote measuring point in the schematic diagram.

# B JCKB ASSY

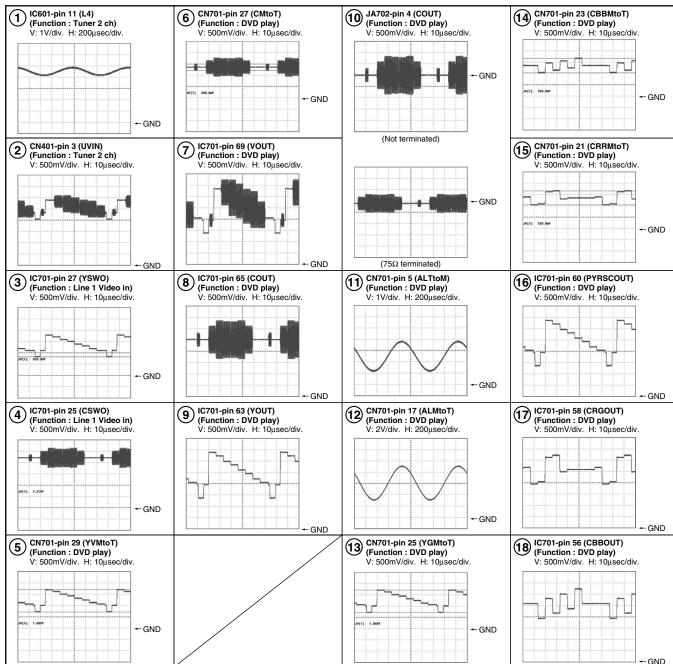
Α

В

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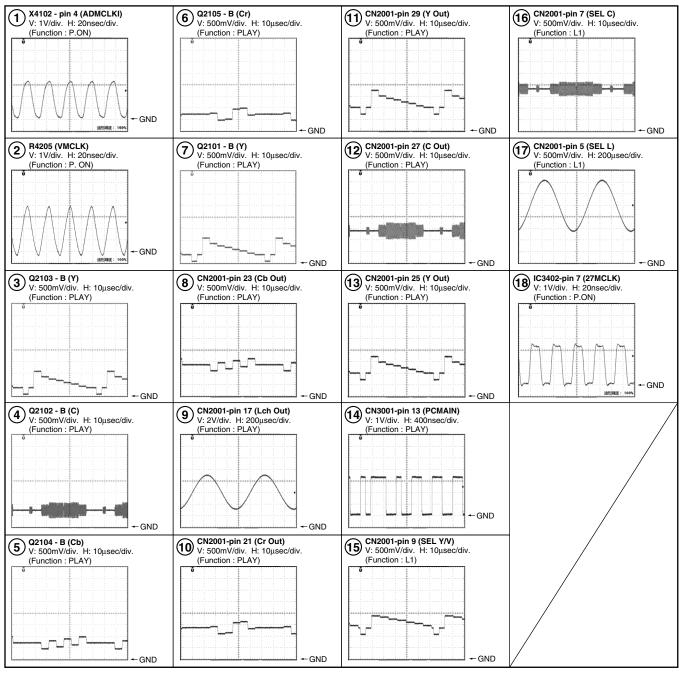
46

Measurement condition;

No.3 to No.8, No.10 to No.13 : 75% Color-bar, A1 disc 2-20 No.15 ,No.16 : 75% Color-bar

No.9, No.14 : 1kHz, 2Vrms, A1 disc 2-1

No.17 : 1kHz, 2Vrms



47

В

С

D

Ε

3 2 Α В С D Е DVR-520H-S 3

В

D

Ε

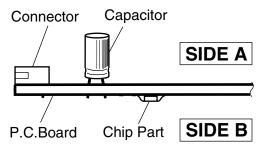
# 4. PCB CONNECTION DIAGRAM

### **NOTE FOR PCB DIAGRAMS:**

- Part numbers in PCB diagrams match those in the schematic diagrams.
- 2. A comparison between the main parts of PCB and schematic diagrams is shown below.

Symbol In PCB Diagrams	Symbol In Schematic Diagrams	Part Name
(0 0 0 B C E	B C E B C E	Transistor
• <u>000</u> BCE	E O	Transistor with resistor
(0 0 0) D G S		Field effect transistor
@00 <u>%</u> 000X	***************************************	Resistor array
000	<b>-</b>	3-terminal regulator

- 3. The parts mounted on this PCB include all necessary parts for several destinations.
  - For further information for respective destinations, be sure to check with the schematic diagram.
- 4. View point of PCB diagrams.

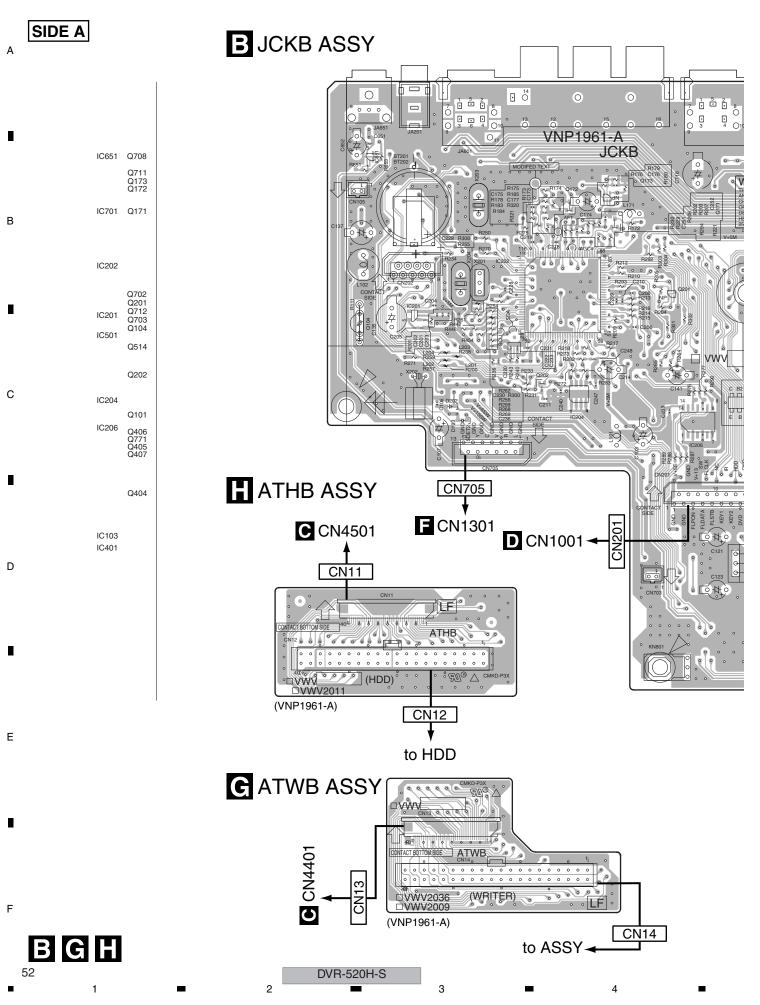


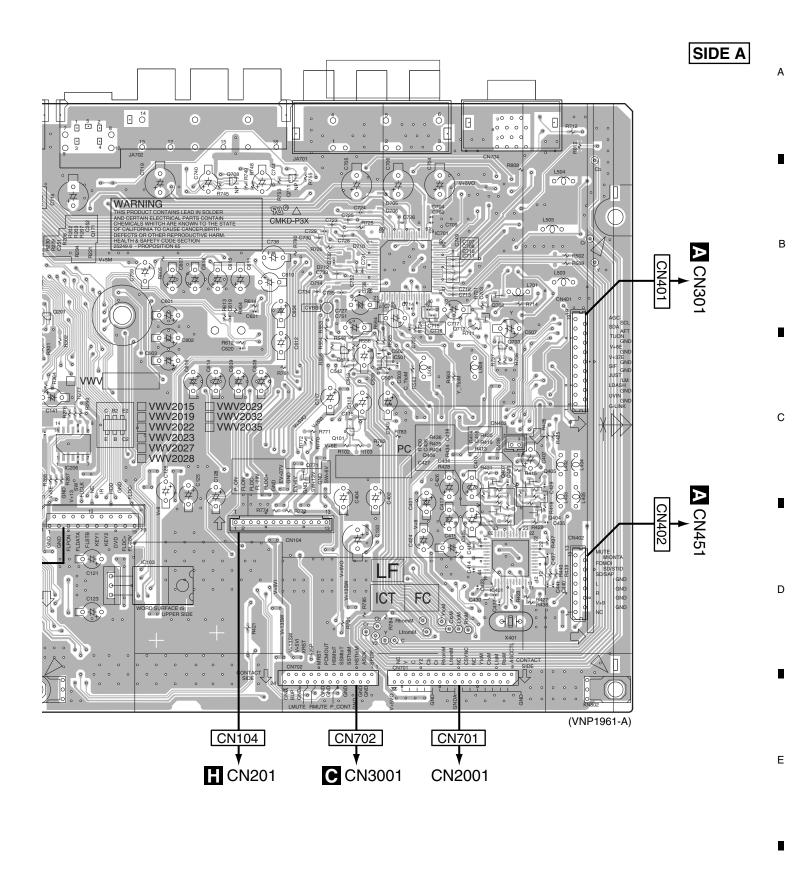
4.1 TUNB ASSY SIDE A SIDE A A TUNB ASSY Q306 Q301 Q302 Q304 Q451 (VNP1963-A) DVR-520H-S

SIDE B SIDE B A TUNB ASSY Q303 Q307 Q305 R306 AFT O MUTE. O A IC451 IC452 (VNP1963-A) DVR-520H-S

В

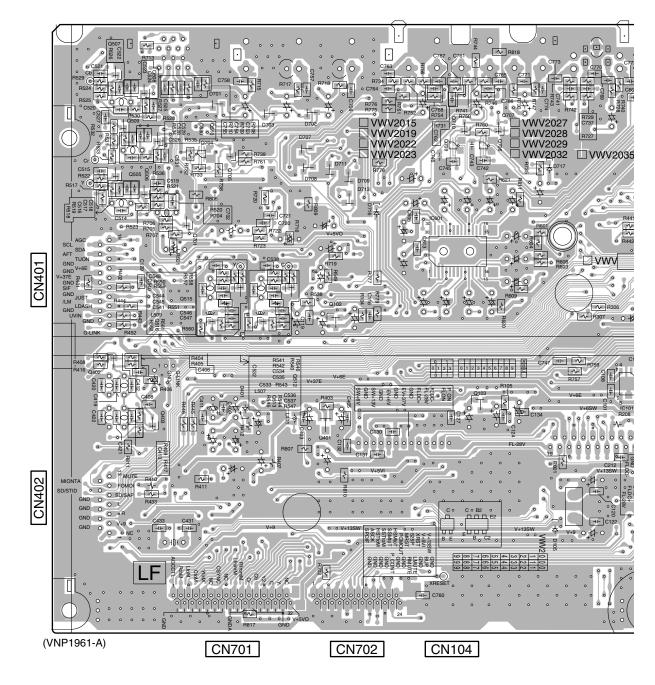
### 4.2 JCKB, ATWB and ATHB ASSYS



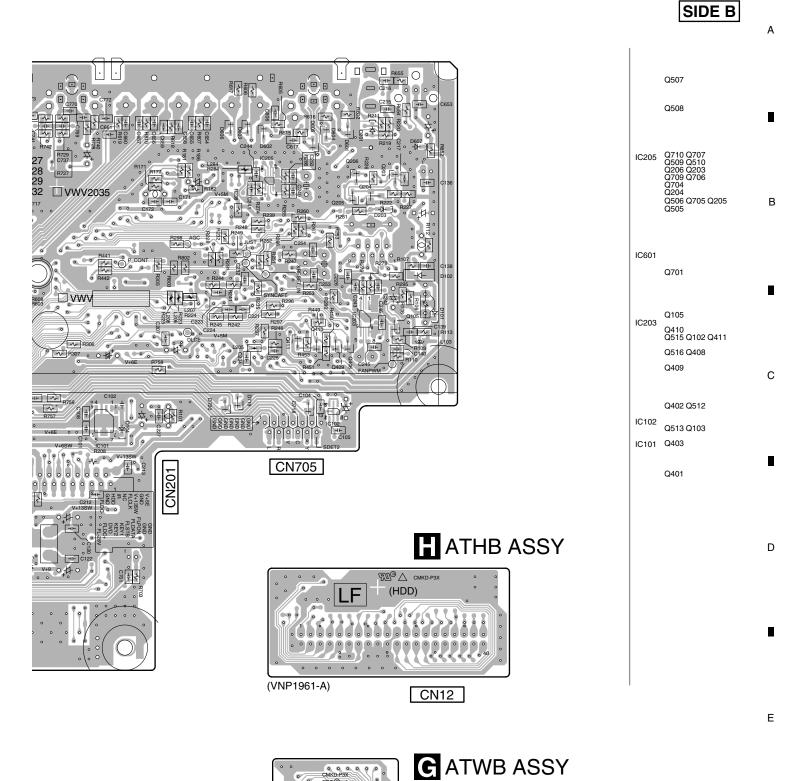


SIDE B

# **B** JCKB ASSY



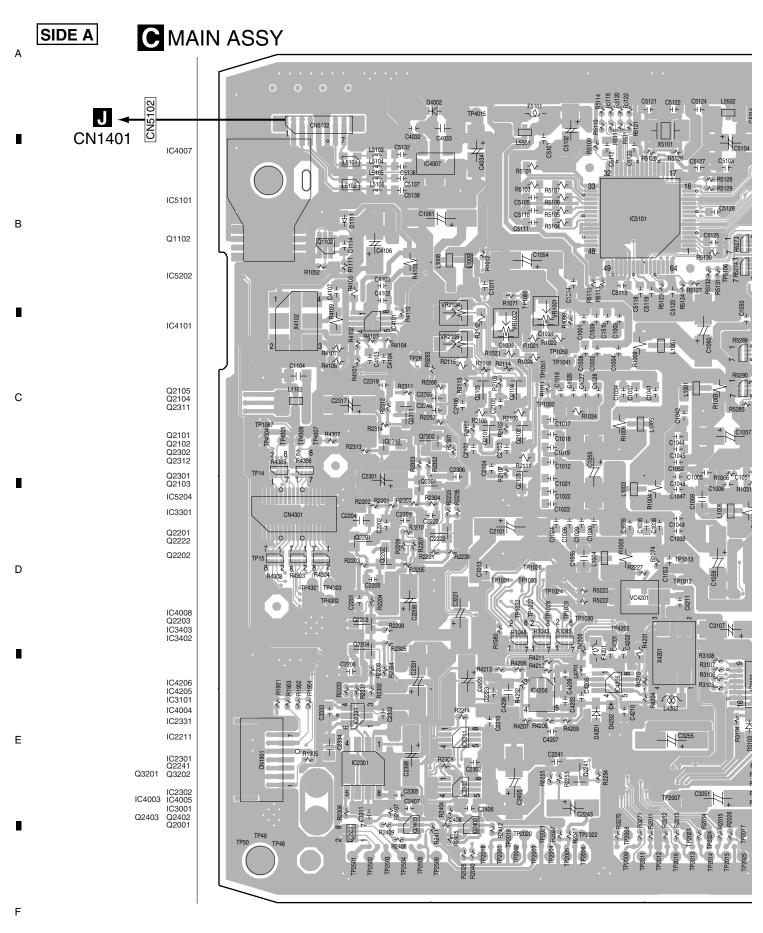
В



(VNP1961-A) CN14

BGH

4.3 MAIN ASSY

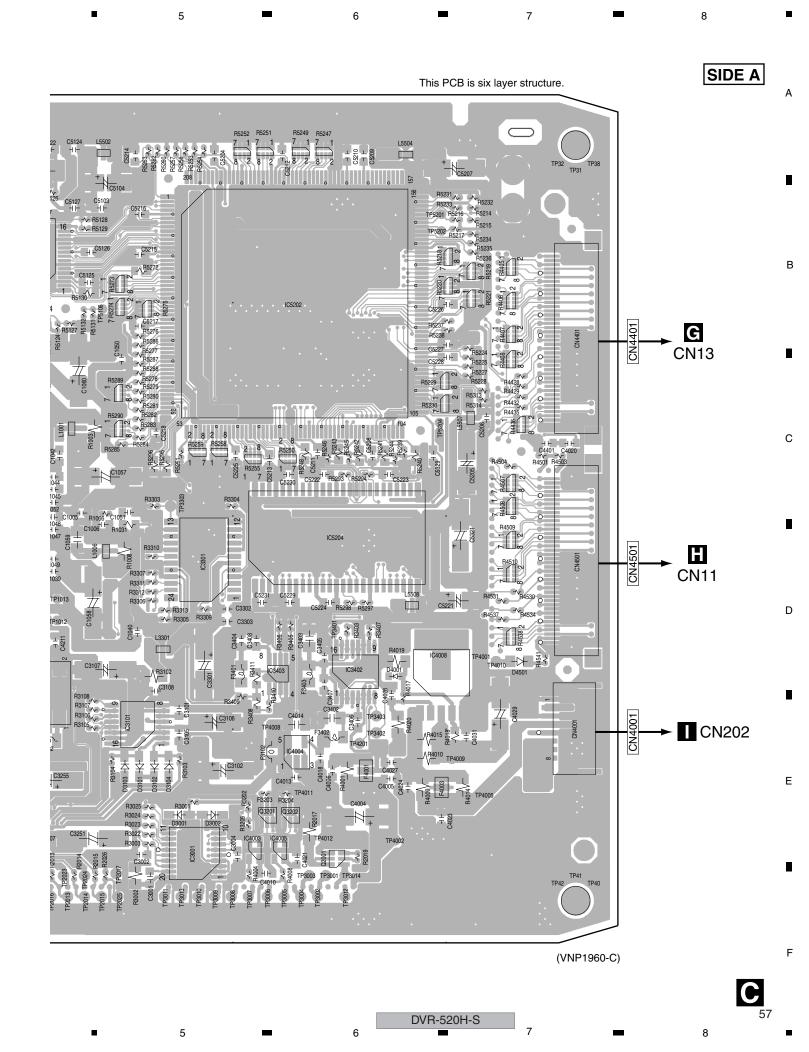


**C** 56

DVR-520H-S

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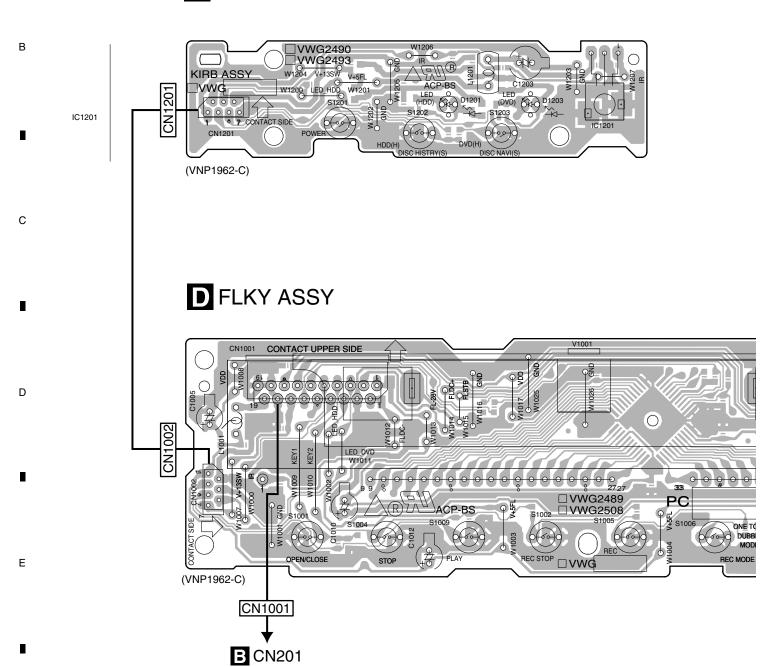


**C** MAIN ASSY SIDE B TP1 TP2 IC1103 IC1104 IC1301 TP4402 TP4401 → C1421 C1422 HH В C1424 IC5301 TP4416 ₹ TP4416 EXTENDED TP4418 EXTENDED TP4418 EXTENDED TP4421 EXTENDED TP4421 EXTENDED TP4422 EXTENDED EXTE IC5341 IC1421 -**Λ**-R1412 IC1102 R4416 TP4423 TP4424 TP4425 TP4428 TP4428 TP4429 TP4428 TP4429 TP4430 TP IC4006 C1403 IC5321 IC1001 C1405 HH C1407 IC5322 IC1401 IC5002 IC1101 L1401 + 8 27 TP4530 C1402 ⊣⊢ IC3201 TP3253 Q4000 IC4002 C4002 IC1201 C4007 IC2501 C4011 IC3251 C4030 IC3002 CN3001 **B** CN702 DVR-520H-S

В

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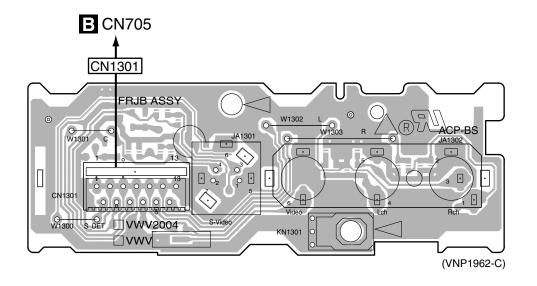
## **E** KIRB ASSY

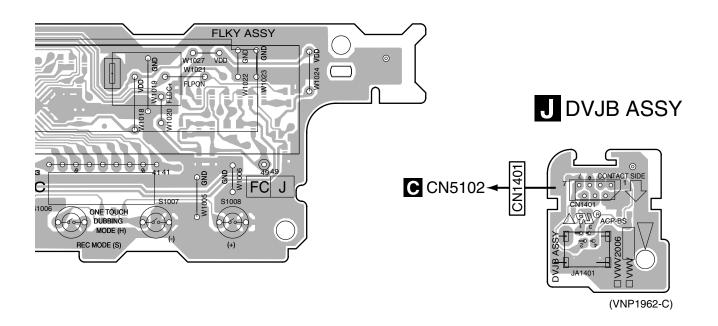


DE

SIDE A

# F FRJB ASSY





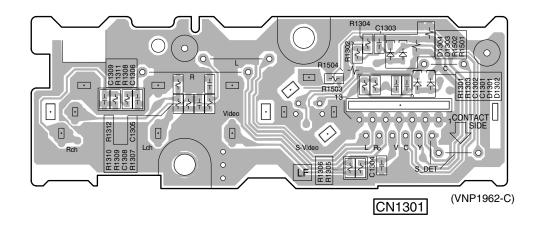
D F J

\_

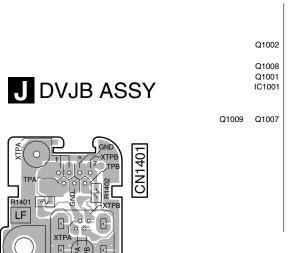
DVR-520H-S

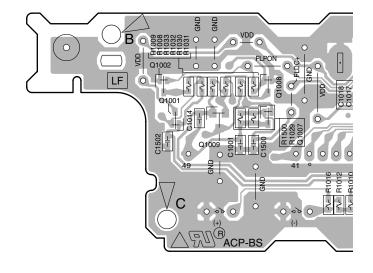
SIDE B

## FRJB ASSY



# **D** FLKY ASSY





DFJ

(VNP1962-C)

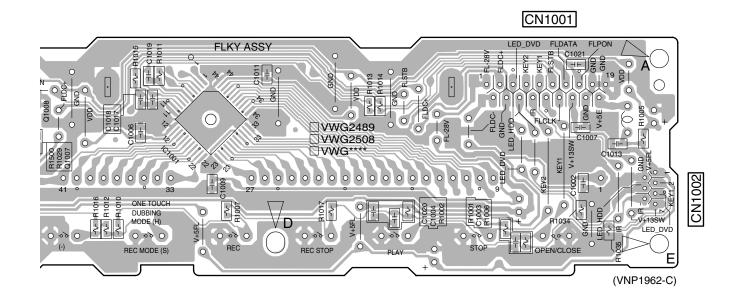
DVR-520H-S

-

SIDE B

# **E** KIRB ASSY

Q1201 Q1202 (VNP1962-C)



63

Е

4.5 POWER SUPPLY UNIT

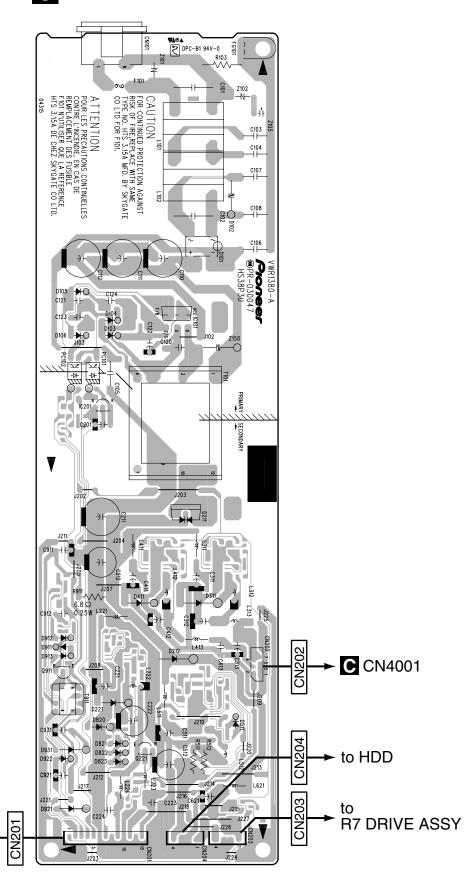
SIDE A

В

POWER SUPPLY UNIT

3

SIDE A



**B** CN104

DVR-520H-S

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64

Ε

В

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NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- ullet The  $\triangle$  mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples. Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

 $560 \Omega \rightarrow 56 \times 10^{1} \rightarrow 561 \dots RD1/4PU \boxed{561} J$  $\rightarrow 1R0 \qquad \qquad RSIP \square RO K$ 

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

 $5.62k \Omega \rightarrow 562 \times 10^{1} \rightarrow 5621 \cdots RN1/4PC \boxed{5621}F$ 

Mark No.	Description	Part No.	Mark No.	<b>Description</b>	Part No.	
NSP 1FLKB	ASSY	VWM2258	C455, C463, C	C464, C466, C468	VCG1039	
	Y ASSY	VWG2489	C477 (4.4uF)	, ,	VCG1039	
	B ASSY	VWG2403 VWG2493	C471 (10/50V)	)	VCH1224	
			C465 (3.3/50V		VCH1225	
	BASSY	VWV2004	C405 (3.3/30 V	)	VOI11223	
2DVJE	3 ASSY	VWV2006	RESISTORS			
NSP 1JACB	ASSY	VWM2265	R327		RS1/10S0R0J	
	B ASSY	VWV2009	R465		RS1/16S6802F	
	B ASSY	VWV2011	VR453 (2.2k)		VCP1123	
	B ASSY	VWV2011 VWV2023	VR451, VR452	2 (4 71)	VCP1154	
2JCK	D A331	V VV V 2023	Other Resistor	'	RS1/16S###J	
1TUNB	ASSY	VWV2010				
1MAIN	ASSY	VWV2025	<b>OTHERS</b>			
^	ER SUPPLY UNIT	VWR1380	CN451 13P S	OCKET	ALCDZ0Z0	
I OVVL	III OOI I LI OIVII	V VVI 11000			AKP7070	
			CN301 19P S U301 TV TUN		AKP7073 VXF1022	
Mark No.	Description	Part No.	0001 17 1010	ILITIAON	VALIOZZ	
A	•					
A TUNB			В јскв	ASSY		
<u>SEMICONDL</u>	<u>JCTORS</u>		SEMICONDU			
IC451		CXA2064M	IC204		BR24L32F-W	
Q303		2SA1576A	IC701		LA73033M	
Q301		2SC4081	IC601		LC75342M	
Q304, Q451		HN1C01FU	IC501			
Q302		RN4903			MM1504XN	
			<b></b> IC101		MM1565AF	
Q306		UMF21N	<b></b> IC103		NJM78M09FA	
D301		1SS355	IC202		PEG034B	
D302		UDZS33B	<b>∆</b> IC102		PQ1K333M2ZP	
			IC201		PST3245	
COILS AND	<b>FILTERS</b>		IC203		RS5C372A	
L302 INDUC	TOR	CTF1399	10200		1100007271	
L304, L305 C	CHIP BEADS	VTL1081	IC206		TC74HCT7007AF	
L303 RADIAL		VTL1165	IC200		TC7WU04FU	
2000 11/12//12	- 1110001011	V.2.100		2510 0701		
CAPACITOR			Q506, Q508, 0	2510, Q701	2SA1576A	
	<u>.5</u>	00000011001100	Q104		2SB1238X	
C460, C474		CCSRCH681J50	Q102, Q103		2SC2411K	
C301, C303, 0	C451, C467	CEAT100M50		_		
C319		CEAT101M10	Q105, Q172, (	Q205, Q702	2SC4081	
C453		CEAT101M16	Q204		2SD2114K	
C305		CEAT102M6R3	Q202, Q203, (	Q707, Q710	DTA124EUA	
			Q206, Q712		DTC124EUA	
C462		CEAT4R7M50	Q514 <sup>°</sup>		HN1B04FU	
C459, C473		CKSRYB103K50				
C469, C470		CKSRYB105K10	Q708, Q711		HN1C03FU	
C475		CKSRYB123K50	Q201		RN1901	
C311		CKSRYB222K50	Q703, Q706, (	7709	RN4903	
0011		ONOTH DEEENOU	Q103, Q706, Q	×100	UMF21N	
C450		CKCDVD070KE0				
C452		CKSRYB272K50	D102		1SR154-400	
C458, C472		CKSRYB472K50				
C457		CKSRYB473K25	D203		1SS352	
C476		CKSRYB562K50	D201, D716, D		1SS355	
C313, C454, 0	C461	CKSRYF104Z25	D601, D602, E	0604, D605	DF3A5.6FU	
			D718-D720		DF3A5.6FU	
			DVR-520H-S			65
	_		2111 02011 0		•	

<u>Mark No. Description</u>	Part No.	Mark No. Description	Part No.
D103, D202	RB501V-40	R173	RS1/16S3901F
•	-	R519, R526, R534	RS1/16S5600F
D607	UDZS5.1B	R602, R605, R606, R615-R617	RS1/16S75R0F
COIL S AND EU TERS		D70/ D715 D717 D710	DQ1/1607ED0E
COILS AND FILTERS	Laures :	R704, R715, R717, R718	RS1/16S75R0F
L503-L505	LAU100J	R728-R730, R740, R743, R747 R757-R759	RS1/16S75R0F
L701	LAU2R2J	Other Resistors	RS1/16S75R0F
L103	LCYA100J2520	Other nesistors	RS1/16S###J
L102	LTA102J	OTHERS	
L201-L208, L284 CHIP BEADS	VTL1081	X201 (10MHz)	ASS7034
CAPACITOPS		X201 (10MHz) X203 (4.433619MHz)	ASS7034 VSS1176
CAPACITORS	000001404 150	X203 (4.433619MHz) X202 (32kHz)	VSS1176 VSS1197
C211, C220, C225, C226, C543	CCSRCH101J50	CN202 (32KH2) CN202 9P FFC CONNECTOR	52045-0945
C619-C621	CCSRCH101J50	CN705 13P FFC CONNECTOR	52045-0945
C245	CCSRCH120J50	SINTOO TOLETTO OCIVINECTOR	0 <u>0</u> 070 1040
C516, C522, C528, C720, C721 C140	CCSRCH150J50	CN201 19P FFC CONNECTOR	52045-1945
0140	CCSRCH221J50	CN402 13P PLUG	AKP7059
C249, C250, C517, C523, C529	CC6BCH370 IE0	CN402 131 1 EOG CN401 19P PLUG	AKP7062
C249, C250, C517, C523, C529 C542	CCSRCH270J50 CCSRCH331J50	CN104 CONNECTOR 13P	B13B-PH-K
C542 C221, C232, C233	CCSRCH470J50	CN105, CN703 CONNECTOR 2P	B2B-PH-K
C177	CCSRCH470J50 CCSRCH561J50	225, 2 25 2525 1021	
C777	CCSRCH680J50	JA651 OPT.LINK OUT 8MB/S	JFJ1001
0102	00011011000000	JA201 JACK	RKN1004
C215	CCSRCH681J50	BT201 LITHIUM BATTERY	VEM1033
C174, C601-C603, C607-C611	CEAT100M50	(CR2032)	
C614-C616, C618	CEAT100M50	JA701 3P PIN JACK	VKB1150
C103, C107, C117, C118	CEAT100M30	JA601 JACK	VKB1183
C125, C126, C128, C141, C214	CEAT101M10		
0, 0 0, 0 ! - 1, 0 L ! -	JVIIIIIV	JA702 JACK	VKB1184
C507, C652, C717, C727	CEAT101M10	CN702 CONNECTOR	VKN1200
C121, C123, C133, C604, C612	CEAT101M16	CN701 32P FFC CONNECTOR	VKN1263
C748, C751	CEAT101M6R3	KN801, KN802	VNF1084
C704, C716, C719	CEAT102M6R3	WRAPPING TERMINAL	
C539	CEAT220M25		
C722	OF ATOMANA C		
C738	CEAT221M16	C MAIN ASSY	
C739	CEAT221M6R3	SEMICONDUCTORS	
C740, C744	CEAT470M16		Λ <i>V</i> Ε2Ε7\/Τ
C135	CEAT471M16	IC3101	AK5357VT
C705, C706	CEAT471M6R3	IC2301 IC1103	BA7655AF CY62148VLL-70ZI
C137	CEJQ101M16	IC5204	K4S161622D-TC80
C137 C102	CKSQYB225K10	IC5204 IC1101	K4S161622D-1C80 K4S281632E-TC75
C102 C129, C224, C606, C617, C747	CKSRYB103K50	101101	11-0201002E-10/0
C222, C223, C707, C708	CKSRYB104K16	IC1401, IC1421	K4S561632E-TC75
C711-C713, C718, C723-C725	CKSRYB104K16	IC1001	M65673WG-A
2. 1. 3. 13, 37 10, 3720 3723	OKOKI DIOTKIO	IC5002	MM1562FF
C728-C730, C732-C735, C737	CKSRYB104K16	⚠IC4007	MM1603DP
C741	CKSRYB104K16	IC1301	MT48LC4M32B2TG-
C502	CKSRYB105K10	· <del>- · ·</del>	
C241	CKSRYF103Z50	<b>⚠IC4004, IC4006</b>	NJM2872F05
C104-C106, C115, C116	CKSRYF104Z25	IC4206, IC5301, IC5321	NJU7013F
,,		IC3201	PCM1742KE
C119, C120, C122, C124, C127	CKSRYF104Z25	∴ IC4008	PQ015YZ01ZP
C130-C132, C134, C136, C139	CKSRYF104Z25	⚠ IC4002	PQ070XZ02ZP
C173, C175, C202-C204	CKSRYF104Z25	— <del>-</del>	
C216, C217, C228, C234	CKSRYF104Z25	IC4003	PST3428U
C239, C240, C244, C246, C501	CKSRYF104Z25	IC4005	PST3809U
. , , ,,		IC3301	SM5950AM
C519, C525, C531, C605, C613	CKSRYF104Z25	IC3402	SM8707KV
C653, C709, C710, C714, C715	CKSRYF104Z25	IC4101	SN74AHC2G53HDC
C726, C731, C736, C749, C750	CKSRYF104Z25		
C752, C760	CKSRYF104Z25	IC3001	TC74LCX541FT
C101, C209, C210, C212, C213	CKSRYF105Z10	IC3002	TC74VHC14FT
		IC5341	TC74VHC157FT
C218, C219, C227, C231, C236	CKSRYF105Z10	IC1104	TC7SZ126FU
DESISTORS		IC3403, IC4205, IC5322	TC7WHU04FU
RESISTORS R208	RS1/10S0R0J	IC3251	UPC4570G2
R178	RS1/16S1501F	IC5101	UPD72852AGB-8EU
11170	1101/1001001F		
		520H-S	

	5	6	_		7	-	8	
Mark No.	<b>Description</b>	Part No.		Mark No.		<u>Description</u>	Part No.	
IC5202	•	UPD72893BGD-LM	11	C1206 C1	305 C13	308, C1310, C1406	CKSRYB103K50	
IC1102		VYW2176				003, C3004, C4018	CKSRYB103K50	
IC1201		W986416DH-6				214, C5222, C5224	CKSRYB103K50	
101201		VV000+10D110				311, C4104, C5306	CKSRYB104K16	Α
O2101-O210	05, Q2201, Q2203, Q2301	2SA1576A		C4103	010, 020	711, 01101, 00000	CKSRYB223K50	^
Q2312	00, 02201, 02200, 02001	2SA1576A		04100			ONOTH BEEDING	
Q4000		2SC2411K		C5328			CKSRYB473K25	
Q2202, Q222	22	2SC4081		C2202			CKSRYB563K16	
Q2302, Q231		DTC114EUA			005 C10	007-C1010, C1016Cl		
Q2002, Q20		DTOTT-LON				021-C1023	CKSRYF104Z25	
Q2402, Q240	03	HN1B04FU		,		028, C1030, C1035		
Q3201, Q320		RN1903		01020, 01	020, 010	520, 01000, 01000	011011111101220	
Q1102	<u> </u>	RN4982		C1038 C1	041 C10	042, C1047, C1102	CKSRYF104Z25	
Q2001		UMF21N				108, C1114, C1202		
	02, D3101-D3104	1SS355				304, C1312, C1402	CKSRYF104Z25	
20001, 2000	2, 20101 20101	100000				124, C2102-C2106	CKSRYF104Z25	
D5321		HVC359				222, C2223, C2306	CKSRYF104Z25	В
D5322		HVC362		OLLO1, OL	.200, 022	LL, OLLLO, OLOGO	OROTTI 104220	J
D4201		MA2ZV05		C2310 C2	406 C2/	407, C3006, C3101	CKSRYF104Z25	
D1111		RB521S-30				202, C3203, C3205	CKSRYF104Z25	
DIIII		1100210-00				302, C4006-C4008	CKSRYF104Z25	
COIL C AND	FILTEDO					013, C4017, C4021	CKSRYF104Z25	
COILS AND		DTT / 0.00				32, C4202, C4203	CKSRYF104Z25	
·	1-F3403, F4201, F5101	DTF1069		C4024-C40	026, 040	132, C4202, C4203	CK5K1F104Z25	
	9, L1101, L1102, L1301	DTL1106		C4000 CE	110 051	110 CE100	CKCDVE104705	
· ·	2, L3301, L5501-L5505	DTL1106				118, C5120	CKSRYF104Z25	
·	8 EMI FILTER	DTL1106				04, C5215, C5218	CKSRYF104Z25	
L5301, L532	1	LCYA100J2520				30, C5308, C5310	CKSRYF104Z25	
				C5323, C5			CKSRYF104Z25	
L4202		LCYA1R2J2520		C1001, C1	006, C10	048, C1103, C1107	CKSRYF105Z10	
F3201		VTF1096		0			01/05/25/05	С
L2101		VTL1067				209, C1303, C1306	CKSRYF105Z10	
L4201		VTL1079				405, C1409, C1423	CKSRYF105Z10	
L5103-L5106	3	VTL1082		,	,	001, C3404-C3407	CKSRYF105Z10	
						105, C4107, C4201	CKSRYF105Z10	
CAPACITOR	RS			C4401, C4	501, C51	110	CKSRYF105Z10	
C5311, C532	29	CCSRCH100D50						
C3253, C325	58	CCSRCH101J50		C4028 (4.7	,		DCH1200	<del>-</del>
C5105		CCSRCH271J50				015 (10/10V)	VCG1045	
C5325, C532	27	CCSRCH330J50		C1060 (150	,		VCH1246	
C3207		CCSRCH331J50		VC4201 (1	0pF)		VCM1012	
C4208		CCSRCH390J50		RESISTOR	RS			_
C5131-C513	8	CCSRCH4R0C50		R1025, R1	026, R10	042-R1046	RAB4CQ103J	D
C5326		CCSRCH5R0C50		R1048-R10	051, R10	54, R1068, R1069	RAB4CQ103J	
C3254, C325	57	CCSRCH681J50		R1072, R1	073, R52	218-R5221	RAB4CQ103J	
C5121, C512		CCSRCH8R0D50		R5229, R5	230, R52	247, R5249-R5252	RAB4CQ103J	
,				R5255, R5	258, R52	259, R5273-R5275	RAB4CQ103J	
C2208, C231	17, C4034, C5322	CEVW100M16						
·	55, C4001, C4002, C4004	CEVW101M16		R5289, R5	290		RAB4CQ103J	
C4029, C403		CEVW101M16		R1408-R14			RAB4CQ220J	
C2101, C222	21, C2405, C3201, C3204C	EVW101M6R3		R4401, R4	404, R44	410, R4416, R4417	RAB4CQ223J	
C3206		CEVW101M6R3		R4423, R4	502, R45	506, R4512	RAB4CQ223J	
				R4518, R4	519, R45	525	RAB4CQ223J	
C2301		CEVW220M6R3						
	61, C3301, C4022	CEVW221M4		R1114-R11	117, R44	05-R4408, R4436	RAB4CQ330J	Е
C3106	, ,	CEVW2R2M50		R4507-R45	510, R45	38	RAB4CQ330J	
C5321		CEVW470M16		R1303-R13	310, R14	04-R1407	RAB4CQ560J	
C2308, C310	02, C3107, C4003, C5341	CEVW470M6R3		R1423-R14	426		RAB4CQ560J	
	, , ,			R1203-R12	206		RAB4CQ680J	
C5301		CEVW4R7M35						
	32, C1503-C1506	CKSQYB104K16		R5103			RN1/16SE5101D	
C2204, C420	·	CKSQYB105K10		R5108			RN1/16SE9101D	
·	14, C4019, C4033	CKSQYB225K10		R1207, R1	502, R15	507, R2017, R3002	RS1/10S0R0J	
· ·	12, C1014, C1029, C1034	CKSRYB102K50		R3102, R3	201, R32	252, R4001, R4006	RS1/10S0R0J	
,	. ,			R4009, R4	014, R40	016, R4108, R4109	RS1/10S0R0J	
C1040. C104	14, C1049, C1051	CKSRYB102K50						
·	11, C1207, C1208, C1307	CKSRYB102K50		R5209, R5	322		RS1/10S0R0J	
	13, C1407, C1408	CKSRYB102K50		R4019			RS1/10S1001D	F
,	28, C3303, C3408	CKSRYB102K50		R5321			RS1/10S101J	r
· ·	27, C1037, C1052, C1109	CKSRYB103K50		R1501			RS1/10S2202F	
000, 0 102	, 1.11., 0.002, 0.1100	3.12.1.2.30.100		R4020			RS1/10S5600D	
			DVR-52	2-H0				67
	_		DVN-32	.011-0	7		_	

DVR-520H-S 7

	1	-	2	<b>3</b>
	Mark No.	Description	Part No.	Mark No. Description
Α	R4013 R3254, R3266 R4012 R3253, R3265 R4011	·	RS1/16S1001F RS1/16S1002F RS1/16S1200F RS1/16S1201F RS1/16S1501F	RESISTORS R1210 R1206, R1207 R1204 Other Resistors
	R1021, R1023 R3251, R3269 R2105, R2106, F R4018 R5104-R5107	R2111, R2112, R2115	RS1/16S2201F RS1/16S2202F RS1/16S3300F RS1/16S3600D RS1/16S56R0D	OTHERS  IC1201 REMOTE RECEIVER UNIT CN1201 CONNECTOR
	Other Resistors		RS1/16S###J	FRJB ASSY CAPACITORS
	OTHERS			C1308, C1309
В	CN5102 7P FFC CN4401, CN450 CN1901 7P FFC CN4001 8P FFC	1 40P FFC CONNECTOR C CONNECTOR	VKN1411 VKN1811 VKN1812 VKN1813	RESISTORS R1501, R1502 Other Resistors

	CN1901 7P FFC CONNECTOR	VKN1812
	CN4001 8P FFC CONNECTOR	VKN1813
	CN3001 24P FFC CONNECTOR	VKN1814
	CN2001 32P FFC CONNECTOR	VKN1815
	KN3 EARTH METAL FITTING	VNF1109
	X4201 (27.000MHz)	VSS1146
	X5101 (24.576MHz)	VSS1184
	X4102 VCXO(27MHz)	VSS1195
_		

### **FLKY ASSY SEMICONDUCTORS**

IC1001 PT6315

### **COILS AND FILTERS**

L1001 LAU220J

### **SWITCHES AND RELAYS**

S1001, S1002, S1004-S1009 VSG1024

### **CAPACITORS**

C1012 CEJQ101M6R3 C1010 CEJQ220M35 C1001-C1004, C1020 CKSRYB103K50 C1006, C1009, C1013 CKSRYF104Z25 C1011 CKSRYF105Z10

### **RESISTORS**

Other Resistors RS1/16S###J

#### **OTHERS**

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CN1001 19P FFC CONNECTOR 9607S-19F V1001 FLTUBE VAW1081 CN1002 CONNECTOR VKN1183 VNF1129 2 FL HOLDER

### **KIRB ASSY SEMICONDUCTORS**

Q1201, Q1202 DTC124EUA D1203 SLR-343BBT D1201 SLR-343DC

### **SWITCHES AND RELAYS**

S1201-S1203 VSG1024

### **CAPACITORS**

CKSRYF104Z25 C1201, C1202, C1204

Part No.

4

RS1/10S0R0J RS1/10S182J RS1/10S271J RS1/16S###J

RPM7140-H9 VKN1183

CCSRCH471J50

RS1/10S0R0J RS1/16S###J

**OTHERS** 

CN1301 13P FFC CONNECTOR 52492-1320 JA1301 YC CONNECTOR(VERTI) VKB1190 JA1302 3P JACK(VERTICAL) VKB1206 KN301 WRAPPING TERMINAL VNF1084

## ATWB ASSY

CN14 40P ATA CONNECTOR VKN1816 CN13 40P FFC CONECTOR VKN1879

## ATHB ASSY

CN12 40P ATA CONNECTOR VKN1816 CN11 40P FFC CONNECTOR VKN1879

### **POWER SUPPLY UNIT**

POWER SUPPLY UNIT has no service part.

### **DVJB ASSY RESISTORS**

Other Resistors RS1/16S###J

**OTHERS** 

JA1401 DV TERMINAL VKB1207 CN1401 CONNECTOR VKN1183

### 6. ADJUSTMENT

### **6.1 TUNB ASSY ADJUSTMENT**

\* It is not necessary to adjust the ASSY normaly when exchanging the ASSY. But the adjustment is necessary when exchanging the Tuner Module and IC451 stereo decoder IC.

No.	Adjustment Name	Adj. Point	Measurement Point	Adjustment Value	Adjustment State
1	Stereo Decoder ATT adjustment (Input system adjustment)	VR453	Audio ouput (L) (Rear panel)	370mVrms ± 18.5mV	Input a signal of Mono 1kHz/100% modulation to terrestrial tuner input. /through output.
2	Stereo Decoder Wideband adjustment (Input system adjustment)	VR451	Audio ouputs (L/R) (Rear panel)		Input a signal of Stereo 300Hz/30% modulation (NR-ON/L ch only) to terrestrial tuner input. Note 2
3	Stereo Decoder Spectral adjustment (Input system adjustment).	VR452	Audio ouputs (L/R) (Rear panel)	ibesi bomi oi sebaranon	Input a signal of Stereo 3kHz/30% modulation (NR-ON) to terrestrial wave input. /through output Note 2

Note 1: The values for channel separation is defined as those having passed through the following filters:

100Hz - 10kHz : +0/-0.5dB 15.75kHz - 100kHz : -40dB or more

Note 2: The adjustment No.2 and No.3 should be repeated 2 times for good adjustment.

 $(Steps: No.1 \rightarrow No.2 \rightarrow No.3 \rightarrow No.2 \rightarrow No.3)$ 

A TUNB ASSY



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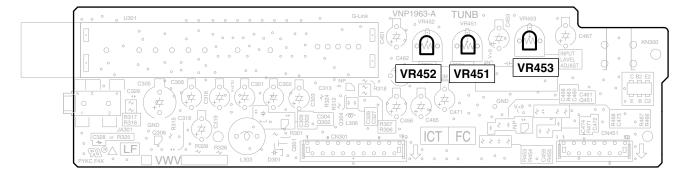


Fig.1 Adjustment Points (TUNB ASSY)

# 6.2 MAIN ASSY ADJUSTMENT

\* It is not necessary to adjust the ASSY normaly when exchanging the ASSY, but confirm the data.

No	Adjustment Name	Adj. Point	Measurement Point	Adjustment Value	Adjustment State
1	Master clock free-running adjustment (Clock system adjustment)	VC4201	MAIN ASSY IC3402 Pin8 (XTO) (SM8707KV)	27.000000MHZ ± 130Hz	No signal input

C MAIN ASSY SIDE A IC3402 Pin8

70

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### 7. GENERAL INFORMATION

### 7.1 DIAGNOSIS

### Service Diagnosis List

#### 7.1.1 CPRM ID NUMBER AND DATA SETTING

The Setting is necessary

- " CPRM ERR" is displayed on the FL display immediately after the power is turned on or in Stop mode.
- When the MAIN ASSY, DRIVE ASSY or the FLASH ROM is exchanged.

#### 7.1.2 MODEL SETTING

#### 7.1.3 DOWNLOAD METHOD

The Setting is necessary

- When the MAIN ASSY is replaced.
- When the JCKB ASSY is replaced.
- When the MAIN ASSY and JCKB ASSY is replaced.

#### 7.1.4 SERVICE MODE

[First Screeen] (Version information, etc)

[Sub Screen 1] (Result of error-rate measurement : Video mode/VR mode)

[Sub Screen 2] (HDD information)

#### [Second Screen] (ATA/ATAPI debug screen)

[Sub Screen 3] (writer maintenance information of ATA/ATAPI DEBUG OSD)

[Sub Screen 4] (ATA/ATAPI DEBUG OSD\_LD degradation judgement)

#### [Fourth Screen] (VR-recording error log)

[Sub Screen 4] (Error log for VR recording)

[Fifth Screen] (Error log for VR playback)

[Sub Screen 2] (Error log for VR playback)

#### 7.1.5 DV DEBUG MODE

[Third Screeen] (DV debug information)

#### 7.1.6 ERROR RATE MEASUREMENT

Only Video mode measurement

### 7.1.7 VIDEO ADJUSTMENT FOR SPECIFIC AREA

#### **Purposes:**

Depending on the area, jitter may appear in a picture received by the tuner, as conditions of signals received by the tuner are different from area to area. To correct this kind of problem, the function of the System Codec AVIO control section for adjusting signals received by the tuner can be used.

### 7.1.8 AGING MODE

71

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### 7.1.1 CPRM ID NUMBER AND DATA SETTING

### Entering the ID Number and ID Data for DVD Recorder

For the DVD recorder, it is necessary with the recoding/playback of DVD–RW disc to set an individual number (ID number) and ID data to each recorder. If the number and data are not set correctly with the following procedure, operations in the future may not be guaranteed. You will find the ID number to be set on the ID label on the rear panel.

3

**Important:** If no ID label is found on the rear panel, write down the specified ID number by checking it according to "How to confirm the ID number" shown below.

### The Input is Necessary When:

- " CPRM ERR" is displayed on the FL display immediately after the power is turned on or in Stop mode.
- When the MAIN ASSY, DRIVE ASSY or the HDD is exchanged.

### **JIGS AND MEASURING INSTRUMENTS**

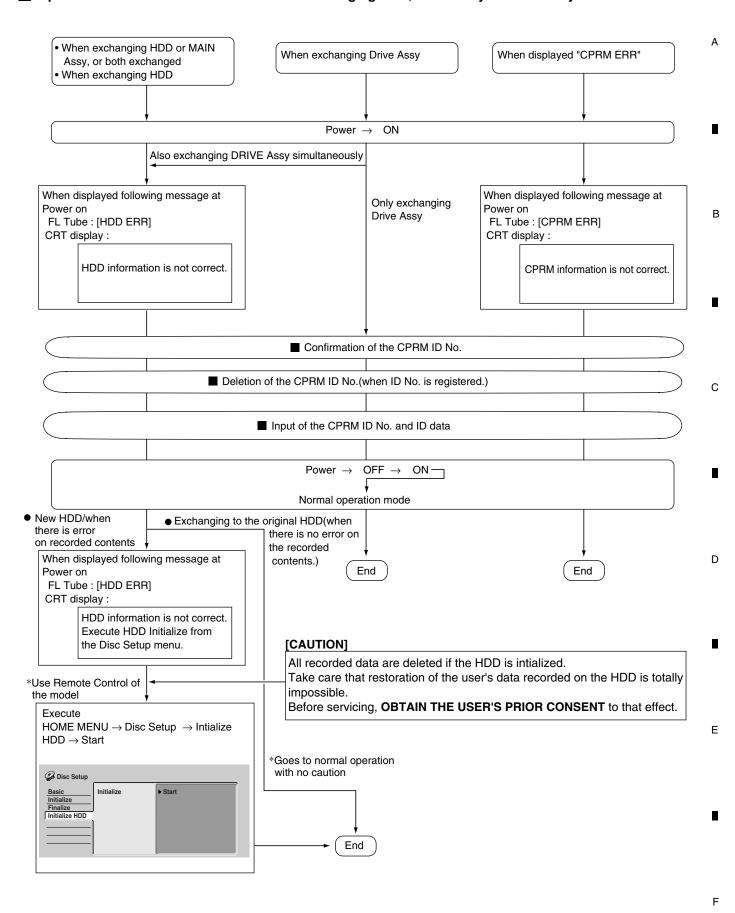


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### ■ Input Flow of the ID No. and ID data when exchanging HDD, MAIN Assy or Drive Assy

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73

# How to Input the ID Number and ID Data

#### Note:

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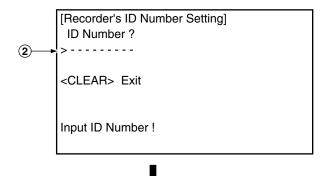
Be sure to enter the ID number in Stop mode.

Use the service remote control (GGF1381) for operations. Only opening/closing of the tray are performed from the player. The ID data disc is swept out automatically after the recorder has read the data from it.

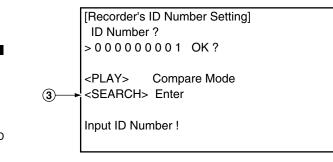
1 To enter the input mode, press ESC+STEREO sequentially in a status with no ID number set, such as after FLASH-ROM downloading.

② As number input is enabled when the unit enters the input mode, input the 9-digit ID number.

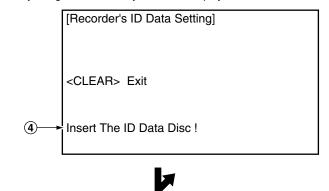
(The entered number is also displayed on the FL display.)



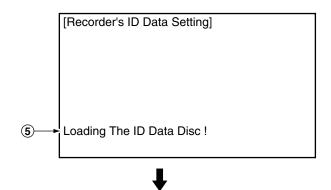
3 After inputting the number, press SEARCH to register the ID number.



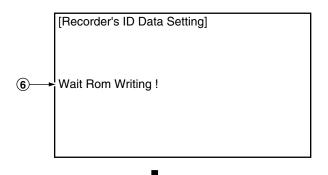
When the ID number has been registered, the unit enters the ID data input mode. (The FL display indicates "INSERT ID.") In this condition, place the ID data disc on the tray and close the tray using the CLOSE key "■/▲" on the player.



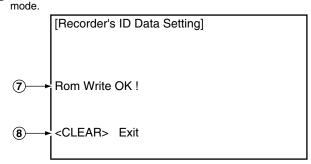
(5) While the data are being read, the message shown in the figure at left is displayed on the screen. (The FL display indicates "LOAD ID.")



(6) When the ID data have been read, the data are written to the FLASH-ROM. (The FL display indicates "WRITE ID.")



- (7) When the ID data have been written to the FLASH-ROM, the message "Rom Write OK" is displayed on the screen. (The FL display indicates "ID DATA OK.")
- (8) After confirming this message, press CLEAR to exit the input

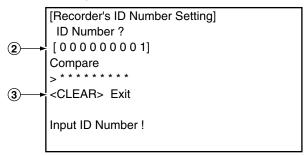


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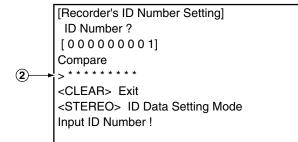
DVR-520H-S

- 2 The set ID number is displayed on the screen (and on the FL display), permitting you to confirm it.
- 3) To exit this mode, press CLEAR.

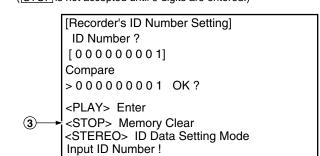


### **■** How to Clear the ID Number

- 1) Press ESC+STEREO sequentially with an ID number already set, and the unit enters the ID number confirmation mode.
- 2 Input the same number as the ID number you have set.



(3) After inputting the number, press STOP.
Only when the entered number matches the set ID number, the ID number is cleared and the unit exits this mode.
If the numbers do not match, you must return to step 2.
(STOP is not accepted until 9 digits are entered.)



75

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### 7.1.2 MODEL SETTING

- The Setup is Necessary When :
  - a) When the MAIN Assy is replaced
  - b) When the JCKB Assy is replaced
  - c) When the MAIN Assy and JCKB Assy are replaced

Note: Make sure of setting the correct number.

3

#### • How to Setup the Model

Α

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After power on, the following screen is displayed on TV monitor.
 Press " 22 " by using the remote control unit for service(GGF1381).

```
[Recorder 's Model Setting]
Input the number by using the remote for Service.

> --

Input No. Model
[ 22 : DVR-520H-S ]
[ 23 : DVR-65H-S ]
```

2) After 1), the following screen is displayed on TV monitor. Press " 011 " by using the remote control unit for service.

```
[Recorder 's Type Setting]
Input the number by using the remote for Service.

> --- (Type -- , Region No. -)
Input No. Type
[ 01 : KU/CA <US> ]
```

The setting complete when OSD is disappeared.

- 3) Unplug the power cable.
- 4) Reset the recorder to all its factory settings.
  - 1. Make sure that the recorder is on.
  - Press and hold [STOP] and press [STANDBY/ON] key on the front panel.The recorder turns off with all settings reset.
- 5) Enter the Service Mode and then confirm the Model Name " DVR-520H/KU/CA ".
  - 1. Make sure that the recorder is on.
  - 2. Press [ESC] then [DISP] keys by using the remote control unit for Service.

```
DVR-520H/KU/CA
 VERSION: 0.60
 SYSCON
            RELEASE_45
                  :1.3685 $
            Rev
TUFLCON
                  MASK
                                    OK
            1 22
   DRIVE
            DVD-RW DVR-107X
                                    OK
            1.10K
                                    OK
            CKT0000353WL
                                    OK
    HDD
            ST380012ACE
                                    80
  DEVICE
            PRISM-PLUS
 REGION
          : 1
       С
            * * * *
           : 64M
 FLASH
```

#### Notes

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 After the setting complete, you can NOT CLEAR the setting data. Make sure the pressing number.

2) " NG " is appeared on TV when unsuitable number is pressed.

In such a case, please unplug the power cable and plug it again. Then restart the model setting.

76

DVR-520H-S

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### 7.1.3 DOWNLOAD METHOD

### • The Download is Necessary When :

- a) After model setting
- b) When "NG" is displayed at First screen (version information, etc)

#### [Notes]

Be sure NOT to turn off the unit during downloading.

If the unit is turned off during downloading, the SYSCON, TUFLCON, and DRIVE programs may not be properly rewritten, in which case the unit may not be able to initialize itself normally when turned on again. If that happens, repair the unit, as described below, then perform downloading again:

• In a case where the power to the unit was shut off during rewriting of the SYSCON program:

The SYSCON program will not function properly if the power to the unit is forcibly shut off while the message "DOWNLOAD-1" is displayed on the FL display during downloading. If downloading of the programs from the disc or through serial communication becomes impossible, replace the FLASH ROM.

• In a case where the power to the unit was shut off during rewriting of the DRIVE program:

The DRIVE program will not function properly if the power to the unit is forcibly shut off while the message "DOWNLOAD-2" is displayed on the FL display during downloading. If downloading of the programs from the disc or through serial communication becomes impossible, replace the DRIVE ASSY.

• In a case where the power to the unit was shut off during rewriting of the TUFLCON program (only for the flash-type TUFLCON microcomputers):

The TUFLCON program will not function properly if the power to the unit is forcibly shut off while the message "DOWNLOAD-3" is displayed on the FL display during downloading. If downloading of the programs from the disc or through serial communication becomes impossible, replace the TUFLCON microcomputer.

### 1. DISC DOWNLOAD METHOD

#### How to Download

This is disc download method to save the initial setting data and user setting data.

However, the following data is deleted after downloading by this method.

- \* Disc history data
- \* REC mode
- \* Last channel (Before turn unit off)
- 1) Open a disc tray in the " DVD " function.
- 2) Put the download disc on the tray.
- 3) Press and hold a "STOP" button for playback,

then press a " DISC NAVIGATOR " button on a front panel.

- The disc tray closes automatically and the disc is loaded.
- The disc tray opens automatically after loading.
- 4) Take out the Download Disc.
  - " DISC DWLD " is displayed on FL and download is started.
  - The display on FL changes to " DOWNLOAD-1 "
  - The display on FL changes to " DOWNLOAD-2 "
  - The display on FL changes to "DOWNLOAD-3" (\*)
  - After download is completed, the power turns off, and turns on and a disc tray closes automatically.
  - \* It takes for about 5 minutes until download is completed.
- 5) Press and hold a " ESC ", then press " DISP " on a test mode remote control unit for the release version confirmation.
- 6) Confirm a firmware release version.
- 7) Press " ESC " on a test mode remote control unit in order to exit the test mode.
  - (\*): "DOWNLOAD-3" is displayed only when the TuFL u-com is FLASH type.

77

В

С

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### 2. Serial DOWNLOAD METHOD

### [Notes]

This method is secondary way when the disc loading is impossible.

#### • JIGS

- \* PC with serial port
- \* RS232C straight cable
- \* RS232C I/F jig (GGF1348)
- \* 7P FFC (VDA1681)
- \* Download program (UFU.exe)
- \* Firmware

#### Connection

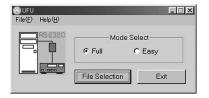
В

С

PC ⇔ RS232C cable ⇔ RS232C I/F ⇔ 7P FFC ⇔ DVD Recorder

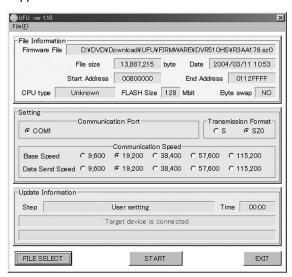
#### • How to Download

- 1) Connect the 232C I/F JIGS above way.
- 2) Turn on the PC and start the "UFU.exe".



3

- 3) Select the Firmware file. ("sz0" file)
- 4) Turn the DVD recorder on and start the download program.
  - " Target Device is connected" is appeared on the screen.



5) Select the Communication Speed (Baud Rate)

a) Base Speed 38,400 b) Data Send Speed 115,200

6) START

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\* Even if you click "START" button, sometimes "Communication Error" may come out one to twice, and download may fail. In this case, please click "START" again.

- \* Other factors can be considerd if download fails 3 times or more.
- \* And it takes about an hour for updating the firmware.

78

For service operations, use the GGF1381 remote control unit for service.

The Service-mode screens consist of nine mode screens, which are classified into such rough categories as recording system and VR playback system, and their subscreens.

• How to enter Service mode : Press the ESC then DISP keys in turn while no GUI is displayed. The first screen (version

information, etc.) shown below is displayed.

• How to exit Service mode : Press the ESC key.

• How to advance to the next Service-mode screen

: While the first screen is displayed, press directly one of the keys 1-9. For service, use the keys 2, 4 or 5, as shown below.

• How to advance to a subscreen within the same Service-mode screen

: Press the DIG/ANA key. Pressing the DIG/ANA key repeatedly will change the subscreens within the same Service-mode screen cyclically.

### ■ The Service-mode screens to be used for service are as follows:

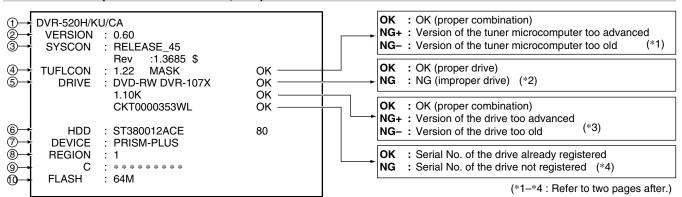
1 = First screen: Version information, etc.

- 2 = Second screen: ATA/ATAPI debug screen (Writer data)
- 4 = Fourth screen: Error log for the VR recording system
- 5 = Fifth screen: Error log for the VR playback system

**Note:** After entering one of the Service-mode screens, if you wish to shift to another Service-mode screen, exit Service mode first, then reenter Service mode and select your desired Service-mode screen.

### Description of Each Service-mode screen

### 1. First screen (version information, etc.)



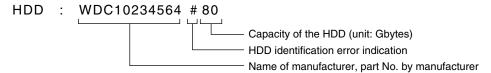
- 1) Model name/destination
- 2 Version of the recorder software
- ③ Revision No. of the system-control computer software (Edition administration No. [from top to bottom, common software, firmware, application software])
- 4 Version No. of the tuner microcomputer, Mask or Flash

Result of the combination ckeck with system u-com

- (Model name, version No., model type, serial No.)
- 6 Data of the built-in HDD, capacity of the HDD
- 7 Version No. of PRISM
- ® Region No.
- 9 CPRM data (CPRM kev No.)
- (ii) FLASH ROM information

While the first screen shown above is displayed, press the DIG/ANA key to enter the subscreen shown below. **Note:** Each time the DIG/ANA key is pressed, the display changes between the first screen and its subscreen.

### • Details on HDD data are described below:



If any abnormality exists in HDD connection, the indications shown in Table 1 below are displayed.

79

8

В

С

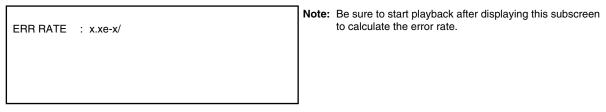
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HDD identification conditions	Example of HDD data to be displayed	Remarks
Failure in physical identification of HDD (no connection, defective HDD, interface error)	Blank space	
Physical identification of HDD possible, but not identified	WDC 10234564 # 80	"#" is displayed as HDD identification error
Physical identification of HDD possible, HDD identified, but failure in logical formatting	WDC 10234564 ! 80	"!" is displayed as HDD identification error
Physical identification of HDD possible, HDD identified, and correct logical formatting (HDD correctly identified)	WDC 10234564 80	

While the first screen shown above is displayed, press the DIG/ANA key to enter the subscreen shown below. Note: Each time the DIG/ANA key is pressed, the display changes between the first screen and its subscreen.

### Subscreen 1: Result of error-rate measurement



During playback in VR mode, the average error rate of the past 10 VOBUs is displayed, and during playback in DVD-Video or Video mode, the average error rate of the past 256 sectors is displayed. During playback in VR mode, the rotation rate of the drive (/: normal speed, no display = double speed) is also displayed.

#### Subscreen 2: HDD information

HDD Info Life Time: 87599h 09m 05s

Cumulative HDD-on time

#### How the data on cumulative HDD-on time are processed in memory

Storage place: Backup SRAM, Flash ROM

Timing of referring to the data on cumulative HDD-on time: When the power is turned on, the backup SRAM is referred to regarding the data on cumulative HDD-on time, and the data are stored in the RAM. If referring to the backup SRAM fails, the flash ROM is referred to.

Timing of updating the data on cumulative HDD-on time: While the HDD is on, the data on cumulative HDD-on time in the RAM is updated every 3 seconds, and every time updating is executed the data are stored in the backup SRAM. When the power is turned off, the data are stored in the flash ROM.

#### How to clear the data on cumulative HDD-on time

Backup SRAM: When the HDD Identification Setting is performed, the data on cumulative HDD-on time are automatically cleared. The HDD Identification Setting is automatically performed when the CPRM setting is performed on the CPRM setting screen (to display the CPRM setting screen, press the ESC then the STEREO keys).

Notes: The data on cumulative HDD-on time are not cleared when resetting to factory-preset values is performed.

The data on cumulative HDD-on time are not cleared when the system-control computer software is downloaded.

Flash ROM: The data on cumulative HDD-on time cannot be cleared (they are not cleared even if resetting to factory-preset values is performed or if the system-control computer software is downloaded).

Note: The data on cumulative HDD-on time in the flash ROM can be cleared if you clear the data in the backup SRAM following the above-mentioned procedures then turn off the power of the unit, because the data in the backup SRAM are stored in the flash ROM when the power is turned off.

### • When "NG" is displayed at First screen (version information, etc)

(\*1) NG+: Version of the tuner microcomputer too advanced

NG-: Version of the tuner microcomputer too old

1. When TUFL  $\mu\text{-com}$  is MASK type

NG+: Download the firmware.

NG- : Replace the TUFL  $\mu$ -com or JCKB ASSY.

2. When TuFL  $\mu$ -com is FLASH type

NG+ : Download the firmware. NG- : Download the firmware.

(\*2) NG: NG (improper drive)

Replace the correct Drive Assy.

(\*3) NG+: Version of the drive too advanced

NG-: Version of the drive too old

NG+ : Download the firmware. NG- : Download the firmware.

(\*4) NG: Serial No. of the drive not registered

Check the part No. and replace the correct Drive Assy.

81

В

D

Ε

### 2. Second screen (ATA/ATAPI debug screen)

Subscreen 1 of the second screen is displayed when the ESC, DISP, then "2" keys are pressed, in that order. **Note:** Each time the DIG/ANA key is pressed, the display changes cyclically among subscreens 1 to 4.

### Subscreen 1: Command log (ALL) of ATA/ATAPI DEBUG OSD

```
ATA/ATAPI History - ALL
 32 010000000000A000
                               OK
 32 2A0000DEBB00063000
                               OK
 32 2A0000DF1E000063000
                               OK
 32 2 A 0 0 0 0 0 D F 8 1 0 0 0 0 6 3 0 0 0
                               OK
 32 2A00000DFE4000062000
                               OK
 32 2A00000E046000063000
                               OK
 32 2A00000E0A9000063000
                               OK
 32 2 A 0 0 0 0 0 E 1 0 C 0 0 0 0 6 3 0 0 0
                               OK
>32 2A00000E16F00006200023A00
```

(Not for Service)

3

### Subscreen 2: Command log (ERROR) of ATA/ATAPI DEBUG OSD

(Not for Service)

#### Subscreen 3: Writer mentenance information of ATA/ATAPI DEBUG OSD

The cumulative power-on time and error log that are administered by the writer are displayed. Such information is obtained when the power is turned on. Thereafter, each time the SEARCH key on the remote control unit for service is pressed while subscreen 3 is displayed, the updating command is sent, and the data on the subscreen are updated. Care must be taken when updating this subscreen, because an undesired command is inserted if it is executed while recording, etc.

```
ATA/ATAPI
            Writer MaintenanceInfo
           00 00 00 0000 00000000;
Power ON
           01 00 00 0000 00000000
0102:56
DVD
           02 00 00 0000 00000000
           03 00 00 0000 00000000
R0053:48
                                         Error log for the Writer
W0022:16
           04 00 00 0000 00000000
CD
           05 00 00 0000 00000000
R0034:04
           06 00 00 0000 00000000
              00 00 0000 0000000
W0000:00
           07
               0 0 - 0 0
                                        (Not for Service)
```

- 1 Power-on time/cumulative power-on time
- 2 Duration of emission of the laser diode (LD) for DVD-R/DVD while reading
- 3 Duration of emission of the LD for DVD-W/DVD while writing
- 4) Duration of emission of the LD for CD-R/CD while reading
- 5 Duration of emission of the LD for CD-W/CD while writing

(Reference)

MTTF time of each LD (as the guideline of life span of each LD)

R7R Drive Assy (Read + Write total time)

DVD: 4700h CD: 11000h

82

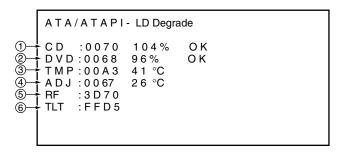
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В

### • Subscreen 4: ATA/ATAPI DEBUG OSD\_LD degradation judgment

6

The degrees of degradation of the LD (laser diode) for the writer (LDs for CD and DVD separately), temperature, and RF level are displayed. To update the data on the subscreen, press the SEARCH key on the remote control unit for service while subscreen 4 is displayed. See Table 1 below for a description of each item and the conditions for updating data.



5

5

Table 1: Description of each item and conditions for updating data

No.	Item	Description	Conditions for updating by pressing the SEARCH key	Remarks
1)	CD	Degradation judgment of LD for CD. Regarded as NG when the value is 120% or higher (same standard as for the PC drive)	No disc inserted in the disc tray	*1
2	DVD	Degradation judgment of LD for DVD. Regarded as NG when the value is 120% or higher (same standard as for the PC drive)	No disc inserted in the disc tray	*1
3	ТМР	Current temperature inside the Writer	No disc inserted in the disc tray	*1
4	ADJ	Temperature (approx. 25°C) inside the Writer during adjustment	No disc inserted in the disc tray	*1
(5)	RF	RF level (16-bit data, proportional calculation performed using the actual RF level value with 2.5 V = 0xFFFF as the maximum value, displayed in 4-digit hexadecimal)	During playback of disc medium	*2
6	TLT	Writer adjustment data for straight (non-HDD) model (FFFF is diplayed when the writer is not adjusted.)	No condition	

<sup>\*1 :</sup> For correct judgment, after leaving the unit at a normal temperature (25°C typ.) for some time, judgment must be performed immediately after the unit is turned on with no disc loaded.

83

8

8

В

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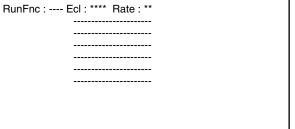
Ε

<sup>\*2:</sup> Use this item only for confirmation before and after lens cleaning, as the lens becomes dirty with dust.

### 3. Fouth screen (VR-recording-related error log)

Subscreen 1 of the fourth screen is displayed when the ESC, DISP, then "4" keys are pressed, in that order. **Note:** Each time the DIG/ANA key is pressed, the display changes cyclically among subscreens 1 to 11.

### • Subscreen 1:



(Not for Service)

3

### • Subscreens 2 and 3:

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These subscreens are not for service use.

### Subscreen 4: Error log for VR recording

```
Recording Error History Display 01-06-01 20:05:30 No SysHdr IN 01-06-02 00:22:10 Write Error
```

 Recording-related error log for the last 18 errors, divided into 2 screens (generation time [year-month-day, hour:minute:second], error data in simplified description)

#### Notes:

- For details on error messages, see Table 2 "Description of VR-recording-related errors".
- The two error-log screens can be switched by pressing the SPEED+ or SPEED- key.

### • Subscreens 5 to 11:

These subscreens are not for service use.

В

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Subscreen 1 of the fifth screen is displayed when the ESC, DISP, then "5" keys are pressed, in that order. Note: Each time the DIG/ANA key is pressed, the display changes cyclically among subscreens 1 to 4.

### • Subscreen 1:

```
G: 001-01  00h00m00s00# -. - e - - 00 . 00M
Tgt: STOP Now: STOP Spd: 0
Man: STOP Sub: 0 VBF: 000 ABF: 00
TrMd:STOP TrSt:0 TNo: Ver:00 RvMd:STOP RvSt:0 DNo: Aer:00
CcSt: STOP
                ld: 00000000
Stc: 00000000 Tpp-Av1: +-0 V-A: +-0
MPEG2 720x480 A0 AC-3 2ch 0256k
NT ASP: 43 CGMS: 0 APS: 0 Src: 0
END: 00h00m00s00
                             Cell: 000
```

### Subscreen 2: Error log for VR playback

```
G: 01-01 00m00s# -. - e - - 00000000
      h m s Message
                             h m s Err
G001:000000 Tr:Nullblk
L002:001230 Tr:SchLate
L002:004103 Tp:VobDif+
L002:004104 Tp:VobDof-
```

- ① Data on location of the display Original(G)/play list (L), title No., chapter No. (X:XX-XX), time of the display (min, sec, frame [XXmXXsXX]), busy mark of the virtual mechanical-control computer (#), error rate of the transfer data (X.XeXX), playback logical address (ID [XXXXXXXX])
- ② Error message log Original(G)/play list (L), title No., time of generation (min, sec [XXX:XXXX]), playback-related error log for the last 13 errors (XX:XXXXXXX)

- For details on error messages, see Table 1 "Description of VR-playback-related errors".
- If a VR-playback-related error is generated, a problem in data reading from the disc may be suspected. (The possibility of a problem on the drive side is high.)

### • Subscreens 3 and 4:

These subscreens are not for service use.

5

85

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Table 1: Description of VR-playback-related errors

3

Error Message	Description
Tr : NullBlk	Transfer task: NULL at the top block (Detecting NG stream made at the DVR-1000 series and starting protection process.)
Tr : ReadErr	Transfer task: ATA read error
Tr : SchLate	Transfer task: ATA search late
Tr : SemTOvr	Transfer task: Timeout for gaining semaphore (no synchronization with the display)
Tr : NaviErr	Transfer task: Inconsistency between NAVI (navigator) of management data and actual NAVI
Tr : OrderEr	Transfer task: Inconsistent order
Mn : Av1Hang	Main task: Detects hang-up of AV decoder and starts recovery
ERR_RCV!	TPP task: Detects hang-up of AV decoder and starts recovery
Tp : VobDif+	TPP task: The decoder STC advances by 1 VOBU hour.
Tp : VobDif-	TPP task: The STC of the management information advances
Tp : midNULL	TPP task: The management information pointer designated was NULL.
Tp : ScanNg	TPP task: Failure to set the TPP memory when scanning was canceled.
Tp : RStepEr	TPP task: Although the reverse step had failed, the operation was forcibly terminated because the top cell was located.
Tp : tppErr	TPP task: Inconsistency occurred.
Rv : 1stTOvr	Reverse playback task: Timeout for waiting for interruption to the top VOBU immediately after starting decoding
Rv : OpnTOvr	Reverse playback task: Timeout for waiting for B-picture of the open GOP immediately after starting decoding
Rv : OplTOvr	Reverse playback task: Timeout for waiting for I-picture of the open GOP immediately after starting decoding
Rv : LnkTOvr	Reverse playback task: Timeout for waiting for link
Rv : LnkFail	Reverse playback task: Starts compensation by detecting link failure
Rv : R2FTOvr	Reverse playback task: Starts retrial after detecting timeout from reverse pause to forward pause
Rv : TopVbEr	Reverse playback task: Forced termination because of a possible error of the top data during reverse normal playback
Rv : OrderEr	Reverse playback task: Inconsistent order
Av : B/CTOvr	AV1: Buffer-clear timeout
Av : StrmOvr	AV1: Timeout for waiting for stream ready
Av : TpmTOvr	AV1: Timeout for TP mode change
Av : SpmTOvr	AV1: Timeout for a step command
CC_OS_ERR	Closed caption task: OS error
	·

Abbreviations: STC = System Time Clock VOBU = Video Object Unit GOP = Group Of Picture B-picture = Bidirectionally predictive-picture

I-picture = Intra-picture P-picture = Predictive-picture TP mode change = AV1 term (Trick Play mode change)

86

DVR-520H-S

## • Error related to MPEG Encoder

5

Error Message	Description
Stream NG	Inappropriate input stream data
Stm Start NG	Failure to start encoding (reasons not clear)
AVEnc Hang	Inappropriate MPEG encoder
No SysHdr IN	System packet is not input periodically
Strm Start NG	Timeout waiting for system packet input at the beginning
IN Encode *	Changes cannot be made in the process of encoding
EncModul Hang	Encoder routine is hung up.

## • Error related to Drive system

Error Message	Description
BUF Overflow	Overflow of the Stream Buffer
Drive Hang	The Drive is hung up.
Write Err	The Drive failed to write and could not be recovered.
Read Err	Reading failed, ECC failed, etc.
Drv Hard Err	Abnormality in the drive hardware or firmware
Mech No Res	No response from the mechanical-control computer
Drv Timeout	Timeout waiting for drive operation
NWA Exhaust	NWA surpassed and impossible to use
MKB Invalid	MKB reading error
Drv Err	General error of the drive
Fail Repair	Repair failed
ReadOnly DISC *	Because some data are invalid, data cannot be written
May Be V mode	AlthoughTMP_VMGI is not written, it may be Video Mode disc.
Rzn Rsv NG	Reserve RZone failed
Rzn Cls NG	Close RZone failed
Rzn Rpr NG	Repair RZone failed
Bdr Opn NG	Open Border failed
Bdr Cls NG	Close Border failed
Format NG	Format failed
OPC NG	OPC failed
PCA Full	PCA has been used up.
RMA Full	RMA has been used up.
VTSI_B Wr Err	Video Mode VTSI BUP Write Error
VTSI Wr Err	Video Mode VTSI Write Error
TMP-VMG WrErr	Video Mode TMP VMGI Write Error
CLS Rzon Fail	Video Mode Close Rzone failure

### • Error related to Dubbing

Error Message	Description
Mem get NG	Video Mode Copy Memory has not ensured.
V Rsv RzoneNG	Video Mode Copy Reserve Rzone failed
VCHDD Info NG	Obtaining Video Mode Copy HDD Cell information failed
VC Pck Anl NG	Analizing Video Mode Copy Pack failed
VC VOBU SizeE	Video Mode Copy VOBU Size NG
Tracon Trn NG	Video Mode Copy Tracon tranfer has not been completed.

87

Е

С

## • Error related to Dubbing (continued)

Error Message	Description
Strm TransfNG	Video Mode Copy Stream Transfer NG
VC FlushC NG	Video Mode Copy Flush Cache NG
VC Transf Stp	Video Mode Copy Transfer Stop
VC CopyCancel	Video Mode Copy Copy Cancel
VC Idling NG	Video Mode Copy idling NG
VC TSO BLK NG	Video Mode Copy TSO Block transfer has not been completed.
VC Cell Max	Maximum number for Video Mode copy Cells exceeded
VC HDD Inf NG	No information on Video Mode Copy HDD
VC HDD C Err	Inappropriate Video Mode Copy HDD content
V2H SRC Prot	$VR \rightarrow HDD$ copy prohibitted material
V2H Aud Ch NG	VR →HDD Audio Channel NG
V2H Aud Stm N	VR →HDD Audio Stream number NG
V2H Aud Md NG	VR →HDD Audio Mode NG
V2H V Reso NG	VR →HDD Video resolution NG
V2H Unknown	VR →HDD other NG
H2D CP SomeNG	VR →HDD copy and other NG

3

## Other Errors

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Error Message	Description
DRAM NG	Abnormality in access to the Work DRAM
SRAM NG	Abnormality in access to the backup work SRAM
CPRM IC NG	Inappropriate CPRM IC
Drive Destroy	The drive has crashed.
MKB REVOKED	Error in gaining data
WM Cracked	WM Cracked
VBR-SRAM NG	Abnormality in VBR SRAM
BK BATT Down	Backup RAM data has been erased.
BK FSYS Dirty	Backup RAM data has not been wrtten on the File Sys.
VOBU Info NG	Inappropriate VOBU information
Ourob Strm NG	Inappropriate stream data to the Ouroboros input
WaterMark Det	Watermark detected
No Video	No video input (not locked)
Disc Full	No further data can be written because the disc is full.
No More Info *	No more space in the internal work-management area
No Permission *	No permission to write to the disc
Limit Over *	Standard maximum limit exceeded
Rec Pause *	No operation permitted during recording pause
Invalid Param *	Invalid parameter
Protect Src *	Source to be recorded is copy-protected.
Now Busy *	In the process of the emergency processing
Invalid Disc *	The disc cannot be recognized.
Invalid UDF *	Invalid UDF content
Invalid VMG *	Invalid VMG content
Invalid TMVMG	Invalid TMP_VMGI content
Unmatch Stamp *	Impossible to modify because of nonmatching time stamp
Virgin DISC	Virgin Disc

F

Ε

88

### • Other Errors (continued)

5

Error Message	Description
SW Vpb mode *	Switching to video playback routine is required.
SW Vrec mode *	Switching to video recording routine is required.
NV Pck MK Err	Error in creating NaviPack
NV Pck DMA Er	Inappropriate NaviPack DMA
Cell Close NG	Cell Close NG
Relocation Do	VR-recording data was relocated
Something *	undetermined error
Status NG *	Abnormality in change of statuses
Irr Action *	Incorrect action
Abort *	Cancellation
BusReset Done	Bus Reset has been excecuted.
Repair Excec	Repairing has been executed.
Format Excec	Formatting has been executed.
BUG	Some bugs
PARAM NO ACCP	Recording parameter is not matched.
DRAM CLR Err	Video Mode DRAM (Stream Buffer) Clear failure
V Categ ID NG	Inappropriate Category ID
V Cate Inf NG	Inappropriate Category information
V Ext TY NG	Type NG
V Ext MAX Ovr	Count Max exceeded
V ExtToo Big	The extension file is too large.
Over Heat	Abnormal temperatute

### • Error related to HDD

Error Message	Description
HDD unauthor	Inconsistent HDD serial No.
HDD Destroy	HDD is not recognized on the bus.
TT Rec Over	Title recording time full
HDDReset Done	HDD Reset executed
Task No Activ	Task has not been activated.
HDD Buff High	High-level process executed for the HDD Buffer
HDD Trans Err	DMA error in HDD copy transfer
HDD Zero WR	MBR readout generated
HDD Initialize	HDD initialized
HDD MBR NG	Inconsistent MBR data
HDD SIG NG	Inconsistent HDD Management Data Magic
HDD INFO BAD	Incorrect HDD Management Data
HDD IRRG POFF	Abnormal power off
HDD SMART NG	Inappropriate HDD SMART

### No Error

Error Message	Description
Non Err *	Normal

### Notes;

- Any error message marked with \* is displayed "RecErr : -------" on the Subscreen 1 of the fourth screen.
- In a case of an error in the drive system, scratches or dirt on a disc, or a problem of the drive itself (dirty pickup) may be suspected.

Abbreviations: ECC = 4 byte Code for Error Correction UDF = Universal Disc Format

PCA = Power Calibration Area OPC = Optical Power Control

NWA = Next Writable Address

VMG = Video Manager
RMA = Recording Management Area
MKB = Media Key Block
TMP\_VMGI = Temporary Video Manager
Information

Border = from Lead-in to Lead-out

89

В

С

D

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F

DVR-520H-S

5

### **Table 3: List of Key Codes**

### How to enter each check mode

Test mode remote control unit : [A8\*\*]

Remote control unit supplied with the DVR : [AB\*\*]

No.	Check Item	Key Input	Operation / purpose	Remarks
		$[ESC] \to [A.MON]$	Turns on/off EE mode cyclically	
1	EE system (same as preview)	[PLAY]		Make sure that CGMS = 11 becomes when CGMS = 10 is input. EE mode: Simulation mode for recording status
		[STOP]	Stops the EE system in EE mode	
2	Error-rate measurement	[ESC] → [SIDEB]	V-mode recording: After recording for 10 seconds, the unit starts playback while displaying the error rate. DVD-Video: The error rate is automatically measured, then the result will be displayed.	For details, see " 7.1.4 ERROR RATE MEASUREMENT ".
3	Settings for specific areas	[ESC] → [CHP/TIM]	Enters Adjustment mode for AVIO settings	Settings are made for the selected input (TUNER, LINE).
		[ESC]	Determines the settings, then exits Adjustment mode	For details, see " 7.1.5 SETTINGS FOR SPECIFIC AREAS ".

3

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- How the ESC code is processed

   When the ESC code is received, ESCAPE mode is entered, but in combination with the code(s) that follow(s), a specific meaning is added.

   If ESC codes are received continuously, ESCAPE mode is retained.

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Press the ESC, DISP, then "3" keys, in that order.

①— (DV/1394) Init:OK AV:01 DV:01 INT4:02	
@ [Recoder] GUID:00E036000160001 IRM  ③ iPCR:C03F0000 oPCR:0000007A  ④ [DV] GUID:0080880303480E96  ⑤ VN:VICTOR MN:GR-D50K  ⑥ TM:C3 TS:75 CT:32 WP:01 PS:FF OS:00  ⑦ CA:A000002020 CV:FF MD:VTR  ⑧ [DVdecoder:Yes]  ⑨ TC:00h20m35s02f RD:02/02/05 RT:10h34m50s  10 ASPECT:4:3 CGMS:000000 APSTB:00 DEC:525-60  ⑤ SF:32kHz QU:12bit AMODE:4) Stereo  [DVencode:No]  13 TC:hmsf RD:// RT:hms  4 ASPECT: CGMS: APSTB:	Boldface alphanumerics : Fixed indications Nonboldface alphanumerics : Variable indications

No.	Item	Description	Remarks		
	Init	Whether the initialization of uPD72893B (1394LINK & DVcodec IC) has been completed (OK) or not (NG)	In a case of NG, communication with uPD72893B may have failed.		
1	AV	Number of AV devices on the local bus			
	DV	Number of DV devices on the local bus	If the number does not become 01 even if a DV device is connected, identification of that device fails.		
	INT4	Number of executing INT4(PIO) interrupt processing routines until a POWER ON notification arrives from uPD72893B (normally, 02)			
2	GUID	GUID set in ConfigROM of the unit	In a case of ROOT (IRM), IRM is displayed at the rightmost of the GUID indication		
<u> </u>	iPCR	iPCR value of the unit			
3	oPCR	oPCR value of the unit			
4	GUID	GUID set in ConfigROM of the connected DV device	Data are displayed only if one DV device is identified. If the connected DV device is ROOT (IRM), IRM is displayed at the rightmost of the GUID indication		
(5) (6)	VN	Vendor name set in ConfigROM of the connected DV device	Data are displayed only if one DV device is identified. (Depending on the device, the vendor name may not be set in ConfigROM.)		
	MN	Model name set in ConfigROM of the connected DV device	Data are displayed only if one DV device is identified. (Depending on the device, the vendor name may not be set in ConfigROM.)		
	тм	Transport Mode data obtained from the DV device	се		
	TS	Transport State data obtained from the DV device			
	СТ	Cassette Type data obtained from the DV device	Data are displayed only if one DV device is identified.		
•	WP	Copy-protection data obtained from the DV device	Sala are displayed only if one by device is identified.		
	PS	Power-state data obtained from the DV device			
	os	Output signal mode data obtained from the DV device			
7	CA	Connect AV data obtained from the DV device	Data are displayed only if one DV device is identified.		
	cv	Camera/VTR data obtained from the DV device			
	MD	DV device mode	Camera or VTR is displayed only if one DV device is identified		
8	[DVdecode:XXX]	Whether Yes (in the process of requesting DV input) or No is indicated in XXX	Normally, Yes is indicated only when CH is set to DV		

91

В

С

D

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No.	Item	Description	Remarks
9	тс	Time-code data of the DVdecode Stream, or response data of the Time Code command	Stream time-code data are obtained when playback in the forward direction is performed. Otherwise, time-code data are obtained through an AV/C command.
	RD	Rec Date of DVdecode Stream	
	RT	Rec Time of DVdecode Stream	
	ASPECT	Aspect Ratio of DVdecode Stream	
	CGMS	CGMS of DVdecode Stream (from left to right, CGMS data of bits 5-4: Audio ch2, bits 3-2: Audio ch1, and bits 1-0: Video)	Recording of DV input cannot be performed unless the value of CGMS is 00.
10	APSTB	APS trigger bit of DVdecode stream	
	DEC	With/without DVdecode stream input	With input: Signal type (525-60, 625-50, 1125-60, 1250-50, or Invalid) is indicated, Without input: "No" is indicated.
133	SF	Sampling Frequency of DVdecode Stream	If SF is 44 kHz, it is considered that 44.1-kHz audio is input, and sound is muted on the unit.
11)	QU	QUANTIZATION of DVdecode Stream	
	AMODE	AUDIO MODE of DVdecode Stream	
12	[DVencode:XXX] Whether Yes (in the process of requesting DV output) or No is indicated in XXX		Normally, Yes is indicated only with HDD or DVD playback
	TC	TIME CODE of DVencode stream	
13	RD	REC DATE of DVencode stream	
	RT	REC TIME of DVencode stream	
	ASPECT	Aspect Ratio of DVencode stream	
14)	ссмѕ	CGMS of DVencode stream (common to video, audio ch1 and audio ch2)	Normally, sources other than CGMS=00 are not output.
	APSTB	APS trigger bit of DVencode stream	

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92

DVR-520H-S

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Symptoms	Location in the Debug Screen	Items to be Checked, and Conditions	Possible causes
No operation for either DV	Θ	Check the init indication:  OK: Initialization of DV-related LSIs (IC5101, IC5202) appropriately completed NG: Communication failure between DV-related LSIs (IC5101, IC5202) and HOST microcomputer (IC1001). Initialization of DV-related LSIs (IC5101, IC5202) has not been completed properly.	Defective IC, defective soldering, defective power supply, etc.
		Check the number of DV devices when one DV device is connected to the recorder:  10: The connected DV device is correctly identified.  Other than 01: The connected DV device is not correctly identified.	Defective DV terminals, improper connection of the DV-terminal board, defective IC, defective cables, an IEEE 1394 device other than the DV device connected
	©	Check of DV decoding when the recorder channel is set to DV: Yes: The recorder is in the process of a DV input operation No: The recorder is not executing a DV input operation	Defective IC, defective soldering, defective power supply, etc.
No picture nor sound for DV input	6	Check DEC: 525-60: An NTSC DV signal is input from the DV device. 625-50: A PAL DV signal is input from the DV device. No: No DV signal is input from the DV device.	Defective DV terminals, improper connection of the DV-terminal board, defective IC, defective source device Note: As to a model having the Input Line System setting, if the setting and the actual input signal system do not match, no picture appears.
DV input recording impossible	6	Check CGMS: 00: A copy-permitted source is being input. Other than 00: A copy-protected source is being input.	Recording cannot be performed for a copy-protected source.
No sound for DV input	@	Check SF: 32 khz: An audio signal with 32-kHz sampling frequency is being input. 48 khz: An audio signal with 48-kHz sampling frequency is being input. 44 khz: An audio signal with 44.1-kHz sampling frequency is being input.	An audio signal with 44.1-kHz sampling frequency is muted.
No picture nor sound for DV output	©	Check DVencode during DVD/HDD playback: Yes: The recorder is in the process of a DV output operation No: The recorder is not executing a DV output operation (No is also displayed during playback of copy-prohibited sources or simultaneous-recording/playback.)	Defective IC, defective soldering, defective power supply, etc.

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### 7.1.6 ERROR RATE MEASUREMENT

#### How to enter Error-Rate Measurement mode

Press the ESC key then the SIDE-B key of the remote control unit for service to enter Error-Rate Measurement mode. During playback of DVD-VIDEO, Error-Rate Measurement mode can also be entered by pressing the ESC key then the PLAY key.

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#### How to exit Error-Rate Measurement mode

Press the ESC key. The error-rate display disappears, and Error-Rate Measurement mode is exited.

Note: The error rate cannot be measured in VR mode or during CD playback.

#### Functions

① Video-mode recording (recording medium)

In this mode, DVD recording is automatically performed for 10 seconds, the recorded DVD title is played back while the error rate is being measured, then as soon as playback of the recorded DVD title is finished, playback stops.\*1 After error-rate measurement is finished, the average error rate will be displayed on the FL display and OSD. Only in a case in which the calculation of the average error rate fails, the tray will open.

2 DVD-VIDEO (playback medium)

Only during playback, when the ESC key then the SIDE-B key (or the ESC key then the PLAY key) are pressed, the error rate is calculated and displayed on the FL display and OSD.(\*2) Only in a case in which the calculation of the average error rate fails, the tray will open.

### ■ Changes of display

### Table 1: Video mode (recording medium)

	Omeration	Display		
	Operation	FL Display	OSD (On Screen Display)	
	"ERROR RATE" is displayed on the FL display for an instant.	ERROR RATE		
	DVD recording starts.	ERROR RATE		
	DVD recording is performed for 10 seconds.	x x x x x		
	The recorded DVD title is played back while the error rate is being measured, then as soon as playback is finished it stops.	ER x . x E - x	ERR RATE : x.xE-x -	
)	After error-rate measurement is finished (*1), the average error rate, the measurement-finish mark (*), and the OK/NG-judgment result (*3) will be displayed on the FL display and OSD. (If the tray opens as a result of NG judgment, the display on the FL display and OSD will be retained.)	ER x.xE-x	ERR RATE : x.xE-x * OK	

### Table 2: DVD-Video (playback medium)

Onewstien	Display		
Operation	FL Display	OSD (On Screen Display)	
Only during playback, when the corresponding keys are pressed, the error rate is calculated and displayed on the FL display and OSD. (*2)	ER x . x E - x	ERR RATE : x.xE-x -	
After error-rate measurement is finished (*1), the average error rate, the measurement-finish mark (*), and the OK/NG-judgment result (*3) will be displayed on the FL display and OSD. (If the tray opens as a result of NG judgment, the display on the FL display and OSD will be retained.)	ER x.xE-x	ERR RATE : x.xE-x - OK	

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Table 3: On judgment whether error-rate measurement is finished or not

Recording Mode	Judgment whether error-rate measurement is finished or not	Recording/playback duration required for error-rate measurement	
Video mode	After playback of a certain amount (*) of data Measurement of the 16 ECC blocks is performed 16 times, then the grand sum is used for calculation of the error rate. The capacity is as follows: 16 ECC blocks × 16 sectors × 2048 bytes × 16 times = 8388608 bytes = 67108864 bits	The time required for completion of error-rate measurement varies, depending on the input video signal to be recorded.  (The more the motion in the input video signal to be recorded is animated, the shorter the playback time required for completion of error-rate measurement becomes.)	

<sup>\*2 :</sup> During DVD-VIDEO error-rate measurement, even after error-rate measurement is finished, playback continues, and the display of the error rate results is retained. In this playback mode, if Error-Rate Measurement mode is exited by pressing the ESC key, then it is reentered by pressing the ESC and SIDE-B keys (or ESC and PLAY keys), the error rate will not be updated, and the previous value is displayed. To reset the previous error rate, stop disc playback.

Table 4: List of OK/NG threshold values

Disc Type	Recording Mode	Finalized or not finalized	Reference Value	Display	
DVD-VIDEO			$8.0 \times 10^{-4}$	OK / NG	
DVD D	Video mode	Finalized	$1.0 \times 10^{-3}$	OK / NG	
DVD-R		Not finalized	$1.0 \times 10^{-3}$	OK / NG	
DVD DW	Video mode	Finalized	$1.0 \times 10^{-3}$	OK / NG	
DVD-RW		Not finalized	1.0 × 10 <sup>-3</sup>	OK / NG	

95

8

В

С

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<sup>\*3 :</sup> OK/NG judgment In DVD/VIDEO and Video Mode recording, OK/NG judgment is displayed under the following conditions:

### 7.1.7 VIDEO ADJUSTMENT FOR SPECIFIC AREA

**Purposes:** Depending on the area, jitter may appear in a picture received by the tuner, as conditions of signals received by the tuner are different from area to area. To correct this kind of problem, the function of the System Codec AVIO control section for adjusting signals received by the tuner can be used.

**How to enter setting modes:** To enter General Setting mode, press the ESC key then the CHP/TIM key of the remote control unit for service. To enter Specific Channel Setting mode, press the DIG/ANA key in General Setting mode.

How to exit setting modes: Press the ESC key. The setting mode is exited, the OSD disappears.

### 1. Specific Channel Setting mode

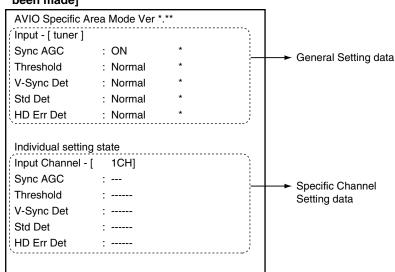
Α

This mode is entered when the DIG/ANA key is pressed in General Setting mode. In this mode, specific settings can be made for up to 12 channels. For channels that do not have specific settings, the settings of General Setting mode are applied.

Display in Specific Channel Setting mode (A picture from the tuner can be viewed using the semitransparent OSD display.)

#### [Display in Specific Channel Setting mode]

# [When specific channel settings have NOT been made]



# [When specific channel settings have been made]

AVIO Specific Area Mode Ver *.**				
Input - [ tuner ]				
Sync AGC	:	ON	*	
Threshold	:	Normal	*	
V-Sync Det	:	Normal	*	
Std Det	:	Normal	*	
HD Err Det	:	Normal	*	
Individual setting	sta	ate		
Input Channel - [		1CH]		
Sync AGC	:	ON		
Threshold	:	Auto Thresho	old Level[3]	
V-Sync Det	:	Normal		
Std Det	:	Normal		
HD Err Det	:	Normal		

- \*: setting is the default.
- If a channel that does not have specific settings is displayed, the setting figures are displayed as hyphens (--). If the setting figures are not displayed as hyphens, those settings have been specifically set even if they are identical to the default settings or those of General Setting mode.
- The channels to be displayed in "Input Channel" are as follows:
- In a case of line input: L1-L3, DV
- In a case of tuner input: Received channel (a channel to be set in specific channel settings)

96

DVR-520H-S

5

Key	Operation	Setting (*: Default)	Remarks	
DIG/ANA	Switches cyclically between General Setting mode and Specific Channel Setting mode.			
INPUT SELECT, CHANNEL +/- (*R)	Switches inputs or channels.	_		
[SIDE A], [SIDE B]	Sets Sync AGC.	ON (*) / OFF –		
[Rev ×3], [×3 Fwd]	Sets Threshold level.	(*) Normal Auto Threshold Level Manual Threshold Level Pedestal Level		
[Rev CHAPTER SKIP] [CHAPTER SKIP Fwd]	Sets Threshold level.	According to the setting of Threshold, the values can be changed within the range mentioned below.	-	
		Normal: The value is fixed, with no display of the value.	-	
		Auto Threshold Level: 0-8 (Default: 0)	-	
		Manual Threshold Level: 0-8 (Default: 0)	-	
		Pedestal Level:     0-8 (Default: 0)	-	
[Rev SCAN], [SCAN Fwd]	Sets V-Sync Det.	Normal (*) / Short / Long		
[Rev STILL STEP], [STILL STEP Fwd]	Sets Std Det.	Normal (*) / Non STD		
[SPEED +], [SPEED -]	HD Err Det	Normal (*) / Fast / Stop	-	
PLAY	All channels assigned to have specific settings are canceled, and the specific settings are reset to their default values.	-	Settings of General Setting mode are not affected.	
CLEAR	Initializes the setting of Specific Channel Setting mode.	_	Pressing the key resets the settings of Specific Channel Setting mode for that channel to the initial values. Settings of General Setting mode are not affected.	
PAUSE	The specific-setting data for the currently selected channel are reset to their default values. (But the assignment of a channel having specific settings is not canceled.)	Settings of General Setting mode are not affected (retained).		
ESC	Exits AVIO setting for specific areas, clearing the OSD.	-	-	

<sup>\*</sup>R: Refers to keys on the remote control unit supplied with this unit. The keys without "R" refer to the remote control unit for service.

97

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• Screen display when Specific Channel settings are made on 12 (maximum) channels: In such a case. If a channel which does not have specific settings is selected, the individual setting state for that channel is not displayed, as shown in the figure below, and the settings cannot be modified. In such a case, if you wish to make Specific Channel Settings for the currently selected channel, you must clear the

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# [With 12 channels having specific settings, when the currently selected channel does not have specific settings]

AVIO Specific Area Mode Input - [ TUNER ] Sync AGC : ON Threshold : Normal V-Sync Det : Normal Std Det : Normal HD Err Det : Normal Individual setting state Sorry! You can store only 12 channels for Specific Area mode.

Specific Channel Settings for one or more channels beforehand.

### 2. General Setting mode

This mode can be entered only during recording/playback stop. In this mode, each item and its current settings are displayed on the OSD. The currently selected input mode (TUNER or LINE) is displayed. If L1, L2, L3 or DV is selected for input, general settings for the line input can be made, and if TUNER is selected, general settings for the tuner input can be made.

### [General Setting mode] (\*2)

AVIO Specific Area Mode
Input - [ tuner ]
Sync AGC : ON \*
Threshold : Normal \*
V-Sync Det : Normal \*
Std Det : Normal \*
HD Err Det : Normal \*

\*: setting is the default.

98

Key	Operation	Setting (*: Default)	Remarks
INPUT SELECT, CHANNEL +/- (*R)	Switches inputs or channels.	-	-
[SIDE A], [SIDE B]	Sets Sync AGC.	ON (*) / OFF –	
[Rev ×3], [×3 Fwd]	Sets Threshold level.	(*) Normal Auto Threshold Level Manual Threshold Level Pedestal Level	
[Rev CHAPTER SKIP] [CHAPTER SKIP Fwd]		According to the setting of Threshold, the values can be changed within the range mentioned below.	-
		Normal: The value is fixed, with no display of the value.	-
		Auto Threshold Level:     0-8 (Default: 0)	-
		Manual Threshold Level:     O-8 (Default: 0)	_
		Pedestal Level: 0-8 (Default: 0)	-
[Rev SCAN], [SCAN Fwd]	Sets V-Sync Det.	Normal (*) /Short/Long	-
[Rev STILL STEP], [STILL STEP Fwd]	Sets Std Det.	Normal (*) /Non STD –	
[SPEED +], [SPEED -]	HD Err Det	Normal (*) /Fast/Stop	
CLEAR	Initializes the setting of General Setting mode.	Pressing the key resets all setting of General Setting mode to the initial values. Settings of Specific Channel Setting mode are not affected (they are retained).	
ESC	Exits AVIO setting for specific areas, clearing the OSD.	-	-

<sup>\*</sup>R: Refers to keys on the remote control unit supplied with this unit. The keys without "R" refer to the remote control unit for service.

\*1: In General Setting mode, if the channel displayed has specific settings, the following will be displayed.

### [Display in General Setting mode when the channel currently displayed has specific settings]

AVIO Specific Area Mode Ver\*.\*\*
Input - [tuner ]
Sync AGC : ON \*
Threshold : Normal \*
V-Sync Det : Normal \*
Std Det : Normal \*
HD Err Det : Normal \*
This channel is set up individually.

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### Notes:

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Commands from the remote control unit are accepted during Aging mode.

If Aging mode is quit using the ESC key, indications on the FL display will return to normal display.

Cancel timer settings before entering Aging mode.

Set the recording rate beforehand. It cannot be changed during Aging mode.

\* Be aware that all recorded data are deleted when the aging for the DVD-RW and HDD is executed.

### Table 1: Aging for the DVD-RW and DVD-R

	Aging for the DVD-RW	Aging for the DVD-R
To enter Aging mode	Press the DVD key to switch to DVD. Install a recordable DVD-RW disc. After disc detection, press the ESC key then the REP.B key on the remote control unit for servicing to enter Aging mode.	Press the DVD key to switch to DVD. Install a recordable DVD-R disc. After disc detection, press the ESC key then the REP.B key on the remote control unit for servicing to enter Aging mode.
To quit Aging mode	Press the ESC key on the remote control unit for servicing to quit Aging mode and return to Normal mode. This also results in the following:  • If during recording: Recording is stopped.  • If during playback: Playback is paused.  • If during initialization: The unit stops after initialization is finished.  • If the tray is being opened/closed: The unit stops after the tray is opened/closed.	Press the ESC key on the remote control unit for servicing to quit Aging mode and return to Normal mode. This also results in the following:  If during recording: Recording is stopped.  If during playback: Playback is paused.
	During Aging mode, the following operations are repeated in the order shown below.  ① The tray opens. ② The tray closes. ③ Initialization ④ Recording for 60 minutes ⑤ Playback for 45 minutes	During Aging mode, the following operations are repeated in the order shown below.  ① The tray opens. ② The tray closes. ③ Recording for 1 minute ④ Recording pause for 6 minutes ⑤ Recording stops. ⑥ Playback for 1 minute ⑦ Playback pause for 6 minutes ⑧ Playback stops. Note: A continuous test of the above operations is possible for approximately 23 hours.
Function	③ Initialization is performed according to the setting specified in "DVD-RW automatic initialization" (accessed by selecting "Unit Setting" then "Option").	After ② the tray closes, disc detection is performed, and if 99 titles have already been registered, the unit stops there. The number of loops is retained and indicated on the FL display. An error indication is retained as an OSD.
	During Aging, the number of loops is indicated on the FL display, as shown below. [AGING 0001]	During Aging, the number of loops is indicated on the FL display, as shown below. [AGING 0001]
	If an error is generated, the aging operation stops.  Note: Indications on the FL display are retained, and this information is also retained as an OSD.	If an error is generated, the aging operation stops.  Note: Indications on the FL display are retained, and this information is also retained as an OSD.
		Note: Recording time depends on the recording rate set. For example, if the recording rate is MN32, only up to 60 titles can be registered. Check the setting for recording rate before performing aging.

100

DVR-520H-S

	Table 2. Aging for the HDD				
	Aging for the HDD				
To enter Aging mode	Press the HDD key to switch to HDD. Press the ESC key then the REP.B key on the remote control unit for servicing to enter Aging mode.				
To quit Aging mode	Press the ESC key on the remote control unit for servicing to quit Aging mode and return to Normal mode. This also results in the following:  • If during recording: Recording is stopped.  • If during playback: Playback is paused.  • If during erasure of all memory data from the HDD, the unit stops after all memory data have been erased.				
Function	During Aging mode, the following operations are repeated in the order shown below.  ① Erasure of all the memory data from the HDD ② Recording for 60 minutes ③ Playback for 60 minutes  During Aging, the number of loops is indicated on the FL display, as shown below. [AGING 0001]  If an error is generated, the aging operation stops.  Note: Indications on the FL display are retained, and this information is also retained as an OSD.				

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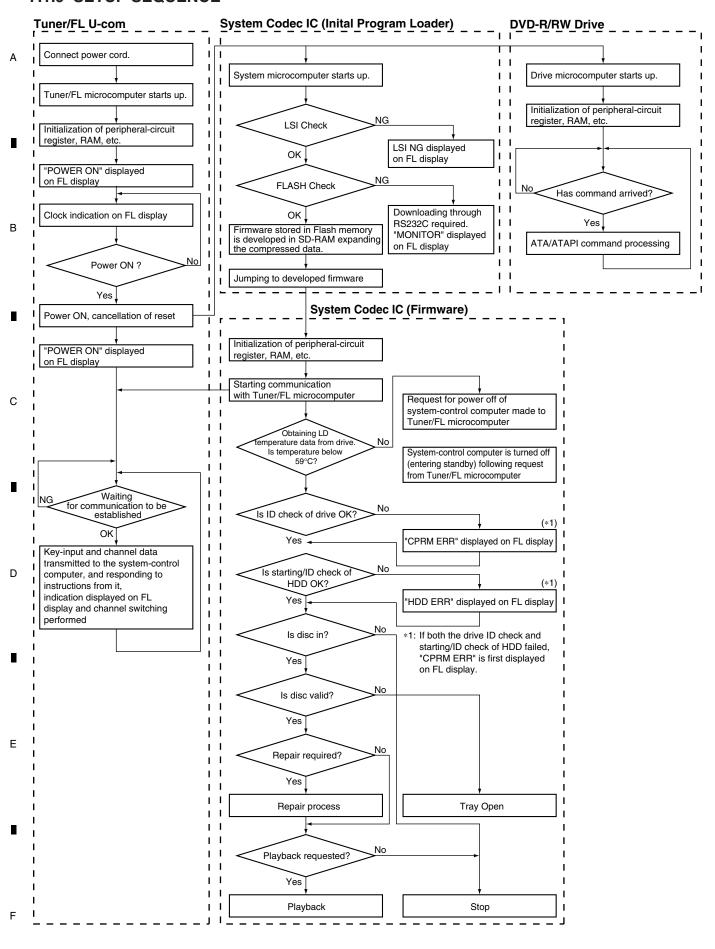
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101

### 7.1.9 SETUP SEQUENCE

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102

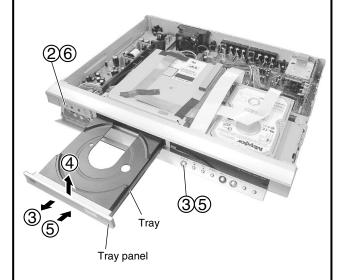
**Note 2 :** For performing the diagnosis shown below, the following jig cable for service is required:

• GGD1370 (Flexible cable)

## **Diagnosis of the MAIN Assy**

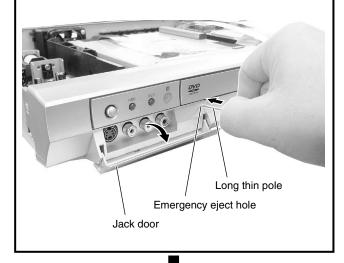
### 1 Bonnet Case S and Tray panel

- 1 Remove the bonnet case S by removing the eight screws.
- Press the U STANDBY/ON button to turn on the power.
- $\bigcirc$  Press the  $\triangle$  OPEN/CLOSE button to open the tray.
- (4) Remove the tray panel.
- (5) Press the ■ OPEN/CLOSE button to close the tray.
- (6) Press the  $\circ$  STANDBY/ON button to turn off the power.



### How to open the tray when the power cannot be on

When the player cannot eject disc tray due to power failure or any other reasons, open the jack door, and use a long thin pole and push the emergency eject hole under the tray panel to eject.



### 2 Front panel section

1 Unhook the four hooks.

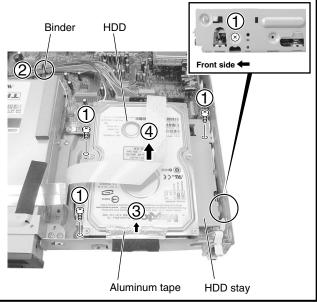
2 Remove the front panel section.

1 Front panel section

2 Bottom view

3 HDD Section

- 1 Remove the four screws.
- (2) Remove the binder.
- (3) Remove the aluminum tape.
- (4) Remove the HDD with HDD stay.



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103

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DVR-520H-S

## 4 DRIVE Assy R7 (DVD-R/RW WRITER)

- 1 Remove the some jumper wires from the clamp.
- Remove the binder.

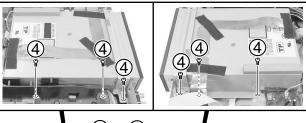
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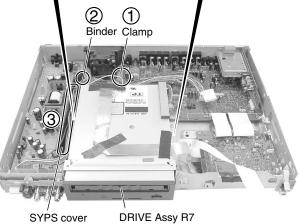
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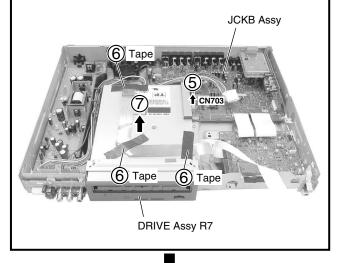
- 3 Remove the some jumper wires from the SYPS cover.
- (4) Remove the six screws.







- 5 Disconnect the connector.
- 6 Remove the three tapes.
- Remove the DRIVE Assy R7.

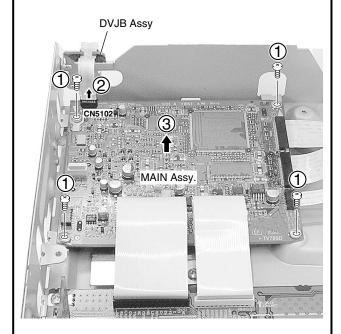


### **5** MAIN Assy

1 Remove the four screws.

3

- 2 Disconnect the flexible cable.
- 3 Stand the MAIN Assy.



Note: This photograph may show a different model.

However, the method for disassembly is the same.

1

104

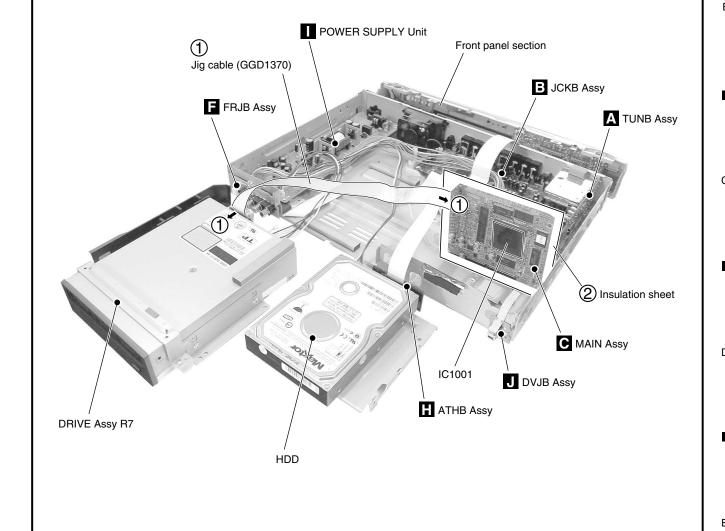


- 1 Connect the jig cable.
- $\bigcirc$  Insert the insulation sheet between the MAIN Assy and base chassis.
- 3 Arrange the unit as shown in the photo below.

#### Caution:

Main IC (IC1001) on the MAIN Assy generate heat to arround 80 degrees.

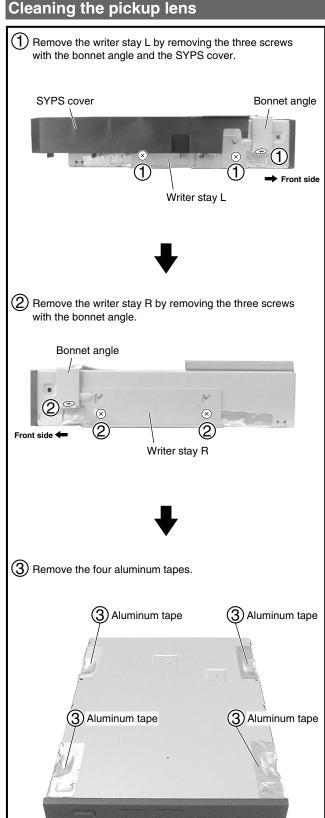
Be careful when works.

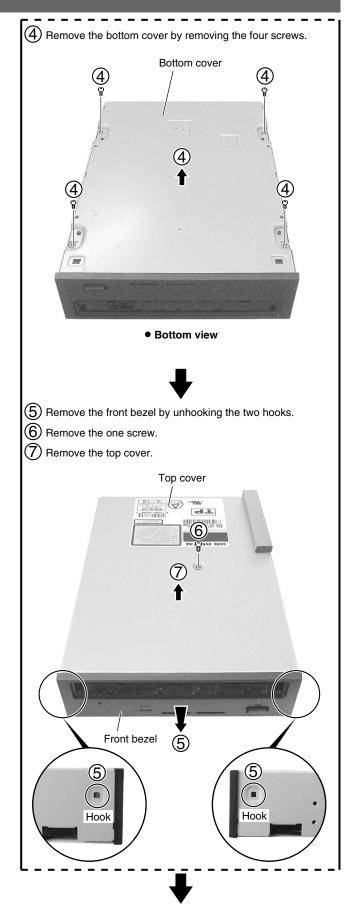


Diagnosis

105







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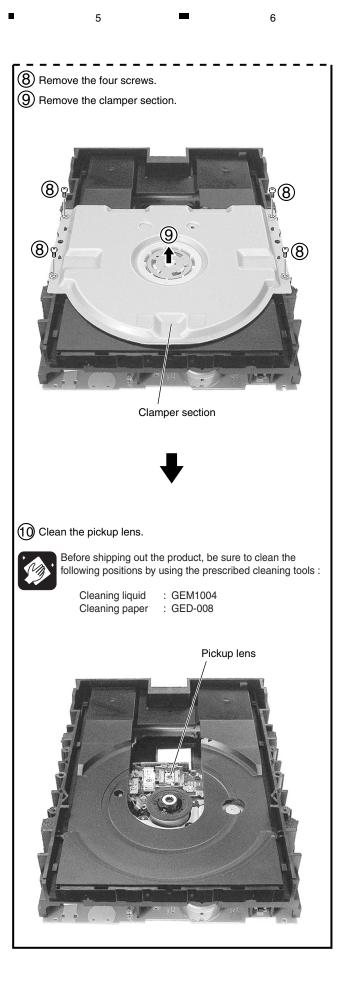
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DVR-520H-S

Bottom view



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DVR-520H-S

3

### List of IC

 ${\tt PEG034B, RS5C372A, LC75342M, LA73033M, AK5357VT, PST3428U, PST3809U, M65672WG-C, UPD72852AGB-8EU, UPD72893AGD-LML}$ 

• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

## ■ PEG034B (JCKB ASSY : IC202)

• TUFL Microcomputer

#### ● Pin Function

No.	Pin Name	Signal Name	I/O	Function	Active
1	P95/ANEX0/CLK4	FLCLK	0	FL Driver communication line CLK	_
2	P94/DA1/TB4in	SYNC	ı	C-Sync of input video	1
3	P93/DA0/TB3in	AVLINKIN	ı	Input line of NexTViewLink	_
4	P92/TB2in/Sout3	IR	ı	Pulse input of remote control	_
5	P91/TB1in/Sin3	J_CLOCK	ı		
6	P90/TB0in/CLK3	SYNCAFT	I	C-Sync of input video	1
7	BYTE	BYTE	ı		
8	CNVss	PGM	ı	Communication line	
9	P87/XCin	NC	(O)		_
10	P86/XCout	NC	(O)		_
11	-RESET	XRESETIN	ı	u-Con Reset	
12	Xout	XOUT	ı		
13	Vss	GND	_		
14	Xin	XIN	I		
15	Vcc	VCC	-		
16	P85/-NMI	NMI	ı		<u> </u>
17	P84/-INT2	NC	ı		
18	P83/-INT1	SLICEONFB	ı	Feedback from SLICEON pin	↑?
19	P82/-INT0	XINTRA	ı	Alarm/interval interruption	<u> </u>
20	P81/TA4in	LED HDD	0	for HDD Model	Н
21	P80/TA4out	LED DVD	0	for HDD Model	$\uparrow \downarrow$
22	P77/TA3in	NC	(O)		-
23	P76/TA3out	FANPWM	0	FAN power control	Н
24	P75/TA2in	JOGB	I	Phase VOL input	$\uparrow \downarrow$
25	P74/TA2out	NC	(O)		-
26	P73/-CTS2/-RTS2/TA1in	IICRST	0		
27	P72/CLK2/TA1out	AVLINKOUT	0		
28	P71/RxD2/SCL/TA0in/TB5in	SCL	I/O	I2C communication (clock)	_
29	P70/TxD2/SDA/TA0out	SDA	I/O	I2C communication (data)	_
30	Vss2	GND	_		
31	LP2	LP2	0		
32	LP3	LP3	0		
33	LP4	LP4	0		
34	Vdd2	VDD2	-		
35	M2	M2	ı	Mode switch	
36	M1	M1	ı		
37	P11/SLICEON	SLICEON	0	Slicer operating signal	H?
38	P67/TxD1	TXD	0	Communication line for firmware download/monitor	_
39	P66/RxD1	RXD	ı	Communication line for firmware download/monitor	_
40	P65/CLK1	SCLK	(O)	Communication line for firmware download/monitor	_

108

DVR-520H-S

No.	Pin Name	Signal Name	I/O	Function	Active
41	P64/-CTS1/-RTS1/CLKS1	BUSY	0	Communication line for firmware download/monitor	_
42	P63/TxD0	SSTTOM	0	SYS controller communication line (Tuner $\rightarrow$ Main)	_
43	P62/RxD0	SSMTOT	I	SYS controller communication line (Main $\rightarrow$ Tuner)	_
44	P61/CLK0	SCK	ı	SYS controller communication line (clock)	1
45	P60/-CTS0/-RTS0	HSTTOM	0	Tuner $\rightarrow$ SYS handshake	L
46	P57/-RDY/CLKout	DLCONT	0	Voltage supply SW of FLASH-ROM writing	L
47	P56/ALE	WRT	0	Write signal	Н
48	P55/-HOLD	SDAEEP	I/O	SDA line for EEPROM	_
49	P54/-HLDA	SCLEEP	0	SCL line for EEPROM	_
50	P53/BCLK	VOLCE	0	Communication line CE	Н
51	P52/-RD	VOLDATA	0	Communication line DATA	_
52	P51/-WRH/-BHE	VOLCLK	0	Communication line CLK	_
53	P50/-WRL/-WR	DLCE	I	Signal for serial I/O mode selection	_
54	P47/-CS3	S1	0		
55	P46/-CS2	LET	0	Letterbox signal add	Н
56	P45/-CS1	SQU	0	Squeeze signal add	_
57	P44/-CS0	BLANK	ı		
58	P43/A19	XTHROU	0		
59	P42/A18	NC	(O)		_
60	P41/A17	NC	(O)		_
61	P40/A16	SWVION	0	Independent source SW for video I/O output circuit	Н
62	P37/A15	SWSTBY	0	Standby mode of video input selector	Н
63	P36/A14	NC	(O)		
64	P35/A13	NC	(I)		
65	P34/A12	SCTHRU	0		
66	P33/A11	NC	(I)		
67	P32/A10	SDET3	ı	S terminal detection of Video input 3	L
68	P31/A9	SDET2	ı	S terminal detection of Video input 2	L
69	Vcc	VCC	_		
70	P30/A8	SDET1	1	S terminal detection of Video input 1	L
71	Vss	GND	_		
72	P27/A7	NC	(O)		_
73	P26/A6	NC	(O)		_
74	P25/A5	NC	(O)		_
75	P24/A4	NC	(O)		_
76	P23/A3	P_SAVEBS	0		
77	P22/A2	FOMO	0		_
	1	1			

**■** F

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DVR-520H-S

System Power ON

5

M1ONTA

P\_CONT

NC

0

0

(O)

78 P21/A1

P20/A0

80 P17/D15/-INT5

1 2 3 4

No.	Pin Name	Signal Name	I/O	Function	Active
81	P16/D14/-INT4	HSMTOT	ı	SYS → Tuner handshake	<b>\</b>
82	P15/D13/-INT3	DCTRI	ı		
83	P14/D12	MUTE	0	MUTE control	Н
84	P13/D11	SU/SAPID	ı	SAP detection	Н
85	P12/D10	ST/STID	ı	Stereo detection (STID)	Н
86	P11/D9	XRESET	0	System Reset output	L
87	P10/D8	LDASH	0		
88	P07/D7	STBYQ	0		
89	P06/D6	LM/	0		
90	P05/D5	I/BG	0		
91	P04/D4	XP_SAVE	0		
92	P03/D3	TUON	0	Tuner power	Н
93	P02/D2	YCSW	0		
94	P01/D1	RSTCTL	0	Reset signal mask from the system controller	L
95	P00/D0	FLPON	0	FL Driver Power ON	Н
96	P107/AN7/-KI3	MODEL1	A/D IN	Input for destination judgment	-
97	P106/AN6/-KI2	MODEL2	A/D IN	Input for destination judgment	-
98	P105/AN5/-KI1	AGC	A/D IN	Field intensity detection	-
99	P104/AN4/-KI0	FUNC	A/D IN		
100	P103/AN3	KEY2	A/D IN	Main unit key input	-
101	P102/AN2	KEY1	A/D IN	Main unit key input	-
102	P101/AN1	NC	_		
103	Avss	GND	_		
104	P100/AN0	AFT	A/D IN	AFT voltage input	-
105	VREF	VREF	_		
106	AVcc	AVCC	_		
107	P97/-ADTRG/Sin4	FLSTB	0	Communication line strobe of FL driver	L
108	Vdd1	VDD1	_		
109	SYNCIN	SYNCTEXT	I	Video input for sync. sep.	
110	SVREF	SLICE	I	Slice level input	
111	Vss1	GND	_		
112	Vdd3	VDD3	_		
113	CVIN1	CVIN1	I	Video input for teletext	
114	Vss3	GND	_		
115	FSCIN	FSCIN	I	Fsc input	
116	P96/ANEX1/Sout4	FLDATA	0	Communication line data of FL driver	-

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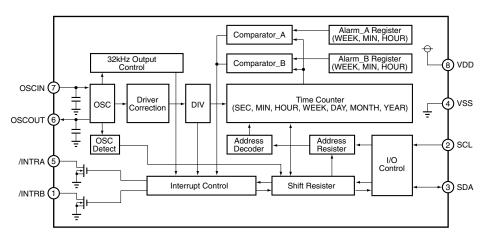
110

## ■ RS5C372A (JCKB ASSY : IC203)

• Real Time Clock IC

5

## • Block Diagram



### Pin Function

5

No.	Pin Name	I/O		Function								
1	/INTRB	0		in 32768Hz crystal use), cycled interrupt for CPU, or output alarm interrupt ut 32.768kHz when activated power from 0V.								
2	SCL	I	Shift clock input Synchronize with this clock Exceed VDD, and can input	k, and input and output data from a SDA terminal. ut to 6V.								
3	SDA	I/O		Serial input and output Synchronize with SCL, and input and output writing data or readout data. Exceed VDD, and can input to 6V. Nch open drain output in the output.								
4	VSS	_	Ground pin									
5	/INTRA	0		or output alarm interruption (ALARM_A, ALARM_B). state when activated power from 0V. N ch open drain output.								
6	OSCOUT	0	Oscillation circuit output Connect a crystal resonator of 32.768kHz or 32.000kHz between OSCIN and									
7	OSCIN	ı	Oscillation circuit input	OSCOUT, and constitute oscillation circuit. (component parts of oscillation circuit except crystal resonator have it built-in.)								
8	VDD	-	Positive supply input									

111

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## ■ LC75342M (JCKB ASSY : IC601)

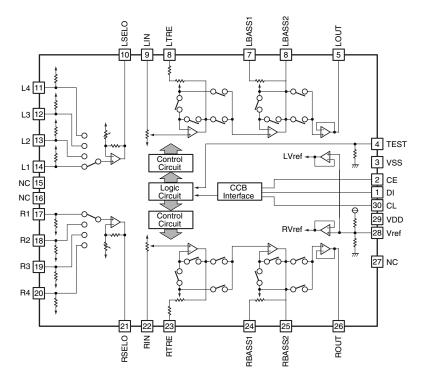
2

• Electric Volume IC

## Block Diagram

В

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3

### Pin Function

No.	Pin Name	Function	No.	Pin Name	Function
1	DI	Serial data input for control	16	NC	Not connected
2	CE	Chip enable pin Data are written in the internal latch by a timing of "H" $\rightarrow$ "L", and each analog switch works. Data transfer is enabled by "H" level.	17	R1	Jacob signal pie
3	VSS	Ground pin	18	R2	Input signal pin
4	TEST	Pin for electronic volume test Set to VSS electric potential.	19	R3	
5	LOUT	Volume and equalizer output pin	20	R4	
6	LBASS2	Capacitor and resistor connection pins for bus	21	RSELO	Input selector output pin
7	LBASS1	bandpass filter	22	RIN	Volume and equalizer input pin
8	LTRE	Capacitor connection pin for treble bandpass filter	23	RTRE	Capacitor connection pin for treble bandpass filter
9	LIN	Volume and equalizer input pin	24	RBASS1	Capacitor and resistor connection pins for bus
10	LSELO	Input selector output pin	25	RBASS2	bandpass filter
11	L4		26	ROUT	Volume and equalizer output pin
12	L3		27	NC	Not connected
13	L2	Input signal pins		Vref	0.5XVDD voltage generation block
14	L1		29	VDD	Power supply pin
15	NC	Not connected	30	CL	Clock input pin for control

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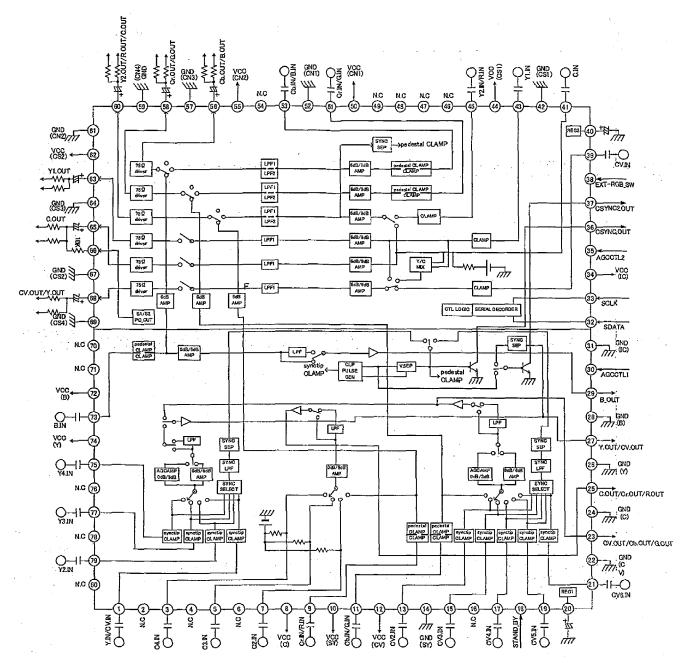
112

## ■ LA73033M (JCKB ASSY : IC701)

Video selector and Video driver

5

## Block Diagram



113

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8

В

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## ■ AK5357VT (MAIN ASSY : IC3101)

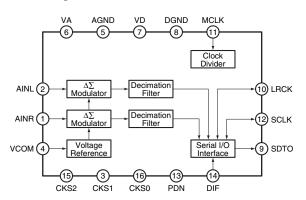
• 96kHz 24 bit  $\Delta\Sigma$  ADC

### ♠ Pin Arrangement (Top view)

#### AINR [ CKS0 CKS2 AINL [ 15 DIF CKS1 ∨сом [ 13 PDN AGND [ SCLK VA 🗌 6 11 MCLK LRCK VD [ SDTO DGNS [

## Block Diagram

3



## Pin Function

В

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	No.	Pin Name	I/O	Function
	1	AINR	ı	R ch analog input
	2	AINL	ı	L ch analog input
	3	CKS1	ı	Mode select 1
	4	VCOM	0	Common voltage output, bias voltage of VA/2 and ADC input
	5	AGND	-	Analog ground
Ī	6	VA	-	Analog power supply, 2.7V to 5.5V
	7	VD	-	Digital power supply, 2.7 to 5.5V (fs = 4k to 48kHz), 3.0 to 5.5V (fs = 48k to 96kHz)
Ī	8	DGND	-	Digital ground
	9	SDTO	0	Audio serial data output, outputs "L" in the power down mode.
	10	LRCK	I/O	Channel clock I/O, outputs "L" by master mode in the power down mode.
	11	MCLK	ı	Master clock input
	12	SCLK	I/O	Audio serial data clock, outputs "L" by master mode in the power down mode.
	13	PDN	I	Power down mode "H": power up, "L": power down
	14	DIF	ı	Audio interface format, "H": 24 bit I2S compatibility, "L": 24 bit MSB justify
Ī	15	CKS2	ı	Mode select 2
Ī	16	CKS0	ı	Mode select 0

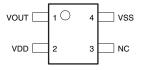
114

## ■ PST3428U (MAIN ASSY: IC4003)

• Reset IC

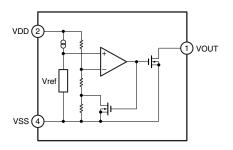
## • Pin Arrangement (Top view)

5



## ● Block Diagram

7

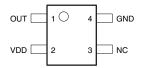


### Pin Function

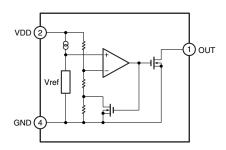
No.	Pin Name	Function
1	VOUT	Reset signal output
2	VDD	Power supply / voltage detection
3	NC	Not connected
4	VSS	vss

# PST3809U (MAIN ASSY : IC4005) • Reset IC

### • Pin Arrangement (Top view)



## • Block Diagram



### Pin Function

5

No.	Pin Name	Function
1	OUT	Reset signal output
2	VDD	Power supply / voltage detection
3	NC	Not connected
4	GND	Ground

115

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DVR-520H-S

■ M65673WG-A (MAIN ASSY : IC1001)
• Signal Processing IC for DVD Recorder

## • Pin Arrangement (Top view)

_	Α	В	C D	Е	F	G	Н	J	K	L	М	Ν	Р	R	Т	U	٧	W	Υ	ΑA	ΑB	AC	AD	ΑE	AF /	AG AH	<u>_</u>
٦ . ١	_	<u> </u>		· _	$\widehat{}$	_	$\widehat{}$	_	_	$\overline{}$	_	$\overline{}$		_			<u> </u>	$\overline{}$	_	_	$\overline{}$	_	_	_	<u> </u>		ገ
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9	0	$\bigcirc$	OC	) (	$\bigcirc$																	$\bigcirc$	$\bigcirc$	Ō	0	00	)
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14	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
15	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
16	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
17	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$						$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
18	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$																	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
19	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$																	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
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22	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$																	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
23	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
24	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
25	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
26	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
27	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
28	$\bigcirc$	$\bigcirc$	$\circ$	$) \bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$	)
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## • I/O buffer list

Buffer Name	Main Function	Remarks
PDIDGZ	Input buffer (5V tolerant)	
PDUDGZ	Input buffer (5V tolerant), pull-up	
PDDDGZ	Input buffer (5V tolerant), pull-down	
PDO04CDG	Output buffer, 4mA	
PDO08CDG	Output buffer, 8mA	
PDO0204DGZ	Output buffer, 2/4mA	
PDO0406DSGZ	Output buffer, 4/6mA	For SDRAM IF
PDO0406DSGZ×2	Output buffer, 8/12mA	For SDRAM IF
PDT0204DGZ	3 state output buffer, 2/4mA	
PDB04DGZ	Bidirectional buffer, 4mA	
PDB08DGZ	Bidirectional buffer, 8mA	
PDB0204DGZ	Bidirectional buffer, 2/4mA	
PDB0406DSGZ	Bidirectional buffer, 4/6mA	For SDRAM IF

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| VDD | 1.2V Power supply | VDS | 3.3V Power supply | GND |

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	28	27	56	22	54	23	22	7	20	6	8	17	16	12	4	3	72	Ξ	9	တ	ω	/	9	2	4	က	N	_	
Ą	NDD	REC6560[3]	AVDD2DA10	AVSS2DA10	AVDD 1DA10	DVREQ	DVVIDEO[1]	PLL2AVSS	DVVIDEO[7]	REC656[2]	REC656[7]	TRST	PLLON	restwod[2]	restwod(3)	HADRS[26]	HADRS[22]	HADRS[21]	HOLKEN	HCLKO	HDATA[9]	HDATA[10]	HDATA[13]	HDATA[14]	HADRS[14]	HADRS[30]	HCS[1]	NC	AH
AG	HKEYPLS	REC6560(1) F	RECesson2] A	YOUT	ROUT A	DVACK	DWIDEO[0]	DWIDEO[4]	DVVCLKI	RECe56[1]	REC 656 [6]	PLLRST	TMS	TESTMOD[1] T	TESTMOD[4] T	HADRS[25]	HAD RS[23]	HADRS[20]	HRAS	DQMWS[1]	HDATA[8]	HDATA11]	HDATA[12]	HDATA[15]	HADRS[13]	HCS[2]	HCS[4]	NC	AG
ΑF	WM1DTI[3]	PEDCTL	ACCCTL	REC6560[5]	IREF[1]	BOUT	REC6560[6]	D/V/IDEO(3)	PLL2AVDD	REC656[0]	REC656[4]	DVVCLKO	тро	теѕтмор[0]	TESTMOD(5)	HADRS[24]	HADRS[18]	HADRS[17]	HCAS	DOMWS[0]	HDATA[4]	HDATA[3]	HDATA[0]	HADRS[12]	HCS[0]	HCS[3]	HOE	HDREQ[1]	ΑF
ΑE	WM1DT[[1]	WM1DT[4]	WM1DT[[7]	AGOCTL	1000	AVDD1DA10	AVSS1DA10	REC6560[7]	ADMCLKI	DVVIDEO[5]	REC656[3]	PXCLK	IQL	CSYNC	NDD	HADRS[29]	HADRS[19]	HADRS[16]	HDCS[0]	HDWE	HDATA[5]	HDATA[2]	HDATA[1]	HADRS[11]	NDD	нряеаю	HDACK[1]	[0]XL	AE
AD	WM1DTO[4]	WM1DTO[6]	WM1DTI[2]	WM1DTI[6]	RECesector	RE06560[4]	IREF[0]	GOUT	DWIDEO[2]	P656CLKI	DWIDEO[6]	RECesel(5)	TCK	VIPWM	TESTMOD[6]	HAD RS[28]	HAD RS[27]	HAD RS[15]	HDCS[1]	HDATA[7]	HDATA[6]	HADRS[9]	HAD RS[10]	HCS[5]	HDACK[0]	TX[1]	RX[1]	RX[0]	AD
Q	WWCLKO	WM1DTQ[1]	WM1DTQ[5]	WM1DTEG	WM1DTI[5]	dav	VDD3	QQA	GND	VDD3	dav	GND	VDD3	QQA	NDD 1	VDD3	GND	QQA	VDD3	GND	VDD	VDD3	VDD	HWAIT	TX[2]	RX[2]	RTS[2]	RTS[1]	AC AC
AB	ADD	WM2DTO[6]	WM1DTO[0]	WM1DTO[2]	—	VDD3		!	!	!				!									VDD3	TX[3]	RX[3]	CTS[0]	RTS[3]	CTS[3]	AB
ΑĄ	WW2DTO[2]	WM2DTQ[4]	WM2DTO[3] WM1DTO[0]	WN2DTO[7]	WM1DTO[3] WM2DTC[1]	GND																	GND	RTS[0]	CTS[1]	SCLK[0]	SCLK[1]	[O]LNI	ΑA
>	PACKETEN	STREAM	SYNC	WWZDTO(0)	WW2DTQ(4)	QQA																	VDD	CTS[2]	INT[2]	INT[3]	NT[4]	[S]INI	>
>	AVDDAD 10	VBGR10	NBC10	TSRW	WM2DTO[1]	VDD2																	VDD3	INT[1]	INT[6]	DADRS[4]	DADRS[2]	DADRS[3]	≯
>	VRM10 A	CVBSIN	VRT10	AVSSAD10	TSCLK	GND																	GND	[2]LNI	DADRS[6]	DADRS[5]	DADRS[1]	DADRS[0]	>
⊃	AVD DWD8	DVSSAD10	VRBD10	VRB10 A	VRTD10	QQA						GND	GND	GND	GND	GND	GND						ADD	DADRS[8]	DADRS[7] [	DADRS[10] [	DBS[1] [	i lolsad	<b>&gt;</b>
<b>-</b>	VRB8	VRT8 [	CIN	AVSSAD8	DVDDAD10	VDD2						GND	GND	GND	GND	GND	GND						VDD3	DADRS[9]	DADRS[11]	SOO	DRAS	DCAS	⊢
Œ	BG8	NIS	CRIN	AVDDAD8	AVSSAD8 [	GND						GND	GND	GND	GND	GND	GND						GND	DCLKO	DDQM[1]	DWE	DDQM[0]	DDATA[7]	Œ
۵	AVD DWD8	AVSSAD8	DVSSAD8	DVDDAD8 /	EDATA(15) /	NDD						GND	GND	GND	GND	GND	GND						NDD	DDATA[8]	DDATA[9]	DDATA[10]	DDATA[6]	DDATA[5]	<u> </u>
z	EDATA(0)	EDATA(1)	EDATA[2]	EDATA[13] [	EDATA[14] E	VDD3						GND	GND	GND	GND	GND	GND						VDD3	DDATA[11]	DDATA[12]	DDATA[13] [	DDATA(3)	DDATA(4)	z
Σ	EDATA[3]	EDATA[4]	EDMTA[5]	EDATA[11] E	EDATA[12] E	GND						GND	GND	GND	GND	GND	GND						GND	SPIDATAO	DDATA[14] [	DDATA(15) C	DDATA[2]	DDATA[1]	Σ
_	EDATA[6]	EDATA[7]	EDATA[8]	EDATA[9] E	EDATA[10] E	ddv								<u> </u>									NDD	SCIDATA[0] S	SPICS	SPIDATAI	SPICLK	DDATA[0]	_
¥	EDOM E	EWE	ECAS	ECLKEN	ECIKO	VDD3																	VDD3	S [0]HOVVOA	SCIDATA[1]	adv (	SCICLK[1]	SCICLK[0] [	ㅗ
_	ERAS	ECS	EADRS[11]	EADRS[8] E	EADRS[9]	GND																	VDD	DVDADT[4] DV	DVDAREQ S	DVDAACK	SCICS[1] 9	scics[0] s	_
I	EBS[0]	EBS[1]	EADRS[10] E	EADRS(6) E	EADRS[7] E	ddv																	GND	вско р	DVDADT[3] D	/DADT[1] D	-		I
G	EADRS[0]	EADRS[1]	EADRS[2] E.	EADRS[4] E	EADRS[5] E	VDD3																	VDD3	ACMOD[0]	LRCKO D	DVDADT[6] DVDADT[1]	DVDADT[6] DVDADT[0]	DVDADT[2] DVDAADF[1]	ڻ ت
ш	EDATA[16] E	EDATA[17] E	EDATA[30] E	EDATA[31] E	EDATA[3] E	GND	VDD3	NDD	GND	VDD3	QQA	GND	VDD3	NDD	GND	VDD3	VDD	GND	VDD2	VDD	GND	VDD3	VDD	SPDIFI	ACMOD[1]	ADATAI D	DVDADT[7] DV	ADATAO D	ш
ш	EDATA[18] EI	EDATA[19] EI	EDATA[29] EI	EDATA28] EI		AT1DATA[9]	AT1DATA4]	DMARG	1ADR[2]		AT2DATA[9]	AT2DATA[3]	AT 2DM ACK	AT2ADR[1]	ADD	PLL1AVDD	DVAMCLKI	ARDATA[0]	ARDATA[2]	ARDATA[5]	ARDATA[7]	ARCAS	ARCS[1]		SHCDATAO	DVBCK	BCKI	IRCKI /	ш
Ω	EDATA[20] EI	EDATA[21] EE		EDATA[27] EI	DATA[10] AT1	IDATA[3] AT	AT1RESET AT	AT1ADR[0] AT11ORDY AT1DMARQ	DATA[14] AT	DATA[10] AT2	EDATA[5] AT	AT2DATA[0] AT	AT2DIOR AT:	AT2ADR[0] AT	TRACE	VMCLK PL	DVAMCLKO DV	ARDATA(1) AF	ARDATĄ(3) AF	ARDATĄ6] AF	ARDQM[0] AF	ARRAS /		ADRS[0] AR	SPICBCKI SP		SPDIFO	DVADATA	
O	EDATA[22] ED	EDATA[25] ED	DATA[15] ED	AT1DATA[14] ED	DATA[5] AT1	DATA(0) AT	AT1DIOR AT	ADR(0) AT	DATA[13] AT2	DATA(8) AT2	DATA(4) AT2	AT2RESET AT2	AT2IORDY AT	AT2CS[1] AT	DBI T	PLL3AVDD V	VDD DV/	AMCLK2 AR	ARDATA[4] AR	ARDATA(13) AR	ARWE AR	ARCS[0] A	DRS[13] ARV	ADRS[1] AR	ARADRS[2] SF	зноцяскі зноцяско	SRCBCKOS	DVLRCK DV	O
В	EDATA[23] EDA	EDATA[24] EDA	ATIDATA[8] ATIDATA[13] ATIDATA[15] EDATA[26]	AT1DATA[6] AT1D	ATIDATA[1] ATIDATA[5] ATIDATA[10] ATIDATA[14]	AT 1DM ACK AT 1 DATA(0) AT 1 DATA(3)	AT1INTRQ AT	AT1CS[1] AT1.	AT2DATA[11] AT2DATA[12] AT1DATA[13] AT2DATA[14] AT1ADR[2]	AT2DATA[7] AT2DATA[8] AT2DATA[10] AT2DATA[15]	AT2DATA[2] AT2DATA[4] AT2DATA[5]	AT2DMARQ AT2	ATZINTRQ AT2	AT2CS[0] AT2	n dav	PLL3AVSS PLL	ADCCLKO \	AMCLK1 AM			ARDATA[9] A	ARDOM[1] AR	ARADRS[12] ARADRS[13] ARADRS[14]	ARADRS[1] ARADRS[1] ARADRS[0] ARADRS[10]	ARADRS(3) ARA	ARADRS(6) SRC	ARADRS(4) SRC	SPICDATAI DV	В
⋖	VDD ED	AT1DATA[12] EDA	DATA[8] AT 11	AT 1DATA[7] AT1	AT 1DATA[2] AT1	AT1DIOW AT1	AT1ADR[1] AT1	AT1CS[0] AT-	अस्याः। ब्राय	AT2DATA[6] AT2	AT2DATA[1] AT2	AT2DIOW AT2I	AT2ADR[2] AT2	AT 2M ODE AT	RESET	PC0 PLL	PLL1AVSS ADI	DACCLKO AN	ARDATA[14] ARDATA[15]	ARDATA[11] ARDATA[12]	ARDATA[10] ARI	ARDATĄ(8) ARI	ARCLKO ARA	ARADRS[11] ARA	ARADRS[8] ARA	ARADRS[7] ARA	ARADRS[5] ARA	VDD SR	⋖
		_	_	25 ™1	_	-	_	2	20 ATZE	19 M2	18 MZ	17 ATZ	16 ATZ	15 Mg			12 Pu	11 DM	10 ARD	O B	8 WE	7 ARI		5 ARA	4 <sub>ARA</sub>	O ARA	N ABA		

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DVR-520H-S

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• Pin Function

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AG07688	● Pi	n Function	on							
2	No.		Pin Name	I/O	Function	No.		Pin Name	I/O	Function
3 NOD	1	VDD3	VDD3	_	3.3V I/O power supply	56	V26	VRT10	-	TOP side reference voltage
A H282	2	GND	GND	_	Ground	57	V28	VRM10	_	Common voltage
5	3	VDD	VDD	_	1.2V LOGIC power supply	58	U25	VRB10	-	Bottom side reference voltage
6	4	AH28	VDD	_	1.2V LOGIC power supply	59	U26	VRBD10	I/O	Analog test bus (for debugging)
	5	AF26	ACCCTL	0		60	U27	DVSSAD10	-	ADC part digital ground
8   AB26   WM.1DTI(7]   VO WMVWM, Bidirectional buffer   64   U28   AVDDAD8     1.2V LOGIC powers   11   AC25   WM.1DTI(9]   VO WMVWM, Bidirectional buffer   65   T25   AVSSAD8     VIDEO-Analog   T27   VIDEO-Analog   VIDEO-An	6	AF27	PEDCTL	0	VIDEO-Analog, Output buffer	61	T24	DVDDAD10	-	ADC part digital power supply (3.3V)
9	7	AG28	HKEYPLS	0	VIDEO-Analog, Output buffer	62	GND	GND	-	Ground
10   AD25	8	GND	GND	-	Ground	63	VDD	VDD	-	1.2V LOGIC power supply
11   AC24   WM.DTI[6]	9	AE26	WM1DTI[7]	I/O	WM/VWM, Bidirectional buffer	64	U28	AVDDAD8	-	
12	10	AD25	WM1DTI[6]	I/O	WM/VWM, Bidirectional buffer	65	T25	AVSSAD8	_	
13   AP28	11	AC24	WM1DTI[5]	I/O	WM/VWM, Bidirectional buffer	66	T26	CIN	- 1	VIDEO-Analog
14   AD26	12	AE27	WM1DTI[4]	I/O	WM/VWM, Bidirectional buffer	67	T27	VRT8	-	VIDEO-Analog
15	13	AF28	WM1DTI[3]	I/O	WM/VWM, Bidirectional buffer	68	T28	VRB8	-	VIDEO-Analog
16	14	AD26	WM1DTI[2]	I/O	WM/VWM, Bidirectional buffer	69	R25	AVDDAD8	-	
17	15	AE28	WM1DTI[1]	I/O	WM/VWM, Bidirectional buffer	70	R24	AVSSAD8	_	
18	16	AC25	WM1DTI[0]	I/O	WM/VWM, Bidirectional buffer	71	R26	CRIN	1	VIDEO-Analog
19   GND   GND	17	AB24	WM1DTO[7]	I/O	WM/VWM, Bidirectional buffer	72	R28	BG8	-	VIDEO-Analog
20	18	VDD	VDD	-	1.2V LOGIC power supply	73	P28	AVDDAD8	-	
AC26	19	GND	GND	_		74	P27	AVSSAD8	_	
AC26	20	AD27	WM1DTO[6]	1/0	WM/VWM, Bidirectional buffer	75	R27	GIN	ı	VIDEO-Analog
22         AD28         WM1DTO[4]         I/O         WM/VWM, Bidirectional buffer         77         P25         DVDDAD8         —           23         AA24         WM1DTO[2]         I/O         WM/VWM, Bidirectional buffer         78         BND         GND         —         Ground           24         AB25         WM1DTO[2]         I/O         WM/VWM, Bidirectional buffer         79         P24         EDATA[15]         I/O         SDRAM ENC, Bidires           25         VDD         VDD         —         1.2V LOGIC power supply         80         VDD3         VDD3         —         3.3V I/O power supply           26         AC27         WM1DTO[1]         I/O         MM/VWM, Bidirectional buffer         81         N28         EDATA[0]         I/O         SDRAM ENC, Bidires           28         AC28         WMCLKO         O         WMVWM, Output buffer         83         N26         EDATA[2]         I/O         SDRAM ENC, Bidires           29         VDD3         VD3         —         3.3V I/O power supply         84         VDD         VDD         —         1.2V LOGIC power supply           30         AB26         WM1DTO[6]         WMVWM, Output buffer         85         N25         EDATA[3] <td< td=""><td></td><td></td><td></td><td></td><td>·</td><td></td><td></td><td>DVSSAD8</td><td>_</td><td></td></td<>					·			DVSSAD8	_	
23					·	<b>-</b>			_	
24 AB25					,					Ground
25	_			1	,	1		-		SDRAM ENC, Bidirectional buffer
26         AC27         WM1DTO[1]         I/O         WM/VWM, Bidirectional buffer         81         N28         EDATA[0]         I/O         SDRAM ENC, Bidirectional buffer           27         GND         GND         -         Ground         82         N27         EDATA[1]         I/O         SDRAM ENC, Bidirectional buffer           28         AC28         WMCLKO         O         WM/WM, Output buffer         83         N26         EDATA[2]         I/O         SDRAM ENC, Bidirectional buffer           30         AB26         WM1DTO[0]         I/O         WM/WM, Output buffer         85         N25         EDATA[13]         I/O         SDRAM ENC, Bidirectional buffer           31         AA25         WM2DTO[6]         O         WM/WWM, Output buffer         86         GND         GND         -         Ground           32         AB27         WM2DTO[6]         O         WM/WM, Output buffer         87         N28         EDATA[3]         I/O         SDRAM ENC, Bidirectional buffer           33         AB28         VDD         -         1,2V LOGIC power supply         88         GND         GND         -         Ground           34         Y24         WM2DTO[6]         O         WM/WM, Output buffer         8	_				·					· ·
27 GND	_		t	<del>                                     </del>					_	SDRAM ENC, Bidirectional buffer
28         AC28         WMCLKO         O         WM/VWM, Output buffer         83         N26         EDATA[2]         I/O         SDRAM ENC, Bidired           29         VDD3         VDD3         -         3.3V I/O power supply         84         VDD         VDD         -         1.2V LOGIC power s           30         AB26         WM1DTO[0]         I/O         WM/VWM, Output buffer         86         GND         GND         -         GND         GND         -         GND         -         GND         -         GROID         -         CROID         WM2         WM2         TO         SDRAM ENC, Bidired         90 <t< td=""><td></td><td></td><td></td><td>1</td><td>·</td><td></td><td></td><td></td><td><del> </del></td><td>SDRAM ENC, Bidirectional buffer</td></t<>				1	·				<del> </del>	SDRAM ENC, Bidirectional buffer
29         VDD3         - 3.3V I/O power supply         84         VDD         VDD         - 1.2V LOGIC power's           30         AB26         WM1DTO[0]         I/O         WMVVWM, Bidirectional buffer         85         R25         EDATA[13]         I/O         SDRAM ENC, Bidired           31         AA25         WM2DTO[7]         O         WMVVWM, Output buffer         86         GND         GND         - Ground           32         AB27         WM2DTO[6]         O         WMVWM, Output buffer         87         M28         EDATA[3]         I/O         SDRAM ENC, Bidired           33         AB28         VDD         - 1.2V LOGIC power supply         88         MND         GND         - Ground           34         Y24         WM2DTO[5]         O         WMVVM, Output buffer         90         M27         EDATA[4]         I/O         SDRAM ENC, Bidired           36         AA26         WM2DTO[3]         O         WMVVWM, Output buffer         91         M26         EDATA[4]         I/O         SDRAM ENC, Bidired           37         AA28         WM2DTO[1]         O         WMVWM, Output buffer         93         M25         EDATA[1]         I/O         SDRAM ENC, Bidired           38 <t< td=""><td></td><td></td><td></td><td>+</td><td></td><td></td><td></td><td></td><td>1</td><td>SDRAM ENC, Bidirectional buffer</td></t<>				+					1	SDRAM ENC, Bidirectional buffer
30				1					<del>                                     </del>	·
31   AA25   WM2DTO[7]   O WM/VWM, Output buffer   86   GND   GND   - Ground			<del> </del>	+	,					SDRAM ENC, Bidirectional buffer
32         AB27         WM2DTO[6]         O         WM/VWM, Output buffer         87         M28         EDATA[3]         I/O         SDRAM ENC, Bidired           33         AB28         VDD         -         1.2V LOGIC power supply         88         GND         GND         -         Ground           34         Y24         WM2DTO[5]         O         WM/VWM, Output buffer         89         N24         EDATA[14]         I/O         SDRAM ENC, Bidired           35         AA27         WM2DTO[3]         O         WM/VWM, Output buffer         90         M27         EDATA[4]         I/O         SDRAM ENC, Bidired           36         AA26         WM2DTO[3]         O         WM/VMM, Output buffer         91         M26         EDATA[5]         I/O         SDRAM ENC, Bidired           37         AA28         WM2DTO[2]         O         WM/VWM, Output buffer         92         VDD3         -         3.3V I/O power suppl           38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[6]					·					· ·
33   AB28   VDD					·					
34         Y24         WM2DTO[5]         O         WM/VWM, Output buffer         89         N24         EDATA[14]         I/O         SDRAM ENC, Bidired           35         AA27         WM2DTO[4]         O         WM/VWM, Output buffer         90         M27         EDATA[4]         I/O         SDRAM ENC, Bidired           36         AA26         WM2DTO[3]         O         WM/VWM, Output buffer         91         M26         EDATA[5]         I/O         SDRAM ENC, Bidired           37         AA28         WM2DTO[2]         O         WM/VWM, Output buffer         92         VDD3         VDD3         -         3.3V I/O power suppl           38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[11]         I/O         SDRAM ENC, Bidired           39         GND         GND         -         Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[6]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD	_				, I					· ·
35         AA27         WM2DTO[4]         O         WM/VWM, Output buffer         90         M27         EDATA[4]         I/O         SDRAM ENC, Bidired           36         AA26         WM2DTO[3]         O         WM/VWM, Output buffer         91         M26         EDATA[5]         I/O         SDRAM ENC, Bidired           37         AA28         WM2DTO[2]         O         WM/VWM, Output buffer         92         VDD3         -         3.3V I/O power suppl           38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[11]         I/O         SDRAM ENC, Bidired           39         GND         GND         -         Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[1]         I/O	_		t	+		1				
36         AA26         WM2DTO[3]         O         WM/VWM, Output buffer         91         M26         EDATA[5]         I/O         SDRAM ENC, Bidired           37         AA28         WM2DTO[2]         O         WM/VWM, Output buffer         92         VDD3         VDD3         -         3.3V I/O power supply           38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[11]         I/O         SDRAM ENC, Bidired           39         GND         GND         -         Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidired           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND	<u> </u>								1	
37         AA28         WM2DTO[2]         O         WM/VWM, Output buffer         92         VDD3         - 3.3V I/O power supply           38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[11]         I/O         SDRAM ENC, Bidired           39         GND         GND         -         Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidired           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         -         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bid									<del> </del>	· · · · · · · · · · · · · · · · · · ·
38         W24         WM2DTO[1]         O         WM/VWM, Output buffer         93         M25         EDATA[11]         I/O         SDRAM ENC, Bidired           39         GND         GND         -         Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidired           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         -         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidired           45         VDD3         VDD3         -         3.3V IO power supply         100         GND         GND         - <td></td> <td></td> <td></td> <td>+</td> <td></td> <td></td> <td></td> <td></td> <td>1/0</td> <td>,</td>				+					1/0	,
39         GND         GND         - Ground         94         L28         EDATA[6]         I/O         SDRAM ENC, Bidired           40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidired           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         -         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidired           45         VDD3         VDD3         -         3.3V IO power supply         100         GND         -         Ground           46         W25         TSRW         O         TS OUT,Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidired <tr< td=""><td></td><td></td><td></td><td></td><td>·</td><td></td><td></td><td></td><td>-</td><td></td></tr<>					·				-	
40         Y25         WM2DTO[0]         O         WM/VWM, Output buffer         95         L27         EDATA[7]         I/O         SDRAM ENC, Bidired           41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power s           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidired           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         -         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidired           45         VDD3         VDD3         -         3.3V IO power supply         100         GND         GND         -         Ground           46         W25         TSRW         O         TS OUT, Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Ditput           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM EN					·	<b>-</b>				,
41         GND         GND         -         Ground         96         VDD         VDD         -         1.2V LOGIC power's           42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidirectional buffer           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         -         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidirectional buffer           45         VDD3         VDD3         -         3.3V IO power supply         100         GND         GND         -         Ground           46         W25         TSRW         O         TS OUT, Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidirect           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Output           48         V24         TSCLK         O         TS OUT, Output buffer         103         K27         EWE         O										SDRAM ENC, Bidirectional buffer
42         Y26         SYNC         I/O         TS OUT, Bidirectional buffer         97         M24         EDATA[12]         I/O         SDRAM ENC, Bidirectional buffer           43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         — Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidirectional buffer           45         VDD3         VDD3         — 3.3V IO power supply         100         GND         GND         — Ground           46         W25         TSRW         O         TS OUT, Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidirectional buffer           47         GND         GND         — Ground         102         K28         EDATA[9]         I/O         SDRAM ENC, Bidirectional buffer           47         GND         GND         — Ground         102         K28         EDQM         O         SDRAM ENC, Output           48         V24         TSCLK         O         TS OUT, Output buffer         103         K27         EWE         O         SDRAM ENC, Output					·					SDRAM ENC, Bidirectional buffer
43         Y27         STREAM         I/O         TS OUT, Bidirectional buffer         98         GND         GND         —         Ground           44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidirectional buffer           45         VDD3         VDD3         —         3.3V IO power supply         100         GND         GND         —         Ground           46         W25         TSRW         O         TS OUT, Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidirectional buffer           47         GND         GND         —         Ground         102         K28         EDQM         O         SDRAM ENC, Bidirectional buffer           47         GND         GND         —         Ground         102         K28         EDQM         O         SDRAM ENC, Output           47         GND         GND         —         Ground         102         K28         EDQM         O         SDRAM ENC, Output           48         V24         TSCLK         O         TS OUT, Output buffer         103         K27         EWE         O         SDRAM ENC			t	+		1			1	
44         Y28         PACKETEN         I/O         TS OUT, Bidirectional buffer         99         L26         EDATA[8]         I/O         SDRAM ENC, Bidirectional buffer           45         VDD3         -         3.3V IO power supply         100         GND         GND         -         Ground           46         W25         TSRW         O         TS OUT, Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidirectional buffer           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Bidirectional buffer           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Bidirectional buffer           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Output           48         V24         TSCLK         O         TS OUT, Output buffer         103         K27         EWE         O         SDRAM ENC, Output           49         VDD3         VDD3         -         3.3V I/O power supply         104         VDD3         VD3         -         3.3V I/O power										SDRAM ENC, Bidirectional buffer
45         VDD3         VDD3         -         3.3V IO power supply         100         GND         GND         -         Ground           46         W25         TSRW         O         TS OUT,Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidired           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Outpu           48         V24         TSCLK         O         TS OUT,Output buffer         103         K27         EWE         O         SDRAM ENC, Outpu           49         VDD3         VDD3         -         3.3V I/O power supply         104         VDD3         VDD3         -         3.3V I/O power supply           50         W26         NBC10         -         Bias current adjustment terminal         105         K26         ECAS         O         SDRAM ENC, Outpu           51         W27         VBGR10         I/O         Analog test bus (for debugging)         106         L24         EDATA[10]         I/O         SDRAM ENC, Bidired           52         W28         AVDDAD10         -         ADC part analog power supply (3.3V)         107         K25         ECLKEN <t< td=""><td></td><td></td><td></td><td>1</td><td></td><td><b>-</b></td><td></td><td></td><td><del> </del></td><td></td></t<>				1		<b>-</b>			<del> </del>	
46         W25         TSRW         O         TS OUT,Output buffer         101         L25         EDATA[9]         I/O         SDRAM ENC, Bidired           47         GND         GND         -         Ground         102         K28         EDQM         O         SDRAM ENC, Outpu           48         V24         TSCLK         O         TS OUT,Output buffer         103         K27         EWE         O         SDRAM ENC, Outpu           49         VDD3         VDD3         -         3.3V I/O power supply         104         VDD3         VDD3         -         3.3V I/O power supply           50         W26         NBC10         -         Bias current adjustment terminal         105         K26         ECAS         O         SDRAM ENC, Outpu           51         W27         VBGR10         I/O         Analog test bus (for debugging)         106         L24         EDATA[10]         I/O         SDRAM ENC, Bidired           52         W28         AVDDAD10         -         ADC part analog power supply (3.3V)         107         K25         ECLKEN         O         Output buffer, 4/6mA           53         V25         AVSSAD10         -         ADC part analog Ground         108         VDD				1/0	·				1/0	SDRAM ENC, Bidirectional buffer
47         GND         GND         —         Ground         102         K28         EDQM         O         SDRAM ENC, Output           48         V24         TSCLK         O         TS OUT,Output buffer         103         K27         EWE         O         SDRAM ENC, Output           49         VDD3         VDD3         —         3.3V I/O power supply         104         VDD3         VDD3         —         3.3V I/O power supply           50         W26         NBC10         —         Bias current adjustment terminal         105         K26         ECAS         O         SDRAM ENC, Output           51         W27         VBGR10         I/O         Analog test bus (for debugging)         106         L24         EDATA[10]         I/O         SDRAM ENC, Ditput           52         W28         AVDDAD10         —         ADC part analog power supply (3.3V)         107         K25         ECLKEN         O         Output buffer, 4/6mA           53         V25         AVSSAD10         —         ADC part analog Ground         108         VDD         VDD         —         1.2V LOGIC power s           54         V27         CVBSIN         I         Analog Input         109         J28         ERAS									-	
48         V24         TSCLK         O         TS OUT,Output buffer         103         K27         EWE         O         SDRAM ENC, Output         49         VDD3         VDD3         -         3.3V I/O power supply         104         VDD3         VDD3         -         3.3V I/O power supply         50         WDD3         -         3.3V I/O power supply         50         WDD4         WDD4         SDRAM ENC, Output         50			<b>+</b>	0	·	101				SDRAM ENC, Bidirectional buffer
49         VDD3         VDD3         -         3.3V I/O power supply         104         VDD3         -         3.3V I/O power supply           50         W26         NBC10         -         Bias current adjustment terminal         105         K26         ECAS         O         SDRAM ENC, Outpu           51         W27         VBGR10         I/O         Analog test bus (for debugging)         106         L24         EDATA[10]         I/O         SDRAM ENC, Bidired           52         W28         AVDDAD10         -         ADC part analog power supply (3.3V)         107         K25         ECLKEN         O         Output buffer, 4/6mA           53         V25         AVSSAD10         -         ADC part analog Ground         108         VDD         VDD         -         1.2V LOGIC power s           54         V27         CVBSIN         I         Analog Input         109         J28         ERAS         O         SDRAM ENC, Output						1				SDRAM ENC, Output buffer
50 W26 NBC10 - Bias current adjustment terminal 105 K26 ECAS O SDRAM ENC, Output 51 W27 VBGR10 I/O Analog test bus (for debugging) 106 L24 EDATA[10] I/O SDRAM ENC, Bidired 52 W28 AVDDAD10 - ADC part analog power supply (3.3V) 107 K25 ECLKEN O Output buffer, 4/6mA 53 V25 AVSSAD10 - ADC part analog Ground 108 VDD VDD - 1.2V LOGIC power s 54 V27 CVBSIN I Analog Input 109 J28 ERAS O SDRAM ENC, Output	48	V24	<b>+</b>	0	TS OUT,Output buffer	103	K27		0	SDRAM ENC, Output buffer
51         W27         VBGR10         I/O         Analog test bus (for debugging)         106         L24         EDATA[10]         I/O         SDRAM ENC, Bidired           52         W28         AVDDAD10         -         ADC part analog power supply (3.3V)         107         K25         ECLKEN         O         Output buffer, 4/6mA           53         V25         AVSSAD10         -         ADC part analog Ground         108         VDD         VDD         -         1.2V LOGIC power s           54         V27         CVBSIN         I         Analog Input         109         J28         ERAS         O         SDRAM ENC, Output				<u> </u>		1			1	3.3V I/O power supply
52         W28         AVDDAD10         -         ADC part analog power supply (3.3V)         107         K25         ECLKEN         O         Output buffer, 4/6mA           53         V25         AVSSAD10         -         ADC part analog Ground         108         VDD         VDD         -         1.2V LOGIC power s           54         V27         CVBSIN         I         Analog Input         109         J28         ERAS         O         SDRAM ENC, Output	50	W26	NBC10	-	-	105	K26	ECAS	0	SDRAM ENC, Output buffer
53         V25         AVSSAD10         -         ADC part analog Ground         108         VDD         VDD         -         1.2V LOGIC power s           54         V27         CVBSIN         I         Analog Input         109         J28         ERAS         O         SDRAM ENC, Output	51	W27	VBGR10	I/O	Analog test bus (for debugging)	106	L24	EDATA[10]	I/O	SDRAM ENC, Bidirectional buffer
54 V27 CVBSIN I Analog Input 109 J28 ERAS O SDRAM ENC, Output	52	W28	AVDDAD10	-	ADC part analog power supply (3.3V)	107	K25	ECLKEN	0	Output buffer, 4/6mA
	53	V25	AVSSAD10	<u> </u>	ADC part analog Ground	108	VDD	VDD	-	1.2V LOGIC power supply
■ 1 I I I I I I	54	V27	CVBSIN	I	Analog Input	109	J28	ERAS	0	SDRAM ENC, Output buffer
55         U24         VRTD10         -         Input common bias         110         GND         GND         -         Ground	55	U24	VRTD10	_	Input common bias	110	GND	GND	_	Ground

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No.	BALL Address	Pin Name	1/0	Function	No.	BALL Address	Pin Name	1/0	Function
111	J27	ECS	0	SDRAM ENC, Output buffer	166	VDD3	VDD3	_	3.3V I/O power supply
112	GND	GND	_	Ground	167	C25	AT1DATA[11]	I/O	ATAPI-DVD, Bidirectional buffer
113	J26	EADRS[11]	0	SDRAM ENC, Output buffer	168	D24	AT1DATA[10]	1/0	ATAPI-DVD, Bidirectional buffer
114	J25	EADRS[8]	0	SDRAM ENC, Output buffer	169	E23	AT1DATA[9]	I/O	ATAPI-DVD, Bidirectional buffer
115	GND	GND	_			Ground			
116	K24	ECLKO	0	SDRAM ENC, Output buffer	171	A26	AT1DATA[8]	I/O	ATAPI-DVD, Bidirectional buffer
117	VDD3	VDD3	_	3.3V I/O power supply	172	A25	AT1DATA[7]	I/O	ATAPI-DVD, Bidirectional buffer
118	H28	EBS[0]	0	SDRAM ENC, Output buffer	173	B25	AT1DATA[6]	I/O	ATAPI-DVD, Bidirectional buffer
119	H27	EBS[1]	0	SDRAM ENC, Output buffer	174	GND	GND	_	Ground
120	H26	EADRS[10]	0	SDRAM ENC, Output buffer	175	C24	AT1DATA[5]	1/0	ATAPI-DVD, Bidirectional buffer
121	VDD	VDD	_	1.2V LOGIC power supply	176	VDD3	VDD3	_	3.3V I/O power supply
122	G28	EADRS[0]	0	SDRAM ENC, Output buffer	177	E22	AT1DATA[4]	1/0	ATAPI-DVD, Bidirectional buffer
123	GND	GND	_	Ground	178	VDD	VDD	_	1.2V LOGIC power supply
124	J24	EADRS[9]	0	SDRAM ENC, Output buffer	179	D23	AT1DATA[3]	I/O	ATAPI-DVD, Bidirectional buffer
125	GND	GND		Ground	180	A24	AT1DATA[2]	I/O	ATAPI-DVD, Bidirectional buffer
126	G27	EADRS[1]	0	SDRAM ENC, Output buffer	181	B24	AT1DATA[1]	1/0	ATAPI-DVD, Bidirectional buffer
127	H25	EADRS[6]	0	SDRAM ENC, Output buffer	182	GND	GND	-	Ground
128	G26	EADRS[2]	0	SDRAM ENC, Output buffer	183	C23	AT1DATA[0]	1/0	ATAPI-DVD, Bidirectional buffer
129	VDD3	VDD3	_	3.3V I/O power supply	184	D22	AT1RESET	0	Output buffer,8mA
130	F27	EDATA[17]	I/O	SDRAM ENC, Bidirectional buffer	185	E21	AT1DMARQ	ī	ATAPI-DVD, Input buffer
131	F28	EDATA[17]	1/0	SDRAM ENC, Bidirectional buffer	186	GND	GND	-	Ground
132	H24	EADRS[7]	0	SDRAM ENC, Output buffer	187	B23	AT1DMACK	0	ATAPI-DVD, Output buffer
133	VDD	VDD	_	1.2V LOGIC power supply	188	VDD3	VDD3	_	
134	G25		0		189	A23	AT1DIOW	0	3.3V I/O power supply
135	GND	EADRS[4]		SDRAM ENC, Output buffer Ground	190	VDD	VDD		ATAPI-DVD, Output buffer
	F26	-	- 1/0	SDRAM ENC, Bidirectional buffer		C22		0	1.2V LOGIC power supply
136	<u> </u>	EDATA[30]	I/O	·	191		AT1DIOR		ATAPI-DVD, Output buffer
137	GND E27	GND	-  /O	Ground	192	D21	AT1IORDY		ATAPI DVD, Input buffer
138	E28	EDATA[19]	1/0	SDRAM ENC, Bidirectional buffer	193	B22	AT1INTRQ GND	I	ATAPI-DVD, Input buffer Ground
139		EDATA[18]		SDRAM ENC, Bidirectional buffer SDRAM ENC, Bidirectional buffer	194	GND	-	-	
140	F25 VDD3	EDATA[31]	I/O	· ·	195 196	E20 A22	AT1ADR[2]	0	ATAPI-DVD, Output buffer
141		VDD3	-	3.3V I/O power supply			AT1ADR[1]		ATAPI-DVD, Output buffer
142	E26	EDATA[29]	1/0	SDRAM ENC, Bidirectional buffer	197	C21	AT1ADR[0]	0	ATAPI-DVD, Output buffer
143	G24	EADRS[5]	0	SDRAM ENC, Output buffer	198	GND	GND	-	Ground
144	D28	EDATA[20]	I/O	SDRAM ENC, Bidirectional buffer	199	B21	AT1CS[1]	0	ATAPI-DVD, Output buffer
145	VDD	VDD	-	1.2V LOGIC power supply	200	VDD3	VDD3	-	3.3V I/O power supply
146	D27	EDATA[21]	I/O	SDRAM ENC, Bidirectional buffer	201	A21	AT1CS[0]	0	ATAPI-DVD, Output buffer
147	GND	GND	-	Ground	202	VDD	VDD	-	1.2V LOGIC power supply
148	C28	EDATA[22]	I/O	SDRAM ENC, Bidirectional buffer	203	E19	AT2DATA[15]	1/0	ATAPI-HDD, Bidirectional buffer
149	GND	GND	-	Ground	204	D20	AT2DATA[14]	1/0	ATAPI-HDD, Bidirectional buffer
150	F24	EADRS[3]	0	SDRAM ENC, Output buffer	205	C20	AT2DATA[13]	1/0	ATAPI-HDD, Bidirectional buffer
151	E25	EDATA[28]	1/0	SDRAM ENC, Bidirectional buffer	206	GND	GND	-	Ground
152		EDATA[26]	I/O	SDRAM ENC, Bidirectional buffer	207	B20	AT2DATA[12]	1/0	ATAPI-HDD, Bidirectional buffer
153	VDD3	VDD3	-	3.3V I/O power supply	208	A20	AT2DATA[11]	I/O	ATAPI-HDD, Bidirectional buffer
154	B28	EDATA[23]	1/0	SDRAM ENC, Bidirectional buffer	209	D19	AT2DATA[10]	1/0	ATAPI-HDD, Bidirectional buffer
155	C27	EDATA[25]	I/O	SDRAM ENC, Bidirectional buffer	210	GND	GND	-	Ground
156	B27	EDATA24]	I/O	SDRAM ENC, Bidirectional buffer	211	E18	AT2DATA[9]	1/0	ATAPI-HDD, idirectional buffer
157	VDD	VDD	-	1.2V LOGIC power supply	212	VDD3	VDD3	-	3.3V I/O power supply
158	D25	EDATA[27]	I/O	SDRAM ENC, Bidirectional buffer	213	C19	AT2DATA[8]	I/O	ATAPI-HDD, Bidirectional buffer
159	GND	GND	-	Ground	214	VDD	VDD	-	1.2V LOGIC power supply
160	C26	AT1DATA[15]	I/O	ATAPI-DVD, Bidirectional buffer	_	B19	AT2DATA[7]	I/O	ATAPI-HDD, Bidirectional buffer
161	E24	AT1DATA[14]	I/O	ATAPI-DVD, Bidirectional buffer	216		AT2DATA[6]	I/O	ATAPI-HDD, Bidirectional buffer
162	GND	GND	-	Ground	217	D18	AT2DATA[5]	I/O	ATAPI-HDD, Bidirectional buffer
163	A28	VDD	-	1.2V LOGIC power supply	218	GND	GND	_	Ground
164	B26	AT1DATA[13]	I/O	ATAPI-DVD, Bidirectional buffer	219	C18	AT2DATA[4]	I/O	ATAPI-HDD, Bidirectional buffer

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DVR-520H-S 119

I/O ATAPI-HDD, Bidirectional buffer

AT2DATA[3]

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AT1DATA[12]

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I/O ATAPI-DVD, Bidirectional buffer

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No.	BALL Address	Pin Name	I/O	Function	No.	BALL Address	Pin Name	1/0	Function
221	B18	AT2DATA[2]	I/O	ATAPI-HDD, Bidirectional buffer	276	VDD3	VDD3	-	3.3V I/O power supply
222	GND	GND	-	Ground	277	C11	AMCLK2	ı	CLOCK, Input buffer
223	A18	AT2DATA[1]	I/O	ATAPI-HDD, Bidirectional buffer	278	GND	GND	-	Ground
224	VDD3	VDD3	_	3.3V I/O power supply	279	D11	ARDATA[1]	I/O	SDRAM-ATAPI, Bidirectional buffer
225	D17	AT2DATA[0]	I/O	ATAPI-HDD, Bidirectional buffer	280	VDD3	VDD3	-	3.3V I/O power supply
226	VDD	VDD	_	1.2V LOGIC power supply	281	A10	ARDATA[14]	I/O	SDRAM-ATAPI, Bidirectional buffer
227	C17	AT2RESET	I/O	ATAPI-HDD, Bidirectional buffer	282	VDD	VDD	-	1.2V LOGIC power supply
228	B17	AT2DMARQ	1	ATAPI-HDD, Input buffer	283	B10	ARDATA[15]	I/O	SDRAM-ATAPI, Bidirectional buffe
229	E16	AT2DMACK	0	ATAPI-HDD, Output buffer	284	E11	ARDATA[0]	I/O	SDRAM-ATAPI, Bidirectional buffer
230	GND	GND	_	Ground	285	C10	ARDATA[4]	I/O	SDRAM-ATAPI, Bidirectional buffer
231	A17	AT2DIOW	0	ATAPI-HDD, Output buffer	286	GND	GND	_	Ground
232	D16	AT2DIOR	0	ATAPI-HDD, Output buffer	287	D10	ARDATA[3]	I/O	SDRAM-ATAPI, Bidirectional buffer
233	C16	AT2IORDY		ATAPI-HDD, Input buffer	288	A9	ARDATA[11]	1/0	SDRAM-ATAPI, Bidirectional buffer
234	GND	GND	_	Ground	289	B9	ARDATA[12]	I/O	SDRAM-ATAPI, Bidirectional buffer
235	B16	AT2INTRQ	1	ATAPI-HDD, Input buffer	290	GND	GND	_	Ground
236	VDD3	VDD3	<u> </u>	3.3V I/O power supply	291	C9	ARDATA[13]	I/O	SDRAM-ATAPI, Bidirectional buffer
237	A16	AT2ADR[2]	I/O	ATAPI-HDD, Bidirectional buffer	292	E10	ARDATA[2]	1/0	SDRAM-ATAPI, Bidirectional buffer
238	VDD	VDD	-	1.2V LOGIC power supply	293	D9	ARDATA[6]	1/0	SDRAM-ATAPI, Bidirectional buffer
239	E15	AT2ADR[1]	I/O	ATAPI-HDD, Bidirectional buffer	294	VDD3	VDD3	-	3.3V I/O power supply
240	GND	GND	-	Ground	295	A8	ARDATA[10]	1/0	SDRAM-ATAPI, Bidirectional buffer
241	D15		I/O	ATAPI-HDD, Bidirectional buffer	296	B8	1	1/0	·
241	VDD	AT2ADR[0] VDD		· ·	297	C8	ARDATA[9]	0	SDRAM-ATARI, Bidirectional buffer
		<u> </u>	-	1.2V LOGIC power supply	1		ARWE		SDRAM-ATAPI, Output buffer
243	C15	AT2CS[1]	0	ATAPI-HDD, Output buffer	298	VDD	VDD	-	1.2V LOGIC power supply
244	GND	GND	-	Ground	299	A7	ARDATA[8]	1/0	SDRAM-ATAPI, Bidirectional buffe
245	B15	AT2CS[0]	0	ATAPI-HDD, Output buffer	300	E9	ARDATA[5]	1/0	SDRAM-ATAPI, Bidirectional buffe
246	VDD	VDD	-	1.2V LOGIC power supply	301	D8	ARDQM[0]	0	SDRAM-ATAPI, Output buffer
247	A15	AT2MODE	I	ATAPI-HDD, Input buffer	302	GND	GND	-	Ground
248	GND	GND	-	Ground	303	B7	ARDQM[1]	0	SDRAM-ATAPI, Output buffer
249	GND	GND	-	Ground	304	C7	ARCS[0]	0	SDRAM-ATAPI, Output buffer
250	A14	RESET		Input buffer (5V tolerant)	305	VDD3	VDD3	-	3.3V I/O power supply
251	VDD3	VDD3	-	3.3V I/O power supply	306	A6	ARCLKO	0	SDRAM-ATAPI, Output buffer
252	B14	VDD	-	1.2V LOGIC power supply	307	GND	GND	-	Ground
253	C14	DBI	I	TEST, Input buffer	308	B6	ARADRS[12]	0	SDRAM-ATAPI, Output buffer
254	GND	GND	_	Ground	309	E8	ARDATA[7]	I/O	SDRAM-ATAPI, Bidirectional buffe
255	D14	TRACE	ı	TEST, Input buffer	310	D7	ARRAS	0	SDRAM-ATAPI, Output buffer
256	E14	VDD	_	1.2V LOGIC power supply	311	VDD3	VDD3	-	3.3V I/O power supply
257	A13	PCO	0	CLOCK, 3 state output buffer	312	A5	ARADRS[11]	0	SDRAM-ATAPI, Output buffer
258	GND	GND	-	Ground	313	C6	ARADRS[13]	0	SDRAM-ATAPI, Output buffer
259	B13	PLL3AVSS	-		314	B5	ARADRS[9]	0	SDRAM-ATAPI, Output buffer
260	C13	PLL3AVDD	_		315	VDD	VDD	-	1.2V LOGIC power supply
261	D13	VMCLK	1	CLOCK, Input buffer	316	E7	ARCAS	0	SDRAM-ATAPI, Output buffer
262	E13	PLL1AVDD	_		317	D6	ARADRS[14]	0	SDRAM-ATAPI, Output buffer
263	A12	PLL1AVSS	_		318	C5	ARADRS[1]	0	SDRAM-ATAPI, Output buffer
264	VDD3	VDD3	_	3.3V I/O power supply	319	GND	GND	_	Ground
265	B12	ADCCLKO	0	CLOCK, Output buffer	320	B4	ARADRS[3]	0	SDRAM-ATAPI, Output buffer
266	GND	GND	_	Ground	321	A4	ARADRS[8]	0	SDRAM-ATAPI, Output buffer
267	C12	VDD	_	1.2V LOGIC power supply	322	A3	ARADRS[7]	0	SDRAM-ATAPI, Output buffer
268	VDD3	VDD3	<u> </u>	3.3V I/O power supply	323	GND	GND	_	Ground
269	D12	DVAMCLKO	0	CLOCK, Output buffer	324	E6	ARCS[1]	0	SDRAM-ATAPI, Output buffer
270	GND	GND	-	Ground	325	D5	ARADRS[0]	0	SDRAM-ATAPI, Output buffer
271	A11	DACCLKO	0	CLOCK, Output buffer	326	C4	ARADRS[2]	0	SDRAM-ATAPI, Output buffer
272	VDD3	VDD3	-	3.3V I/O power supply	327	VDD3	VDD3	-	3.3V I/O power supply
					1				
273	E12	DVAMCLKI		AUDIO CLOCK, Input buffer	328	A2	ARADRS[5]	0	SDRAM ATARI, Output buffer
274	GND	GND	-	Ground	329	B3	ARADRS[6]	0	SDRAM-ATABL Output buffer
275	B11	AMCLK1	I	CLOCK, Input buffer	330	B2	ARADRS[4]	0	SDRAM-ATAPI, Output buffer

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120 DVR-520H-S
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No.	BALL Address	Pin Name	1/0	Function	No.	BALL Address	Pin Name	I/O	Function
331	GND	GND	_	Ground	386	VDD	VDD	_	1.2V LOGIC power supply
332	E5	ARADRS[10]	0	SDRAM-ATAPI, Output buffer	387	GND	GND	_	Ground
333	D4	SRCBCKI	ı	AUDIO, Input buffer	388	L3	SPIDATAI	I/O	HOST, Bidirectional buffer
334	VDD3	VDD3	_	3.3V I/O power supply	389	VDD	VDD	_	1.2V LOGIC power supply
335	C3	SRCLRCKI	ı	AUDIO, Input buffer			HOST, Bidirectional buffer		
336	B1	SRCDATAI	ı	AUDIO, Input buffer	391	GND	GND	-	Ground
337	A1	VDD	_	1.2V LOGIC power supply	392	L2	SPICLK	1/0	HOST, Bidirectional buffer
338	GND	GND	_	Ground	393	GND	GND	_	Ground
339	C2	SRCBCKO	0	AUDIO, Output buffer	394	L1	DDATA[0]	I/O	SDRAM-DEC. Bidirectional buffer
340	VDD	VDD	_	1.2V LOGIC power supply	395	VDD3	VDD3	_	3.3V I/O power supply
341	D3	SRCLRCKO	0	AUDIO, Output buffer	396	M4	DDATA[14]	1/0	SDRAM-DEC, Bidirectional buffer
342	E4	SRCDATAO	0	AUDIO, Output buffer	397	M3	DDATA[15]	1/0	SDRAM-DEC, Bidirectional buffer
343	F5	SPDIFI	i	AUDIO, Input buffer	398	M2	DDATA[2]	1/0	SDRAM-DEC, Bidirectional buffer
344	D2	SPDIFO	0	AUDIO, Output buffer	399	VDD	VDD		1.2V LOGIC power supply
345	C1	DVLRCK	1/0	AUDIO, Bidirectional buffer	400	N5	DDATA[11]	I/O	SDRAM-DEC, Bidirectional buffer
346	E3	DVBCK	1/0	AUDIO, Bidirectional buffer	401	GND	GND	-	Ground
347	D1	DVADATA	I/O	AUDIO, Bidirectional buffer	402	M1	DDATA[1]	I/O	SDRAM-DEC, Bidirectional buffer
348	F4	ACMOD[1]	ı	AUDIO, Input buffer	403	GND	GND	-	Ground
349	G5	ACMOD[0]	i	AUDIO, Input buffer	404	N4	DDATA[12]	1/0	SDRAM-DEC, Bidirectional buffer
350	E1	LRCKI	i	AUDIO, Input buffer	405	N3	DDATA[12]	1/0	SDRAM-DEC, Bidirectional buffer
351	E2	BCKI	i	AUDIO, Input buffer	406	N2	DDATA[13]	1/0	SDRAM-DEC, Bidirectional buffer
352	GND	GND	-	Ground	407	VDD3	VDD3	-	·
	F3	ADATAI			_	N1		1/0	3.3V I/O power supply
353			I	AUDIO, Input buffer	408		DDATA[4]		SDRAM DEC, Bidirectional buffer
354	GND	GND	-	Ground	409	P5	DDATA[8]	1/0	SDRAM DEC, Bidirectional buffer
355	G4	LRCKO	0	AUDIO, Output buffer	410	P4	DDATA[9]	I/O	SDRAM-DEC, Bidirectional buffer
356	VDD	VDD	-	1.2V LOGIC power supply	411	VDD	VDD	-	1.2V LOGIC power supply
357	H5	BCKO	0	AUDIO, Output buffer	412	P3	DDATA[10]	I/O	SDRAM-DEC, Bidirectional buffer
358	F1	ADATAO	0	DVD-AUDIO, Output buffer	413	GND	GND	-	Ground
359	F2	DVDADT[7]	0	DVD-AUDIO, Output buffer	414	P2	DDATA[6]	I/O	SDRAM-DEC, Bidirectional buffer
360	G2	DVDADT[6]	0	DVD-AUDIO, Output buffer	415	GND	GND	-	Ground
361	G3	DVDADT[5]	0	DVD-AUDIO, Output buffer	416	P1	DDATA[5]	I/O	SDRAM-DEC, Bidirectional buffer
362	J5	DVDADT[4]	0	DVD-AUDIO, Output buffer	417	R1	DDATA[7]	I/O	SDRAM-DEC, Bidirectional buffer
363	H4	DVDADT[3]	0	DVD-AUDIO, Output buffer	418	R2	DDQM[0]	0	SDRAM-DEC, Output buffer
364	G1	DVDADT[2]	0	DVD-AUDIO, Output buffer	419	VDD3	VDD3	-	3.3V I/O power supply
365	H3	DVDADT[1]	0	DVD-AUDIO, Output buffer	420	R3	DWE	0	SDRAM-DEC, Output buffer
366	H2	DVDADT[0]	0	DVD-AUDIO, Output buffer	421	VDD	VDD	-	1.2V LOGIC power supply
367		DVDAADR[1]	0	DVD-AUDIO, Output buffer		R4	DDQM[1]	0	SDRAM-DEC, Output buffer
368	K5	DVDAADR[0]	0	DVD-AUDIO, Output buffer	423	GND	GND	-	Ground
369	J4	DVDAREQ	I/O	DVD-AUDIO, Bidirectional buffer	424	R5	DCLKO	0	SDRAM-DEC, Output buffer
370	GND	GND	-	Ground	425	VDD3	VDD3	-	3.3V I/O power supply
371	J3	DVDAACK	0	DVD-AUDIO, Output buffer	426	VDD	VDD	-	1.2V LOGIC power supply
372	VDD	VDD	-	1.2V LOGIC power supply	427	T1	DCAS	0	SDRAM-DEC, Output buffer
373	J2	SCICS[1]	I/O	HOST, Bidirectional buffer	428	GND	GND	_	Ground
374	VDD3	VDD3	-	3.3V I/O power supply	429	T2	DRAS	_	SDRAM-DEC, Output buffer
375	J1	SCICS[0]	I/O	HOST, Bidirectional buffer	430	GND	GND	_	Ground
376	VDD	VDD	-	1.2V LOGIC power supply	431	T3	DCS	0	SDRAM-DEC, Output buffer
377	K4	SCIDATA[1]	I/O	HOST, Bidirectional buffer	432	T4	DADRS[11]	0	SDRAM-DEC, Output buffer
378	GND	GND	-	Ground	433	U1	DBS[0]	0	SDRAM-DEC, Output buffer
379	L5	SCIDATA[0]	I/O	HOST, Bidirectional buffer	434	VDD3	VDD3	_	3.3V I/O power supply
380	K3	VDD	-	1.2V LOGIC power supply	435	T5	DADRS[9]	0	SDRAM-DEC, Output buffer
381	K2	SCICLK[1]	I/O	HOST, Bidirectional buffer	436	U2	DBS[1]	0	SDRAM-DEC, Output buffer
382	GND	GND	-	Ground	437	U3	DADRS[10]	0	SDRAM-DEC, Output buffer
383	K1	SCICLK[0]	I/O	HOST, Bidirectional buffer	438	VDD	VDD	-	1.2V LOGIC power supply
		GND	_	Ground	439	U4	DADRS[7]	0	SDRAM-DEC, Output buffer
384	GND	GIND	_	around	700	<b>.</b>			ODI IAM DEO, Output buildi

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No.	BALL Address	Pin Name	I/O	Function	No.	BALL Address	Pin Name	1/0	Function
441	V1	DADRS[0]	0	SDRAM-DEC, Output buffer	496	VDD	VDD	-	1.2V LOGIC power supply
442	GND	GND	-	Ground	497	AD4	HDACK[0]	0	Output buffer, 4mA
443	V2	DADRS[1]	0	SDRAM-DEC, Output buffer	498	AF1	HDREQ[1]	ı	HOST, Input buffer
444	VDD	VDD	_	1.2V LOGIC power supply	499	AE3	HDREQ[0]	- 1	HOST, Input buffer
445	U5	DADRS[8]	0	SDRAM-DEC, Output buffer	500	AC5	HWAIT	- 1	HOST, Input buffer
446	GND	GND	-	Ground	501	AF2	HOE	0	HOST, Output buffer
447	V3	DADRS[5]	0	SDRAM-DEC, Output buffer	502	VDD3	VDD3	-	3.3V I/O power supply
448	VDD3	VDD3	_	3.3V I/O power supply	503	GND	GND	-	Ground
449	V4	DADRS[6]	0	SDRAM-DEC, Output buffer	504	AE4	VDD	-	1.2V LOGIC power supply
450	W1	DADRS[3]	0	SDRAM-DEC, Output buffer	505	AD5	HCS[5]	0	HOST, Output buffer
451	W2	DADRS[2]	0	SDRAM-DEC, Output buffer	506	AG2	HCS[4]	0	HOST, Output buffer
452	VDD	VDD	_	1.2V LOGIC power supply	507	AF3	HCS[3]	0	HOST, Output buffer
453	W3	DADRS[4]	0	SDRAM-DEC, Output buffer	508	AG3	HCS[2]	0	HOST, Output buffer
454	GND	GND	_	Ground	509	AH2	HCS[1]	0	HOST, Output buffer
455	GND	GND	_	Ground	510	GND	GND	_	Ground
456	GND	GND	_	Ground	511	AF4	HCS[0]	0	HOST, Output buffer
457	V5	INT[7]	I/O	HOST, Bidirectional buffer	512	VDD	VDD	-	1.2V LOGIC power supply
458	VDD	VDD	-	1.2V LOGIC power supply	513	AD6	HADRS[10]	I/O	HOST, Bidirectional buffer
459	W4	INT[6]	I/O	HOST, Bidirectional buffer	514	GND	GND	_	Ground
460	Y1	INT[5]	I/O	HOST, Bidirectional buffer	515	AE5	HADRS[11]	I/O	HOST, Bidirectional buffer
461	Y2	INT[4]	I/O	HOST, Bidirectional buffer	516	AG4	HADRS[13]	I/O	HOST, Bidirectional buffer
462	VDD3	VDD3	-	3.3V I/O power supply	517	AH3	HADRS[30]	1/0	HOST, Bidirectional buffer
463	Y3	INT[3]	I/O	HOST, Bidirectional buffer	518	VDD3	VDD3	-	3.3V I/O power supply
464	GND	GND	-	Ground	519	AF5	HADRS[12]	I/O	HOST, Bidirectional buffer
465	Y4	INT[2]	I/O	HOST, Bidirectional buffer	520	GND	GND	-	Ground
466	VDD	VDD	-	1.2V LOGIC power supply	521	AH4	HADRS[14]	I/O	HOST, Bidirectional buffer
467	W5	INT[1]	1/0	HOST, Bidirectional buffer	522	AE6		1/0	HOST, Bidirectional buffer
	AA1	INT[0]	1/0	HOST, Bidirectional buffer	523	AD7	HDATA[1]	1/0	HOST, Bidirectional buffer
468 469	AA2		1/0	·	523	VDD3	HADRS[9] VDD3	-	
	AA3	SCLK[1]		HOST, Bidirectional buffer	525	AG5			3.3V I/O power supply
470		SCLK[0]	1/0	HOST, Bidirectional buffer	+		HDATA[15] GND	I/O	HOST, Bidirectional buffer
471	AB1	CTS[3]	I/O	HOST, Bidirectional buffer	526	GND		-	Ground
472	GND	GND	-	Ground	527	AH5	HDATA[14]	I/O	HOST, Bidirectional buffer
473	Y5	CTS[2]	I/O	HOST, Bidirectional buffer	528	GND	GND	-	Ground
474	GND	GND	-	Ground	529	AF6	HDATA[0]	1/0	HOST, Bidirectional buffer
475	AA4	CTS[1]	I/O	HOST, Bidirectional buffer	530	AD8	HDATA[6]	1/0	HOST, Bidirectional buffer
476	VDD	VDD	-	1.2V LOGIC power supply	531	AE7	HDATA[2]	I/O	HOST, Bidirectional buffer
477	AB3	CTS[0]	1/0	HOST, Bidirectional buffer	532	VDD3	VDD3	-	3.3V I/O power supply
478	AB2	RTS[3]	1/0	HOST, Bidirectional buffer	533	AG6	HDATA[12]	I/O	HOST, Bidirectional buffer
479	AC2	RTS[2]	I/O	HOST, Bidirectional buffer	534	VDD	VDD	-	1.2V LOGIC power supply
480	AC1	RTS[1]	I/O	HOST, Bidirectional buffer	535	AH6	HDATA[13]	I/O	HOST, Bidirectional buffer
481	AA5	RTS[0]	I/O	HOST, Bidirectional buffer	536	AG7	HDATA[11]	I/O	HOST, Bidirectional buffer
482	VDD3	VDD3	<del>  -</del>	3.3V I/O power supply	537	AF7	HDATA[3]	1/0	HOST, Bidirectional buffer
483	AB4	RX[3]	I/O	HOST, Bidirectional buffer	538	GND	GND	-	Ground
484	GND	GND	<u> </u>	Ground	539	AE8	HDATA[5]	I/O	HOST, Bidirectional buffer
485	AC3	RX[2]	I/O	HOST, Bidirectional buffer	540	GND	GND	-	Ground
486	VDD	VDD		1.2V LOGIC power supply	541	AD9	HDATA[7]	I/O	HOST, Bidirectional buffer
487	AD2	RX[1]	I/O	HOST, Bidirectional buffer	542	AF8	HDATA[4]	I/O	HOST, Bidirectional buffer
488	AD1	RX[0]	I/O	HOST, Bidirectional buffer	543	AH7	HDATA[10]	I/O	HOST, Bidirectional buffer
489	AB5	TX[3]	I/O	HOST, Bidirectional buffer	544	VDD3	VDD3	_	3.3V I/O power supply
490	AC4	TX[2]	I/O	HOST, Bidirectional buffer	545	AG8	HDATA[8]	I/O	HOST, Bidirectional buffer
491	AD3	TX[1]	I/O	HOST, Bidirectional buffer	546	VDD	VDD	-	1.2V LOGIC power supply
492	GND	GND	_	Ground	547	AH8	HDATA[9]	I/O	HOST, Bidirectional buffer
493	AE1	TX[0]	I/O	HOST, Bidirectional buffer	548	AE9	HDWE	0	HOST, Output buffer
494	GND	GND	_	Ground	549	AF9	DQMWS[0]	0	HOST, Output buffer
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122 DVR-520H-S
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See Sun	No.	BALL Address	Pin Name	1/0	Function	No.	BALL Address	Pin Name	I/O	Function
553         AG9         DOMNYS[1]         O         HOST, Output buffer         BO6         AF16         TDO         O         TEST, Output buffer           555         VDD3         —         334 VIO power supply         699         VDD         —         1.24 VLOGIC power supply           556         AND         —         GROUND         —         Ground         611         VDD3         VDD3         —         3.24 VIO power supply           556         NDD         NDD3         VDD3         —         3.54 VIO power supply         613         AND         GND         —         Ground         1.24 VLOGIC power supply         613         AND         GND         —         Ground         1.25 VLOGIC power supply         613         AND         GND         —         Ground         1.25 VLOGIC power supply         615         MDD         GND         —         Ground         1.25 VLOGIC power supply         615         MDD         —         Ground         1.25 VLOGIC power supply         615         MDD         —         Ground         1.25 VLOGIC power supply         615         MDD         —         3.25 VIO power supply         615         MDD         —         3.25 VIO power supply         615         MDD         —         3.25 VIO power s	551	AD10	HDCS[1]	0	HOST, Output buffer	606	AG16	TMS	ı	TEST, nput buffer
See   NDGS	552	GND	GND	-	Ground	607	GND	GND	_	Ground
585 AR19         HCLKO         O         HOST, Output buffer         810         AE16         TDI         I         TEST, Input buffer           555 AND         GND         —         Ground         611         VDD3         VDD3         —         23 VIO power supply           556 VDD3         VDD3         —         33 VIO power supply         613         GND         —         Ground           556 VDD3         VDD3         —         33 VIO power supply         613         GND         —         Ground           560 VDD         VDD         —         12 V LOGIC power supply         615         AD16         TCK         ITEST, Input buffer           560 VDD         VDD         —         12 V LOGIC power supply         615         MOD         PLLRST         ICCOCK, Input buffer           562 AS10         HRAS         O         HOST, Output buffer         617         GND         GND         —         Ground           564 ROD         GND         GND         —         Ground         619         VDD3         —         33 VIO power supply           565 AS11         HARDRS[17]         VD         HOST, Bufferectional buffer         622         AB17         PXCLC         —         CCLOCK, Output buff	553	AG9	DQMWS[1]	0	HOST, Output buffer	608	AF16	TDO	0	TEST, Output buffer
Sept	554	VDD3	VDD3	-	3.3V I/O power supply	3.3V I/O power supply 609 VDD VDD –		_	1.2V LOGIC power supply	
Sept	555	AH9	HCLKO	0	HOST, Output buffer	610	AE16	TDI	ı	TEST, Input buffer
Sept	556	GND	GND	_	Ground	611	VDD3	VDD3	_	3.3V I/O power supply
558 VD03         VD03         - 3.3 V/D power supply         613 (ADL)         OND         - Onund           556 AD11         HADRS(15)         VO         HOST, Bidirectional buffer         614 AD16         TCK         I         TEST, Input buffer           560 V7D         VDD         - 1.2 VLOQIC power supply         615 VD03         VD03         - 3.3 V/D power supply           561 AF10         HCAS         O         HOST, Output buffer         616 AG17         FLLBST         I         CLOCK, Input buffer           562 AG10         HRAS         O         HOST, Output buffer         618 AG17         DVVCLKO         CLOCK, Output buffer           563 AH10         HCLKEN         O         HOST, Output buffer         619 VD03         VD03         - 3.3 VLO power supply           565 AB1         HADRS(17)         IVO HOST, Bidirectional buffer         620 AE17         PXCLK         O         CLOCK, Output buffer           566 AD12         HADRS(27)         IVO HOST, Bidirectional buffer         622 AH18         REC656(67)         I         VIDEO-Digital, Input buffer           567 AD12         HADRS(27)         IVO HOST, Bidirectional buffer         623 AD16         ARD REC656(7)         I         VIDEO-Digital, Input buffer           568 AD12         HADRS(28) <t< td=""><td>557</td><td>AE10</td><td>HDCS[0]</td><td>0</td><td>HOST. Output buffer</td><td>612</td><td>AH17</td><td>TRST</td><td>1</td><td>TEST, Input buffer</td></t<>	557	AE10	HDCS[0]	0	HOST. Output buffer	612	AH17	TRST	1	TEST, Input buffer
SSS   AD11	558	VDD3		_	3.3V I/O power supply	613		GND	_	Ground
VDD	559		HADRS[15]	I/O				тск		
BSB   AF10				_	1.2V LOGIC power supply	615	VDD3	VDD3	_	3.3V I/O power supply
686 Act 10         HRAS         O         INOST, Output buffer         617 GND         GND         — Ground         — Ground           656 AH10         HCLKEN         O         HOST, Output buffer         618 AF17         DVVCLKO         O         CLOCK, Output buffer           656 AH10         GROD         — Ground         619 VDD3         VDD3         — 3.3 VIO power supply           656 AE11         HADRS[16]         IO         HOST, Bidirectional buffer         622 AE17         PXCLK         O         CLOCK, Output buffer           656 AD12         HADRS[17]         IO         HOST, Bidirectional buffer         622 AH18         REC658([7]         1         VIDEO-Ogidal, Input buffer           656 AD12         HADRS[27]         IO         HOST, Bidirectional buffer         624 AG18         REC658([6]         I         VIDEO-Ogidal, Input buffer           657 AD13         HAH1         HADRS[17]         IO         HOST, Bidirectional buffer         624 AG18         REC658([6]         I         VIDEO-Ogidal, Input buffer           6570 VDD         VDD         —         1.2V LOGIC power supply         627 AF18         REC658([6]         I         VIDEO-Ogidal, Input buffer           6772 AF12         HADRS[18]9         IO         HOST, Bidirectional buffer				0		<del></del>		-		,
Box   Brit   B						_				
See   GND			_			<del>                                     </del>				
568         LETT         HADRS[16]         I/O         HOST, Bidirectional buffer         620         AE17         PXCLK         O         CLOCK, Output buffer           566         GND         GND         —         Ground         —         1.2V LOGIC power supply         628         AG11         HADRS[27]         I/O         HOST, Bidirectional buffer         628         AG18         REC656[6]         I         VIDEO-Digital, Input buffer         677         AF11         HADRS[21]         I/O         HOST, Bidirectional buffer         626         AD17         REC656[6]         I         VIDEO-Digital, Input buffer         677         AF12         HADRS[19]         I/O         HOST, Bidirectional buffer         628         AE18         REC656[6]         I         VIDEO-Digital, Input buffer         677         AF12         HADRS[18]         I/O         HOST, Bidirectional buffer         628         AE18         REC656[6]         I         VIDEO-Digital, Input buffer			-			<del>                                     </del>				
See   GND			_			_		-		
AF11					,					
6588 AD12         HADRS[27]         I/O         HOST, Bidirectional buffer         623         VDD         VDD         -         1.2V LOGIC power supply           6589 AG11         HADRS[20]         I/O         HOST, Bidirectional buffer         624         AG18         REC656[6]         I         I/IDEQ-Digital, Input buffer           670 VDD3         VDD         VDD         VDD         Ground         AG18         REC656[6]         I         VIDEQ-Digital, Input buffer           671 AH11         HADRS[21]         I/O         HOST, Bidirectional buffer         626         AD17         REC656[6]         I         VIDEQ-Digital, Input buffer           672 VDD         VDD         -         1.2V LOGIC power supply         627         AF18         REC656[6]         I         VIDEQ-Digital, Input buffer           673 AE12         HADRS[18]         I/O         HOST, Bidirectional buffer         629         AF18         REC656[6]         I         VIDEQ-Digital, Input buffer           674 AF12         HADRS[18]         I/O         HOST, Bidirectional buffer         630         AG19         REC656[6]         I         VIDEQ-Digital, Input buffer           675 GND         GND         -         Ground         GN1         AS19         REC656[6]         I		-	_			_		-		
AG11					,					, , ,
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AH11			1 1	1	,	<del></del>		<b></b>		
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AE12						<del>                                     </del>		1 1		
AF12	572		VDD		1.2V LOGIC power supply	627		REC656I[4]		VIDEO-Digital, Input buffer
AG12	573		HADRS[19]	I/O	HOST, Bidirectional buffer	628	AE18	REC656I[3]	1	VIDEO-Digital, Input buffer
GND   GND   GND   - Ground   G31   AF19   REC656[0]   I VIDEO-Digital, Input buffer	574	AF12	HADRS[18]	I/O	HOST, Bidirectional buffer	629	AH19	REC656I[2]	ı	VIDEO-Digital, Input buffer
STZ   AD13	575	AG12	HADRS[23]	I/O	HOST, Bidirectional buffer	630	AG19	REC656I[1]	ı	VIDEO-Digital, Input buffer
ND	576	GND	GND	-	Ground	631	AF19	REC656I[0]	ı	VIDEO-Digital, Input buffer
679         AH12         HADRS[22]         I/O         HOST, Bidirectional buffer         634         AE19         DVVIDEO[5]         I/O         VIDEO-Digital, Bidirectional buffer           680         GND         GROD         -         Ground         635         VDD3         VDD3         -         3.3V I/O power supply           681         AE13         HADRS[29]         I/O         HOST, Bidirectional buffer         636         AG20         DVVCLKI         I         CLOCK, Input buffer           682         VDD         VDD         -         1.2V LOGIC power supply         637         AF20         PLL2AVDD         -           684         VDD3         VDD3         -         3.3V I/O power supply         639         AD19         R656CLKI         I         CLOCK, Input buffer           684         VDD3         VDD3         -         3.3V I/O power supply         639         AD19         R656CLKI         I         CLOCK, Input buffer           6845         VDD3         VDD3         -         3.3V I/O power supply         639         AD19         R656CLKI         I         CLOCK, Input buffer           6845         AD3         VDD3         -         3.3V I/O power supply         639         AD14 <td< td=""><td>577</td><td>AD13</td><td>HADRS[28]</td><td>I/O</td><td>HOST, Bidirectional buffer</td><td>632</td><td>AH20</td><td>DVVIDEO[7]</td><td>I/O</td><td>VIDEO-Digital, Bidirectional buffer</td></td<>	577	AD13	HADRS[28]	I/O	HOST, Bidirectional buffer	632	AH20	DVVIDEO[7]	I/O	VIDEO-Digital, Bidirectional buffer
See   GND   GND   - Ground   GNS   VDD3   - 3.3V I/O power supply	578	VDD	VDD	-	1.2V LOGIC power supply	633	AD18	DVVIDEO[6]	I/O	VIDEO-Digital, Bidirectional buffer
AE13	579	AH12	HADRS[22]	I/O	HOST, Bidirectional buffer	634	AE19	DVVIDEO[5]	I/O	VIDEO-Digital, Bidirectional buffer
See   VDD   VDD   -   1.2V LOGIC power supply   637   AF20   PLL2AVDD   -	580	GND	GND	-	Ground	635	VDD3	VDD3	-	3.3V I/O power supply
Seas	581	AE13	HADRS[29]	I/O	HOST, Bidirectional buffer	636	AG20	DVVCLKI	- 1	CLOCK, Input buffer
No.	582	VDD	VDD	-	1.2V LOGIC power supply	637	AF20	PLL2AVDD	-	
685         AG13         HADRS[25]         I/O         HOST, Bidirectional buffer         640         GND         GND         — Ground           686         GND         GND         — Ground         641         AE20         ADMCLKI         I         CLOCKI, Input buffer           587         AH13         HADRS[26]         I/O         HOST, Bidirectional buffer         642         VDD3         VDD3         — 3.3V I/O power supply           588         GND         GND         — Ground         643         AG21         DVVIDEO[4]         I/O         VIDEO-Digital, Bidirectional buffer           589         GND         GND         — Ground         644         AF21         DVVIDEO[3]         I/O         VIDEO-Digital, Bidirectional buffer           590         AD14         TESTMOD[6]         I         TEST, Input buffer         645         AD20         DVVIDEO[2]         I/O         VIDEO-Digital, Bidirectional buffer           591         AE14         VDD         — 1.2V LOGIC power supply         646         AH22         DVVIDEO[3]         I/O         VIDEO-Digital, Bidirectional buffer           592         AF14         TESTMOD[4]         I         TEST, Input buffer         647         AG22         DVVIDEO[1]         I/O         VI	583	AF13	HADRS[24]	I/O	HOST, Bidirectional buffer	638	AH21	PLL2AVSS	_	
See   GND   GND   -   Ground   G41   AE20   ADMCLKI   I   CLOCKI, Input buffer   Se7   AH13   HADRS[26]   I/O   HOST, Bidirectional buffer   G42   VDD3   VDD3   -   3.3V I/O power supply   Se8   GND   GND   -   Ground   G43   AG21   DVVIDEO[4]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   GND   GND   -   Ground   G44   AF21   DVVIDEO[3]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   AD14   TESTMOD[6]   I   TEST, Input buffer   G45   AD20   DVVIDEO[2]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   AE14   VDD   -   1.2V LOGIC power supply   Se6   AH22   DVVIDEO[1]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   AF14   TESTMOD[5]   I   TEST, Input buffer   G47   AG22   DVVIDEO[0]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   AF14   TESTMOD[5]   I   TEST, Input buffer   G47   AG22   DVVIDEO[0]   I/O   VIDEO-Digital, Bidirectional buffer   Se9   AG14   TESTMOD[4]   I   TEST, Input buffer   G48   AE21   REC6560[7]   O   VIDEO-Digital, Output buffer   Se9   AG14   TESTMOD[4]   I   TEST, Input buffer   G49   AF22   REC6560[6]   O   VIDEO-Digital, Output buffer   Se9   AG14   TESTMOD[3]   I   TEST, Input buffer   Se1   AH23   DVREQ   I   VIDEO-Digital, Input buffer   Se1   AH23   DVREQ   I   VIDEO-Digital, Input buffer   Se3   AH15   TESTMOD[2]   I   TEST, Input buffer   Se3   AG23   DVACK   O   VIDEO-Digital, Output buffer   Se3   AG23   DVACK   O   VIDEO-Analog   Se3   AF15   TESTMOD[0]   I   TEST, Input buffer   Se5   AE22   AVSS1DA10   O   VIDEO-Analog   Se3   AE15   TESTMOD[0]   I   TEST, Input buffer   Se5   AE23   AVDD1DA10   O   VIDEO-Analog   A	584	VDD3	VDD3	-	3.3V I/O power supply	639	AD19	R656CLKI	ı	CLOCK, Input buffer
HADRS[26]	585	AG13	HADRS[25]	I/O	HOST, Bidirectional buffer	640	GND	GND	_	Ground
688 GND         GND         - Ground         643 AG21         DVVIDEO[4]         I/O VIDEO-Digital, Bidirectional buffer           689 GND         GND         - Ground         644 AF21         DVVIDEO[3]         I/O VIDEO-Digital, Bidirectional buffer           690 AD14         TESTMOD[6]         I TEST, Input buffer         645 AD20         DVVIDEO[2]         I/O VIDEO-Digital, Bidirectional buffer           691 AE14         VDD         - 1.2V LOGIC power supply         646 AH22         DVVIDEO[1]         I/O VIDEO-Digital, Bidirectional buffer           692 AF14         TESTMOD[5]         I TEST, Input buffer         647 AG22         DVVIDEO[0]         I/O VIDEO-Digital, Bidirectional buffer           693 GND         GND         - Ground         648 AE21         REC6560[7]         O VIDEO-Digital, Bidirectional buffer           694 AG14         TESTMOD[4]         I TEST, Input buffer         649 AF22         REC6560[7]         O VIDEO-Digital, Bidirectional buffer           695 VDD         VDD         - 1.2V LOGIC power supply         650 VDD         VDD         - 1.2V LOGIC power supply           696 AH14         TESTMOD[3]         I TEST, Input buffer         651 AH23         DVREQ         I VIDEO-Digital, Input buffer           697 GND         GND         - Ground         652 GND         GND         - Ground	586	GND	GND	_	Ground	641	AE20	ADMCLKI	ı	CLOCKI, Input buffer
688 GND         GND         - Ground         643 AG21         DVVIDEO[4]         I/O VIDEO-Digital, Bidirectional buffer           689 GND         GND         - Ground         644 AF21         DVVIDEO[3]         I/O VIDEO-Digital, Bidirectional buffer           690 AD14         TESTMOD[6]         I TEST, Input buffer         645 AD20         DVVIDEO[2]         I/O VIDEO-Digital, Bidirectional buffer           691 AE14         VDD         - 1.2V LOGIC power supply         646 AH22         DVVIDEO[1]         I/O VIDEO-Digital, Bidirectional buffer           692 AF14         TESTMOD[5]         I TEST, Input buffer         647 AG22         DVVIDEO[0]         I/O VIDEO-Digital, Bidirectional buffer           693 GND         GND         - Ground         648 AE21         REC6560[7]         O VIDEO-Digital, Bidirectional buffer           694 AG14         TESTMOD[4]         I TEST, Input buffer         649 AF22         REC6560[7]         O VIDEO-Digital, Bidirectional buffer           695 VDD         VDD         - 1.2V LOGIC power supply         650 VDD         VDD         - 1.2V LOGIC power supply           696 AH14         TESTMOD[3]         I TEST, Input buffer         651 AH23         DVREQ         I VIDEO-Digital, Input buffer           697 GND         GND         - Ground         652 GND         GND         - Ground	587	AH13	HADRS[26]	I/O	HOST, Bidirectional buffer	642	VDD3	VDD3	_	3.3V I/O power supply
689 GND         GND         - Ground         644 AF21         DVVIDEO[3]         I/O VIDEO-Digital, Bidirectional buffer           690 AD14         TESTMOD[6]         I TEST, Input buffer         645 AD20         DVVIDEO[2]         I/O VIDEO-Digital, Bidirectional buffer           691 AE14         VDD         - 1.2V LOGIC power supply         646 AH22         DVVIDEO[1]         I/O VIDEO-Digital, Bidirectional buffer           692 AF14         TESTMOD[5]         I TEST, Input buffer         647 AG22         DVVIDEO[0]         I/O VIDEO-Digital, Bidirectional buffer           693 GND         GND         - Ground         648 AE21         REC6560[7]         O VIDEO-Digital, Dutput buffer           694 AG14         TESTMOD[4]         I TEST, Input buffer         649 AF22         REC6560[6]         O VIDEO-Digital, Output buffer           695 VDD         VDD         - 1.2V LOGIC power supply         650 VDD         VDD         - 1.2V LOGIC power supply           696 AH14         TESTMOD[3]         I TEST, Input buffer         651 AH23         DVREQ         I VIDEO-Digital, Input buffer           697 GND         GND         - Ground         652 GND         GND         - Ground           698 AH15         TESTMOD[2]         I TEST, Input buffer         653 AG23         DVACK         O VIDEO-Digital, Output buffer <td>588</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>1</td> <td></td> <td></td> <td>VIDEO-Digital, Bidirectional buffer</td>	588						1			VIDEO-Digital, Bidirectional buffer
550         AD14         TESTMOD[6]         I         TEST, Input buffer         645         AD20         DVVIDEO[2]         I/O         VIDEO-Digital, Bidirectional buffer           591         AE14         VDD         -         1.2V LOGIC power supply         646         AH22         DVVIDEO[1]         I/O         VIDEO-Digital, Bidirectional buffer           592         AF14         TESTMOD[5]         I         TEST, Input buffer         647         AG22         DVVIDEO[0]         I/O         VIDEO-Digital, Bidirectional buffer           593         GND         GND         -         Ground         648         AE21         REC6560[7]         O         VIDEO-Digital, Bidirectional buffer           593         GND         GND         -         Ground         648         AE21         REC6560[7]         O         VIDEO-Digital, Bidirectional buffer           594         AG14         TESTMOD[4]         I         TEST, Input buffer         649         AE22         REC6560[7]         O         VIDEO-Digital, Output buffer           595         VDD         VDD         -         1.2V LOGIC power supply         650         VDD         VDD         -         1.2V LOGIC power supply           598         AH15         TESTMOD[2]         I<	589			_	Ground			1	1/0	VIDEO-Digital, Bidirectional buffer
AE14   VDD				1						
592         AF14         TESTMOD[5]         I         TEST, Input buffer         647         AG22         DVVIDEO[0]         I/O         VIDEO-Digital, Bidirectional buffer           593         GND         GND         -         Ground         648         AE21         REC6560[7]         O         VIDEO-Digital, Output buffer           594         AG14         TESTMOD[4]         I         TEST, Input buffer         649         AF22         REC6560[6]         O         VIDEO-Digital, Output buffer           595         VDD         VDD         -         1.2V LOGIC power supply         650         VDD         VDD         -         1.2V LOGIC power supply           596         AH14         TESTMOD[3]         I         TEST, Input buffer         651         AH23         DVREQ         I         VIDEO-Digital, Input buffer           597         GND         GND         -         Ground         652         GND         GND         -         Ground           598         AH15         TESTMOD[2]         I         TEST, Input buffer         653         AG23         DVACK         O         VIDEO-Digital, Input buffer           599         VDD         VDD         -         1.2V LOGIC power supply         654         G				<u> </u>		_		<del>                                     </del>		<u> </u>
593         GND         GND         — Ground         648         AE21         REC656O[7]         O VIDEO-Digital, Output buffer           594         AG14         TESTMOD[4]         I TEST, Input buffer         649         AF22         REC656O[6]         O VIDEO-Digital, Output buffer           595         VDD         VDD         — 1.2V LOGIC power supply         650         VDD         VDD         — 1.2V LOGIC power supply           596         AH14         TESTMOD[3]         I TEST, Input buffer         651         AH23         DVREQ         I VIDEO-Digital, Input buffer           597         GND         GND         — Ground         652         GND         GND         — Ground           598         AH15         TESTMOD[2]         I TEST, Input buffer         653         AG23         DVACK         O VIDEO-Digital, Output buffer           599         VDD         VDD         — 1.2V LOGIC power supply         654         GND         GND         — Ground           600         AG15         TESTMOD[1]         I TEST, Input buffer         655         AE22         AVSS1DA10         —           601         GND         — Ground         656         AD21         GOUT         O VIDEO-Analog           602         AF						_		1 - 1		<u> </u>
594         AG14         TESTMOD[4]         I         TEST, Input buffer         649         AF22         REC656O[6]         O         VIDEO-Digital, Output buffer           595         VDD         VDD         -         1.2V LOGIC power supply         650         VDD         VDD         -         1.2V LOGIC power supply           596         AH14         TESTMOD[3]         I         TEST, Input buffer         651         AH23         DVREQ         I         VIDEO-Digital, Input buffer           597         GND         GND         -         Ground         652         GND         GND         -         Ground           598         AH15         TESTMOD[2]         I         TEST, Input buffer         653         AG23         DVACK         O         VIDEO-Digital, Input buffer           599         VDD         VDD         -         1.2V LOGIC power supply         654         GND         GND         -         Ground           600         AG15         TESTMOD[1]         I         TEST, Input buffer         655         AE22         AVSS1DA10         -         GOUT         O         VIDEO-Analog           601         GND         GND         -         GROUD         GND         - <t< td=""><td></td><td></td><td></td><td></td><td></td><td>_</td><td></td><td>1 - 1</td><td></td><td><u> </u></td></t<>						_		1 - 1		<u> </u>
VDD						<del></del>	<del> </del>			
596         AH14         TESTMOD[3]         I         TEST, Input buffer         651         AH23         DVREQ         I         VIDEO-Digital, Input buffer           597         GND         GND         -         Ground         652         GND         GND         -         Ground           598         AH15         TESTMOD[2]         I         TEST, Input buffer         653         AG23         DVACK         O         VIDEO-Digital, Output buffer           599         VDD         VDD         -         1.2V LOGIC power supply         654         GND         GND         -         Ground           600         AG15         TESTMOD[1]         I         TEST, Input buffer         655         AE22         AVSS1DA10         -           601         GND         GND         -         Ground         656         AD21         GOUT         O         VIDEO-Analog           602         AF15         TESTMOD[0]         I         TEST, Input buffer         657         AH24         AVDD1DA10         -         GOUT         O         VIDEO-Analog           604         AD15         VIPWM         O         CLOCK, Output buffer         659         AE23         AVDD1DA10         - <td></td> <td></td> <td></td> <td>-</td> <td>·</td> <td></td> <td>1</td> <td></td> <td></td> <td>-</td>				-	·		1			-
597         GND         GND         —         Ground         652         GND         GND         —         Ground           598         AH15         TESTMOD[2]         I         TEST, Input buffer         653         AG23         DVACK         O         VIDEO-Digital, Output buffer           599         VDD         VDD         —         1.2V LOGIC power supply         654         GND         GND         —         Ground           600         AG15         TESTMOD[1]         I         TEST, Input buffer         655         AE22         AVSS1DA10         —           601         GND         GND         —         Ground         656         AD21         GOUT         O         VIDEO-Analog           602         AF15         TESTMOD[0]         I         TEST, Input buffer         657         AH24         AVDD1DA10         —           603         AE15         CSYNC         I         CLOCK, Input buffer         658         AF23         BOUT         O         VIDEO-Analog           604         AD15         VIPWM         O         CLOCK, Output buffer         659         AE23         AVDD1DA10         —										
598         AH15         TESTMOD[2]         I         TEST, Input buffer         653         AG23         DVACK         O         VIDEO-Digital, Output buffer           599         VDD         VDD         -         1.2V LOGIC power supply         654         GND         GND         -         Ground           600         AG15         TESTMOD[1]         I         TEST, Input buffer         655         AE22         AVSS1DA10         -           601         GND         GND         -         Ground         656         AD21         GOUT         O         VIDEO-Analog           602         AF15         TESTMOD[0]         I         TEST, Input buffer         657         AH24         AVDD1DA10         -           603         AE15         CSYNC         I         CLOCK, Input buffer         658         AF23         BOUT         O         VIDEO-Analog           604         AD15         VIPWM         O         CLOCK, Output buffer         659         AE23         AVDD1DA10         -					· ·	_				<u> </u>
See   VDD   VDD   -   1.2V LOGIC power supply   654   GND   GND   -   Ground										
AG15   TESTMOD[1]   I TEST, Input buffer   655   AE22   AVSS1DA10   -										1
601         GND         GND         — Ground         656         AD21         GOUT         O VIDEO-Analog           602         AF15         TESTMOD[0]         I TEST, Input buffer         657         AH24         AVDD1DA10         —           603         AE15         CSYNC         I CLOCK, Input buffer         658         AF23         BOUT         O VIDEO-Analog           604         AD15         VIPWM         O CLOCK, Output buffer         659         AE23         AVDD1DA10         —						_				Ground
602         AF15         TESTMOD[0]         I         TEST, Input buffer         657         AH24         AVDD1DA10         —           603         AE15         CSYNC         I         CLOCK, Input buffer         658         AF23         BOUT         O         VIDEO-Analog           604         AD15         VIPWM         O         CLOCK, Output buffer         659         AE23         AVDD1DA10         —						_				\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
603         AE15         CSYNC         I         CLOCK, Input buffer         658         AF23         BOUT         O         VIDEO-Analog           604         AD15         VIPWM         O         CLOCK, Output buffer         659         AE23         AVDD1DA10         -	601					_				VIDEO-Analog
604 AD15 VIPWM O CLOCK, Output buffer 659 AE23 AVDD1DA10 -	602			1		_				
	603				· ·		1		0	VIDEO-Analog
605 AH16 PLLON I TEST, Input buffer 660 AG24 ROUT O VIDEO-Analog	604	AD15	VIPWM	0	CLOCK, Output buffer	659	AE23			
	605	AH16	PLLON	- 1	TEST, Input buffer	660	AG24	ROUT	0	VIDEO-Analog

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No.	BALL Address	Pin Name	1/0	Function	No.	BALL Address	Pin Name	I/O	Function
661	AD22	IREF[0]	-	VIDEO-Analog	669	AD23	REC656O[4]	0	VIDEO-Digital, Output buffer
662	AF24	IREF[1]	_	VIDEO-Analog	670	AH27	REC656O[3]	0	VIDEO-Digital, Output buffer
663	AG25	YOUT	0	VIDEO-Analog	671	AG26	REC656O[2]	0	VIDEO-Digital, Output buffer
664	AH25	AVSS2DA10	_		672	AG27	REC656O[1]	0	VIDEO-Digital, Output buffer
665	AE24	COUT	0	VIDEO-Analog	673	GND	GND	_	Ground
666	AH26	AVDD2DA10	_		674	AD24	REC656O[0]	0	VIDEO-Digital, Output buffer
667	GND	GND	-	Ground	675	AE25	AGCCTL	0	VIDEO-Analog
668	AF25	REC656O[5]	0	VIDEO-Digital, Output buffer					

В

## Others

BALL Address	Pin Name						
AA23	GND	P12	GND	AC11	VDD	AB23	VDD3
AA6	GND	P13	GND	AC14	VDD	AB6	VDD3
AC12	GND	P14	GND	AC6	VDD	AC10	VDD3
AC17	GND	P15	GND	AC8	VDD	AC13	VDD3
AC20	GND	P16	GND	L6	VDD	AC16	VDD3
AC9	GND	P17	GND	AC11	VDD	AC19	VDD3
F11	GND	R12	GND	AC14	VDD	AC22	VDD3
F14	GND	R13	GND	AC6	VDD	AC7	VDD3
F17	GND	R14	GND	AC8	VDD	F10	VDD3
F20	GND	R15	GND	L6	VDD	F13	VDD3
F23	GND	R16	GND	P6	VDD	F16	VDD3
F8	GND	R17	GND	U6	VDD	F19	VDD3
H6	GND	R23	GND	Y6	VDD	F22	VDD3
J23	GND	R6	GND	F12	VDD	F7	VDD3
M12	GND	T12	GND	F6	VDD	G23	VDD3
M13	GND	T13	GND	F9	VDD	G6	VDD3
M14	GND	T14	GND	J6	VDD	K23	VDD3
M15	GND	T15	GND	AC15	VDD	K6	VDD3
M16	GND	T16	GND	AC18	VDD	N23	VDD3
M17	GND	T17	GND	AC21	VDD	N6	VDD3
M23	GND	U12	GND	AC23	VDD	T23	VDD3
M6	GND	U13	GND	F15	VDD	T6	VDD3
N12	GND	U14	GND	F18	VDD	W23	VDD3
N13	GND	U15	GND	F21	VDD	W6	VDD3
N14	GND	U16	GND	H23	VDD		
N15	GND	U17	GND	L23	VDD		
N16	GND	V23	GND	P23	VDD		
N17	GND	V6	GND	U23	VDD		
				Y23	VDD		

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124

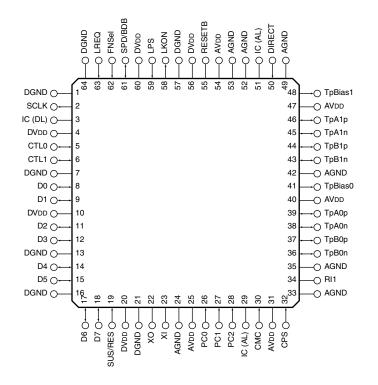
DVR-520H-S

## **■ UPD72852AGB-8EU (MAIN ASSY : IC5101)**

• IEEE1394 Physical IC

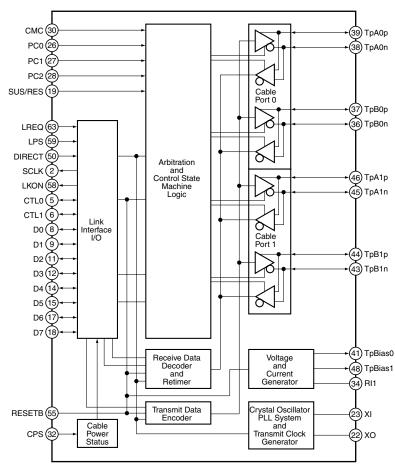
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#### Pin Arrangement



### Block Diagram

5



125

DVR-520H-S

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## • Pin Function

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## • Cable Interface Pins

	No.	Pin Name	I/O	Function
٠	39	ТрА0р	I/O	Port 0 twisted pair cable A positive phase I/O
	38	TpA0n	I/O	Port 0 twisted pair cable A negative phase I/O
	37	ТрВ0р	I/O	Port 0 twisted pair cable B positive phase I/O
	36	TpB0n	I/O	Port 0 twisted pair cable B negative phase I/O
	46	TpA1p	I/O	Port 1 twisted pair cable A positive phase I/O
	45	TpA1n	I/O	Port 1 twisted pair cable A negative phase I/O
	44	ТрВ1р	I/O	Port 1 twisted pair cable B positive phase I/O
	43	TpB1n	I/O	Port 1 twisted pair cable B negative phase I/O
}	19	SUS/RES	I	Suspend/Resume function select 1 : Suspend/Resume on (IEEE1394a-2000 compliant) 0 : Suspend/Resume off (P1394a draft 1.3 compliant)
	32	CPS	I	Cable power status Connect to the cable through a 390 k $\Omega$ resistor and to the GND through a 100 k $\Omega$ resistor. 0 : Cable power fail 1 : Cable power on

3

## • Link Interface Pins

	No.	Pin Name	I/O	Function
	8	D0	I/O	Data input/output (bit 0)
	9	D1	I/O	Data input/output (bit 1)
	11	D2	I/O	Data input/output (bit 2)
	12	D3	I/O	Data input/output (bit 3)
	14	D4	I/O	Data input/output (bit 4)
	15	D5	I/O	Data input/output (bit 5)
	17	D6	I/O	Data input/output (bit 6)
	18	D7	I/O	Data input/output (bit 7)
	5	CTL0	I/O	Link interface control (bit 0)
,	6	CTL1	I/O	Link interface control (bit 1)
	63	LREQ	I	Link request input
	2	SCLK	0	Link control output clock LPS 1 : 49.152 MHz output LPS 0 : Clamp to 0 (The clock signal will be output within 25 µsec after change to "0")
l	59	LPS	I	Link power status input 0 : Link power off 1 : Link power on (PHY/Link direct connection)
	58	LKON	0	Link-on signal output Link-on signal is 6.1444 MHz clock output.
	50	DIRECT	I	PHY/Link isolation barrier control input 0 : Isolation barrier 1 : PHY/Link direct connection

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126

## • Control Pins

No.	Pin Name	I/O	Function
26	PC0	I	Power class set input
27	PC1	ı	This pin status will be loaded to Pwr_class bit which allocated to PHY register 4H.
28	PC2	ı	IEEE1394a-2000 chapter [4.3.4.1]
30	СМС	I	Configuration manager capable setting This pin status will be loaded to Contender bit which allocated to PHY register 4H. 0: Non contender 1: Contender
55	RESETB	I	Power on reset input Connect to GND through a 0.1 μF capacitor. 0 : Reset 1 : Normal
61	SPD/BDB	I FNSel = 0	Speed select (UPD72852GB) 0: MAX. S200 1: MAX. S400
01	סרט/סטס	O FNSel = 1	BIAS Detected output (Logical Inverse) 0 : BIAS is coming from some port. 1 : BIAS is not coming from any port.

## • IC

No.	Pin Name	I/O	Function
29, 51	IC (AL)	_	Internally Connected (Low Clamped) Connected to GND.
3	IC (DL)	_	Internally Connected (Low Clamped) Connected to GND.

## • Power Supply Pins

No.	Pin Name	1/0	Function
25, 31, 40, 47, 54	AVDD	-	Analog power
24, 33, 35, 42, 49, 52, 53	AGND	-	Analog GND
4, 10, 20, 56, 60	DV <sub>DD</sub>	-	Digital VDD
1, 7, 13, 16, 21, 57, 64	DGND	ı	Digital GND

## • Other Pins

No.	Pin Name	I/O	Function
41	TpBias0	0	Port 0 twisted pair output
48	TpBias1	0	Port 1 twisted pair output
34	RI1	-	Resistor connection pin 1 for reference current generator Please connect to GND pin through the 9.1 k $\Omega$ resistor.
23	XI	_	Crystal oscillator connection XI
22	хо	_	Crystal oscillator connection XO
62	FNSel	ı	Function Select 0:#61 acts as SPD (UPD72852GB compliant) 1:#61 acts as BDB

127

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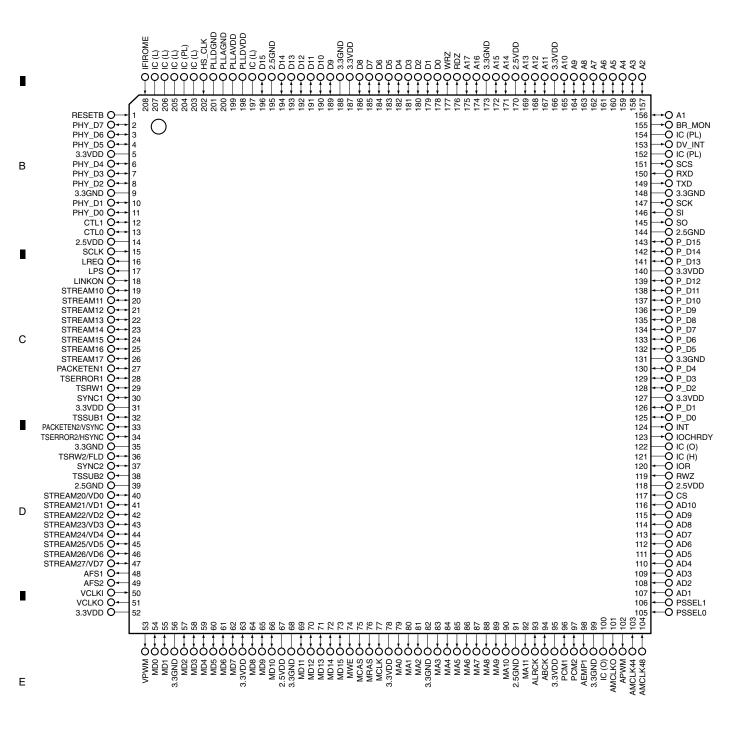
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## ■ UPD72893BGD-LML (MAIN ASSY : IC5202)

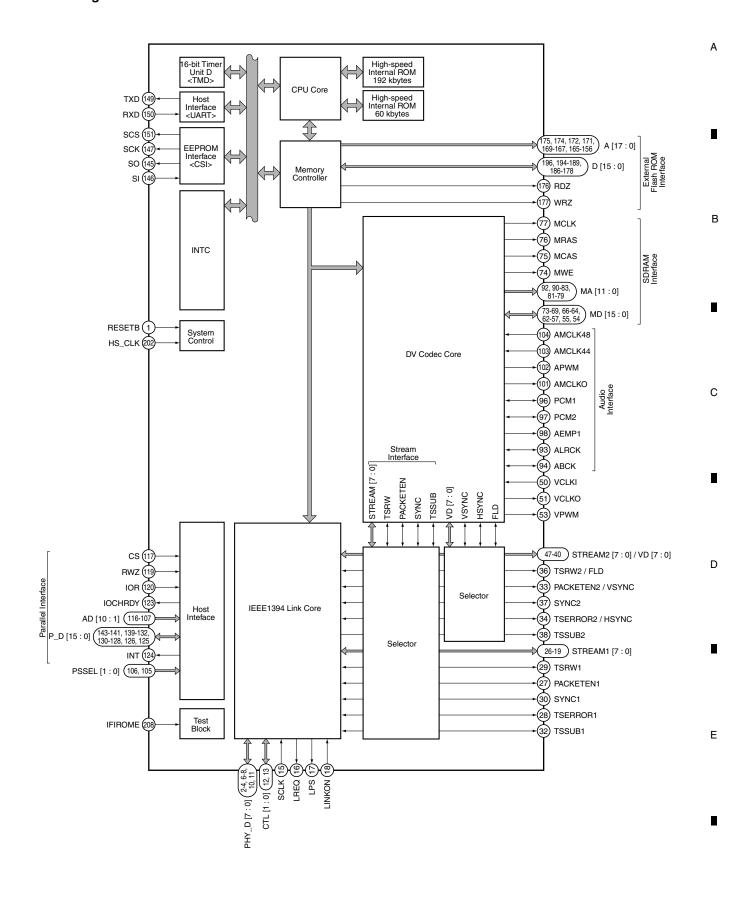
• IEEE1394 Link IC

#### Pin Arrangement



3

128



DVR-520H-S

## • Pin Function

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## (1) Link relation

No.	Pin Name	I/O	Function	Active
18	LINKON	ı	Link-on signal input Clock input When LPS is active, input 0.	_
17	LPS	0	Link power status output Link power OFF: 0 Link power ON: 2.7 MHz pulse output (20 dividing of host clock 54 MHz)	_
16	LREQ	0	Link request output	_
15	SCLK	ı	Clock input for Link control LPS is active: 49.152 MHz input LPS = 0 0: fixed	_
12, 13	CTL [1:0]	I/O	PHY/Link control signal input/output	_
2-4, 6-8, 10,11	PHY_D [7:0]	I/O	Data input/output between PHY-Link	_
26-19	STREAM1 [7:0]	I/O	ISO data bus of stream interface 1	_
27	PACKETEN1	I/O	Packet enable signal input/output of stream interface 1	H/L
28	TSERROR1	I/O	Packet error signal input/output of stream interface 1	H/L
29	TSRW1	I/O	Data read/write enable signal input/output of stream interface 1	_
30	SYNC1	I/O	Frame synchronous signal input/output of stream interface 1	H/L
32	TSSUB1	I/O	Not used Connect to VDD or GND through a resistor.	H/L
47-40	STREAM2 [7:0]	I/O	ISO data bus of stream interface 2	_
33	PACKETEN2	I/O	Packet enable signal input/output of stream interface 2	H/L
34	TSERROR2	I/O	Packet error signal input/output of stream interface 2	H/L
36	TSRW2	I/O	Data read/write enable signal input/output of stream interface 2	_
37	SYNC2	I/O	Frame synchronous signal input/output of stream interface 2	H/L
38	TSSUB2	0	Not used Set to open.	_

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## (2) Video interface pins

No.	Pin Name	I/O	Function	Active	
50	VCLKI	1	Video clock input (27 MHz)	_	
51	VCLKO	0	Video clock output (27 MHz)	_	
47-40	VD [7:0]	I/O	Video data signal	_	
33	VSYNC	I/O	Video vertical sync. signal	L	
34	HSYNC	I/O	Video horizontal sync. signal	L	
36	FLD	I/O	Field index signal	_	
53	VPWM	0	PWM signal for video PLL	_	

130

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## (3) Audio interface pins

No.	Pin Name	I/O	Function	Active
104	AMCLK48	1	Audio master clock input for sampling frequency 48 kHz	_
103	AMCLK44	ı	Audio master clock input for sampling frequency 44 kHz	_
101	AMCLKO	0	Audio master clock output	_
96	PCM1	I/O	Audio PCM serial data At 2ch: System 1 (data of audio block 1) At 4ch: System 1 The above is default setting value. Input/output data of PCM 1 and PCM 2 is replaced by Channel swap setting of an AUDIO_FUNC register.	_
97	PCM2	I/O	Audio PCM serial data At 2ch: Mute At 4ch: System 2 (data of audio block 2) The above is default setting value. Input/output data of PCM 1 and PCM 2 is replaced by Channel swap setting of an AUDIO_FUNC register. Note: Cannot use it in DV decode.	-
98	AEMP1	0	PCM1 emphasis ON/OFF in PCM 1 output	Н
93	ALRCK	I/O	Audio LR clock L ch : High R ch : Low	_
94	ABCK	I/O	Audio bit clock	_
49, 48	AFS [2 : 1]	0	Audio sampling frequency  AFS2 AFS1  44.1 kHz 0 1  48 kHz 0 0  32 kHz 1 0	_
102	APWM	0	PWM signal for audio PLL	

## (4) SDRAM interface pins

No.	Pin Name	I/O	Function	Active
77	MCLK	0	CLK pin connection of SDRAM	_
76	MRAS	0	RAS pin connection of SDRAM	-
75	MCAS	0	CAS pin connection of SDRAM	-
74	MWE	0	WE pin connection of SDRAM	-
92, 90-83, 81-79	MA [ 11 : 0]	0	Address pin connection of SDRAM	_
73-69, 66-64, 62-57, 55, 54	MD [ 15 : 0]	I/O	Data pin connection of SDRAM  Note: Process of pull-up or pull down is necessary.  So connect it to SDRAM directly.	-

131

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## (5) Host interface pins

## (a) Parallel interface pins

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No.	Pin Name	I/O	Function	Active
117	CS	I	Chip select input of parallel interface	L
119	RWZ	I	Read and write control input of parallel interface ISA type bus, SH-1 bus: Write strobe 68000 bus: Read/write selection signal	L
120	IOR	ı	IO read control input of parallel interface ISA type bus, SH-1 bus : Read strobe 68000 bus : Data strobe (DS)	L
123	IOCHRDY	0	Ready output of parallel interface	L
116-107	AD [ 10 : 1]	1	Address input of parallel interface	_
143-141, 139-132, 130-128, 126, 125	P_D [15 : 0]	I/O	Data input/output of parallel interface	_

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## (b) Serial interface pins

No.	Pin Name	I/O	Function	Active
149	TXD	I/O	Serial transmission data output of unsynchronous serial interface (UART)	-
150	RXD	I/O	Serial transmission data input of unsynchronous serial interface (UART)	_

### (c) Others

No.	Pin Name	I/O	Function	Active	
124	INT	0	nterrupt output to the outside		
106, 105	PSSEL [1:0]	I	Parallel/serial interface selection Input signal to select the outside interface which of parallel interface or serial interface.  PSSEL [1:0] Select  00 Serial interface (UART)  01 Parallel interface (ISA type bus)  10 Parallel interface (68000 bus)  11 Parallel interface (SH-1 bus)	_	

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132

## (6) External ROM connection pins

## (a) Flash ROM interface pins

No.	Pin Name	I/O	Function	Active
196, 194-189, 186-178	D [15 : 0]	I/O	External ROM data bus Data bus in the external ROM access. Process of pull-up or pull down is necessary.	_
175, 174, 172, 171, 169-167, 165-156		0	External ROM address bus Address bus in the external ROM access. Can addressing the 256k byte space.	_
176	RDZ	0	ROM read Strobe signal which shows a read cycle for external ROM. It becomes the inactive in the idle state.	L
177	WRZ	0	ROM write Strobe signal which shows a write cycle for external ROM.	L

## (b) EEPROM interface pins

No.	Pin Name	I/O	Function	Active
145	SO	I/O	Serial transmit data output of clock-synchronous system serial interface (CSI)	_
146	SI	I/O	Serial receive data input of clock-synchronous system serial interface (CSI)	_
147	SCK	I/O	Clock output of clock-synchronous system serial interface (CSI)	_
151	SCS	I/O	Chip select output of clock-synchronous system serial interface (CSI)	_

### (7) Clock and reset pins

(1) electrical and recet pine							
No.	Pin Name	I/O	Function	Active			
1	RESETB	-	Reset RESETB input is asynchronous input. When a signal of fixed low-level width is input without relation to an operation clock, take precedence of all operation, and reset the system. Note: RESETB is low-active.	L			
202	HS_CLK	ı	Host clock Clock input pin which is supplied to CPU core and built-in peripheral I/O. Please input 27 MHz clock. Perform 2 multiply with internal PLL by 27 MHz clock, 54 MHz clock is supplied to CPU core and internal peripheral I/O.	_			

133

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## (8) Power supply and ground pins

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No.	Pin Name	I/O	Function	Active	
5, 31, 52, 63, 78, 95, 127, 140, 166, 187	3.3VDD	-	.3V power supply .3V positive power supply pins. Power supply for 3.3V interface I/O.		
14, 67, 118, 170	2.5VDD	_	2.5V power supply 2.5V positive power supply pins. Power supply for internal each block.	_	
39, 91, 144, 195	2.5GND			-	
9, 35, 56, 68, 82, 99, 131, 148, 173, 188	3.3GND	_	Ground pins Connect all GND pins to the common ground.	_	
199	PLLAVDD	_	Analog power supply for multiply circuit Analog positive power supply pin for PLL. Supply 2.5V.	-	
200	PLLAGND	_	Analog ground for multiply circuit Analog ground for PLL	_	
198	PLLDVDD	_	Digital power supply for multiply circuit Digital positive power supply pin for PLL. Supply 2.5V.	_	
201	PLLDGND	_	Digital ground for multiply circuit Digital ground for PLL	_	
121	IC (H)	-	Internally connected pin Connect to VDD directly.	_	
197, 203, 205-207	IC (L)	_	Internally connected pin Connect to ground directly.		
152, 154, 204	IC (PL)	_	Internally connected pin Connect to ground through a resistor.		
100, 122	IC (O)	_	Internally connected pin Set to open.	-	

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### (9) Others

) Others				
No.	Pin Name	I/O	Function	Active
153	DV_INT	I/O	Interrupt pin to the outside for the DV status read out.	Н
155	BR_MON	I/O	Shows the bus reset occurred. There is some delay after real bus reset occurred because of set by the built-in firmware.	Н
208	IFIROME	ı	ROM operation selection input Set to 1 normally.	-

134

DVR-520H-S

3

## ■ Main newly developed technologies

#### 1. Pickup

The pickup supports quadruple-speed recording for the DVD-R/RW.

A liquid-crystal tilt servo system is adopted for the pickup.

#### 2. Recording-signal-processing LSI

• UPD3330GC (DRIVE Assy: IC101)

The recording-signal-processing module of conventional models consists of two chips, but this has been integrated into a single newly developed recording-signal-processing LSI, enabling stable performance and cost reduction.

#### 3. AV-signal-processing LSI

• M65673WG (MAIN Assy: IC1001)

The AV-signal-processing module of conventional models consists

of eleven chips, but this has been integrated into a single newly developed AV-signal-processing LSI, enabling large-scale cost reduction while maintaining the conventional functions. In the new LSI, all the basic functions necessary for a DVD recorder have been integrated. Like conventional models, this model is designed to support multitasking. The main functions are as follows:

- 3-D Y/C separation
- Video decoding
- Frame TBC
- MPEG video encoding
- Dolby Digital Consumer Encoding
- ATA/ATAPI I/F (2 ch)
- Main CPU (32-bit RISC, 54 MHz)
- Graphics engine (OSD, scaling, mixing)
- MPEG video decoding
- Audio decoding (AC-3, MPEG)
- Video encoding
- Progressive conversion
- Audio I/F
- 3-D DNR for playback

#### 4. DV-signal-processing LSI

The DV-signal-processing LSI consists of the following two chips:

• UPD72852AGB-8EU (MAIN Assy: IC5101) A 400-Mbps two-port PHY LSI in compliance with the IEEE1394a-2000 standards

#### • UPD72893BGD-LML (MAIN Assy: IC5202)

An EEE1394 link controller LSI having DV (digital video) encoding/decoding functions. Encoding/decoding of digital video signals in compliance with the SD specifications (NTSC/PAL) of the DV standard is supported. The 32-bit RISC CPU is built in for controlling the IEEE1394 bus and sending/receiving AV/C commands.

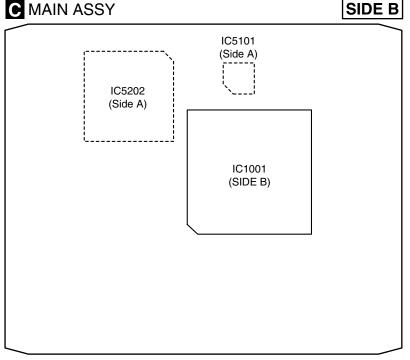


Fig.1 MAIN Assy

135

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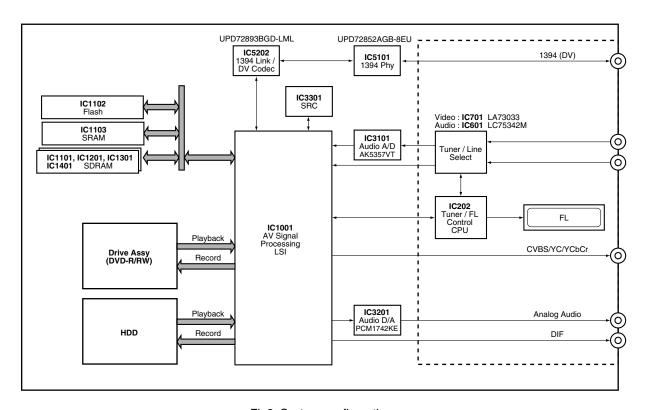
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## System configuration

In each signal-processing LSI of the main function blocks, various processes have been integrated into one chip, which enables simpler system configuration. With the AV-signal-processing LSI at the center, video inputs/outputs, audio inputs/outputs, DV inputs/outputs, writer and various memory cells are connected to it.



3

Fig2. System configuration

#### [Memorized Data]

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- EEPROM (IC204 JCKB ASSY)
   The information about Tuner is backed up.
   (Pre-set CH, AFT ON/OFF, Skip CH, etc)
   Information about timed recording
   Other information
   (The state of Volume, remote control mode and last positions (Line/Tuner, etc)
- CPU SDRAM (IC1101 MAIN ASSY)
   The execution area and working area of a program
- FLASH ROM (IC1102 MAIN ASSY)

  The storing area of a program code and setting information
- SRAM (IC1103 MAIN ASSY)
   The working area for record and the storing area of setting information (backup RAM)
- DEC SDRAM (IC1201 MAIN ASSY)
   The working area of MPEG playback and OSD/Thumbnail (OSD is mainly for Disc Menu creation in Video mode)
- ENC SDRAM (IC1301 MAIN ASSY)
   The working area of MPEG recording and analog input and output (AVIO)

- ATA SDRAM (IC1401 MAIN ASSY)
   The working area of ATA/OSD2/Audio TBC (OSD2 is for all GUI.)
- ATA SDRAM (IC1421 MAIN ASSY)
   This is only for HDD model.
   The working area about HDD operation.
- DV SDRAM (IC5204 MAIN ASSY)
  The working area of Link and DV Codec

136

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## New functions and specifications

In this model, the following new functions and specifications have been included in addition to those of conventional models:

#### 1. Improved multitasking functions

As both the HDD and DVD drive are mounted in this model, like conventional models, the unit is designed to support various multitasking. Furthermore, this model supports DVD multitasking (only in VR mode), which was impossible with conventional models.

#### a Pursuit playback

Playback of the title being recorded by the DVD drive in VR mode or the HDD is supported.

(b) Simultaneous recording/playback 1 Playback of a title other than that being recorded by the DVD drive in VR mode or the HDD is also supported.

© Simultaneous recording/playback 2 DVD playback during HDD recording is supported.

d) Simultaneous recording/playback 3
 HDD playback during DVD recording is supported.

Recording during high-speed dubbing
 HDD recording during high-speed dubbing from the HDD to a
 DVD is supported.

① Playback during high-speed dubbing Playback of an HDD title during high-speed dubbing from the HDD to a DVD is supported.

#### 2. Improved dubbing functions

High-speed dubbing and normal-speed dubbing are supported, as with conventional models. A one-touch dubbing function that enables automatic selection between these dubbing functions is also provided. In this model, high-speed dubbing from a DVD (in VR mode) to the HDD is also an added capability.

#### 3. Disc backup

The function of creating a backup disc for a disc recorded in Video mode is added. The data of the original DVD are transferred to the HDD, then retransferred to the DVD drive, and because no reencoding is required during data transmission between the drives, a backup disc with no degradation of video and audio signals can be created.

### 4. Advanced disc NAVI

In the conventional disc NAVI function, recorded titles are displayed with still pictures as a list. In the advanced disc NAVI function, the title selected with the cursor is displayed as an animated picture with sound.

#### 5. Improved Still Picture menu in Video mode

The disc NAVI function, which enables displaying a list of recorded titles with still pictures, is enabled in Video mode with this model. Selection from among nine title menus is also supported.

#### 6. Adoption of MPEG2 SIF

In MN1-6 modes, MPEG2 SIF has been adopted, instead of the MPEG1 SIF of conventional models. This enables higher-quality recording for longer hours.

### 7. Improved editing functions

For DVD, the original/play-list editing in DVD-VR mode available with conventional models is provided.

For HDD editing, play-list-editing functions almost the same as for the DVD-VR, such as title combination, separation, and partial erasure, are enabled for the HDD with this model. With conventional models, these edit functions are available only for the dubbing list. The automatic-chapter-mark-insertion function in response to a change in audio type (stereo, monaural, bilingual) makes commercial-cutting editing easier.

#### 8. Various-format playback

Playback of WMA, MP3, and JPEG formats is supported.

#### 9. Other functions and specifications

The following main functions and specifications adopted with conventional models are also provided with this model:

- 192-kHz, 24-bit DAC
- 48-kHz, 20-bit ADC
- Digital 3-D Y/C separation circuit
- Digital frame TBC
- 3-D DNR
- DV (iLink) input/output
- · Playback with commercials skipped
- CD/video-CD playback
- Picture creation
- Recording with 3/4-D1 and 2/3-D1 resolutions
- Recording mode with 32-step MNs
- LPCM recording
- High-resolution GUI
- Progressive output

137

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## 7.4 CAUTIONS ON HANDLING THE HDD

### (1) Cautions on Handling the HDD

- The HDD is very sensitive to shocks and vibrations. Care must be taken especially during operation (when the power is on).
- The HDD is very sensitive to electrostatic charges.
  - Rapid change in temperature or humidity may cause deterioration of the HDD.

Note: After receiving damage caused by any above-mentioned factors, the HDD may operate normally for dozens or some hundreds of hours but then suddenly crash. If you are certain you have damaged a new repair part (HDD) while making repairs, do not use the

> The HDD is about 10 times as sensitive to shock during operation than during nonoperation.

#### Reference: Main specifications on damage to the HDD

	During operation	During nonoperation
Shock G (acceleration)	<approx. 20="" g<="" td=""><td><approx. 200="" g<="" td=""></approx.></td></approx.>	<approx. 200="" g<="" td=""></approx.>
Temperature change	< 20°	C/hour
Moisture change	< 20%	%/hour

#### Reference: Estimate value of falling distance vs. shock (G) when the HDD is dropped without protection

Falling Landing surface	Granite surface	Concrete floor	Synthetic-resin- coated table	Antistatic sponge
0.5 inch / 12.7 mm	387	217	200	26
1.0 inch / 25.4 mm	595	457	310	37
2.0 inch / 50.8 mm	1133	600	680	70
4.0 inch / 101.6 mm	1795	1040	1050	267

### (2) Cautions on handling the product on which the HDD is mounted or the HDD as a repair part, and examples of dangerous handling

#### [Cautions on handling the product on which the HDD is mounted]

- While the unit is turned on, the HDD is always in operation. Be sure NOT to impart shock to the unit.
- Examples of dangerous handling: while the power is on
- Bumping on the bonnet

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- Dropping an object, such as a small screwdriver or remote control unit, onto the bonnet, or bumping an object against the cabinet
- Moving the unit by dragging
- · Stacking another product on the unit

Note: Be sure NOT to impart shock, such as bumping or hitting a screwdriver against the HDD, during diagnosis with the bonnet open.

#### • Examples of dangerous handling: while the power is off

- Imparting strong shock, although the HDD is more resistant to shock when the power is off
- Dropping the unit from a height of several centimeters, or after lifting one side of the unit up, then letting the unit drop.
- Do NOT move the unit immediately after the power is turned off. Wait at least 30 seconds after the indication on the FL display changed from POWER OFF to the clock indication before moving the unit.
- If the AC power cord is accidentally disconnected before turning the unit off, wait at least for one minute before moving it. In this case, damage to the HDD caused by sudden shutoff may be small, because the emergency relief mechanism is activated. However, if sudden shutoff occurrs during recording or playback, recorded data may be damaged. Be sure to check operations.

#### [Cautions on handling the HDD as a repair part]

- 1. Handle the HDD in a safe environment:
  - Handle the HDD over an antistatic pad that can also absorb shock.
  - Wear wrist bands to prevent electrostatic charges generated in your body from affecting the HDD.
  - 2. The following must be observed when handling the HDD:
    - Handle one HDD at a time. Do NOT hold several HDDs at the same time.
    - Grip the HDD on both sides so that you do not touch its terminals or circuit boards.
    - Do NOT stack one HDD onto another HDD (even if the HDDs are protected in antistatic bags).
    - Do NOT bump the HDDs against one another.
    - Do NOT bump any tool, such as a screwdriver, or other hard object against the HDD.
    - When a repair part (HDD) is transported and there is a large temperature difference between outdoors and indoors, to the indoor, leave it in its package for about a half day to gradually cool or warm the HDD to room temperature before unpacking it.

#### [Notes on packing for shipment]

- When returning a defective HDD for analysis, handle with care as if it were a good product. Otherwise, the results of analysis may not be correct.
- · When packing, use the antistatic bag and packing materials in which the repair part for service was delivered. Attach a copy of the slip for service or a memo stating symptoms in as much detail as possible.

138

#### ■ Outline and part No. of the HDDs

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\*Pioneer's part No. is not stamped.

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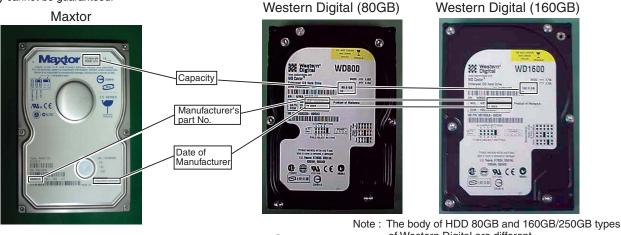
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	Maxtor		Western Digital		Seagate		
Model Name	Capacity	Pioneer's Part No. (for service)	Manufacture's Part No.	Pioneer's Part No. (for service)	Manufacture's Part No.	Pioneer's Part No. (for service)	Manufacture's Part No.
DVR-520H-S	80GB	VXF1010	4R080L0-	VXF1043	WD800BB -xxHEAx	VXF1036	ST38001□ACE-
DVR-65H-S	160GB	VXF1028	4R160L0-	VXF1055	WD1600BB -xxFTAx	VXF1040	ST316002□ACE-

7

- When replacing the HDD, carefully check the capacity and manufacturer's part No. on the part label to avoid replacing with a similar but inappropriate product. You can also check the model No. of the mounted HDD on the Service mode screen.
- Do NOT use repair parts, such as commercially available HDDs, other than those designated above, as their functions, performance or reliability cannot be guaranteed.



Manufacturer's part No.

Date of Manufacturer

Manufacturer

Date of Manufacturer

of Western Digital are different.

How to read the information of Seagate HDD

Ex. Date Code 0435x

04 year (from July) manufactured on 35th week

Fig.1 Location of the data on capacity and part No. of the HDD

#### ■ Confirmation of the jumper pin location of the HDD



139

# Disc / content format playback compatibility

#### General disc compatibility

This recorder is compatible with a wide range of disc types (media) and formats. Playable discs will generally feature one of the following logos on the disc and/or disc packaging. Note however that some disc types, such as recordable CD and DVD, may be in an unplayable format—see below for further compatibility information.







DVD-Video DVD-R







Audio CD Video CD

CD CD-R CD-RW





Fujicolor CD

- · Also compatible with KODAK Picture CD
- Is a trademark of DVD Format/Logo Licensing Corporation.
- 🕏 is a trademark of Fuji Photo Film Co. Ltd.

#### **DVD-R/RW** compatibility

This recorder will play and record DVD-R/RW discs. Compatible media:

- DVD-RW Ver. 1.1, Ver. 1.1 / 2x and Ver. 1.2
- DVD-R Ver. 2.0 and Ver. 2.0 / 4x / 8x

#### Recording formats:

- DVD-R: DVD-Video format (Video mode)
- DVD-RW: Video Recording (VR) format and DVD-Video format (Video mode)

#### CD-R/RW compatibility

This recorder cannot record CD-R or CD-RW discs.

- Compatible formats: CD-Audio, Video CD, ISO 9660 CD-ROM\* containing MP3, WMA or JPEG files
   \* ISO 9660 Level 1 or 2 compliant. CD physical format: Mode1, Mode2 XA Form1. Romeo and Joliet file systems are both compatible with this recorder.
- Multi-session playback: Yes (except CD-Audio and Video CD)
- · Unfinalized disc playback: CD-Audio only

#### Compressed audio compatibility

- Compatible media: CD-ROM, CD-R, CD-RW
- Compatible formats: MPEG-1 Audio Layer 3 (MP3), Windows Media Audio (WMA)
- Sampling rates: 44.1 or 48kHz
- Bit-rates: Any (128Kbps or higher recommended)
- VBR (variable bit rate) MP3 playback: Yes
- VBR WMA playback: No
- WMA encoder compatibility: Windows Media Codec
   8 (files encoded using Windows Media Codec 9 may be playable but some parts of the specification are not

- supported; specifically, Pro, Lossless, Voice and VBR)
- DRM (Digital Rights Management) file playback: No (see also DRM in the Glossaryon page 97)

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- File extensions: .mp3, .wma (these must be used for the recorder to recognize MP3 and WMA files – do not use for other file types)
- File structure: Up to 99 folders / 999 files (if these limits are exceeded, only files and folders up to these limits are playable)

#### WMA (Windows Media Audio) compatibility



The Windows Media<sup>®</sup>logo printed on the box indicates that this recorder can playback Windows Media Audio content

WMA is an acronym for Windows Media Audio and refers to an audio compression technology developed by Microsoft Corporation. WMA content can be encoded by using Windows Media<sup>®</sup> Player version 7, 7.1, Windows Media<sup>®</sup> Player for Windows<sup>®</sup> XP, or Windows Media<sup>®</sup> Player 9 Series.

Microsoft, Windows Media, and the Windows logo are trademarks, or registered trademarks of Microsoft Corporation in the United States and/or other countries.

#### JPEG file compatibility

- Compatible formats: Baseline JPEG and EXIF 2.2\* still image files
  - \* File format used by digital still cameras
- Sampling ratio: 4:4:4, 4:4:2, 4:2:0
- Horizontal resolution: 160 5120 pixels
- Vertical resolution: 120 3840 pixels
- Progressive JPEG compatible: No
- File extensions: .jpg, jpeg, jif, jfif (must be used for the recorder to recognize JPEG files – do not use for other file types)
- File structure: The recorder can load up to 99 folders / 999 files at one time (if there are more files/folders that this on the disc then more can be reloaded)

#### PC-created disc compatibility

Discs recorded using a personal computer may not be playable in this unit due to the setting of the application software used to create the disc. In these particular instances, check with the software publisher for more detailed information.

Discs recorded in packet write mode (UDF format) are not compatible with this recorder.

Check the DVD-R/RW or CD-R/RW software disc boxes for additional compatibility information.

140

## 7.6 CLEANING



Before shipping out the product, be sure to clean the following positions by using the prescribed cleaning tools:

Position to be cleaned	Cleaning tools		
Pickup lenses	Cleaning liquid: GEM1004 Cleaning paper: GED-008		

Position to be cleaned	Cleaning tools	
Fans	Cleaning paper: GED-008	

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DVR-520H-S

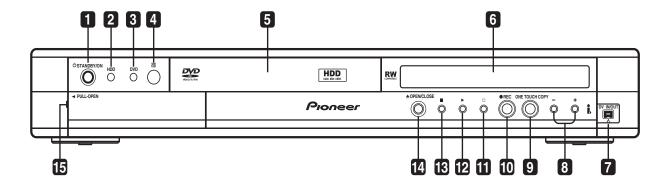
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141

## 8. PANEL FACILITIES

## **8.1 FRONT SECTION**

## Front panel



#### 1 USTANDBY/ON

Press to switch the recorder on/into standby.

#### 2 HDD

В

Press to switch to the hard disk drive (HDD) for recording and playback. The button lights when HDD is selected.

#### 3 DVD

Press to switch to DVD for recording and playback. The button lights when DVD is selected.

#### 4 IR remote sensor

5 Disc tray

#### 6 Front panel display

See Display on page 22 for details.

#### 7 DV IN/OUT jack

Digital input/output jack for use with a DV camcorder.

#### 8 +/–

Use to change TV channels, skip chapters/tracks, etc.

#### 9 ONE TOUCH COPY

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Press to start One Touch Copy of the currently playing title to DVD or the HDD. See also the *Note on copying* on page 9.

#### 10 ● REC

Press to start recording.

#### 11 🗆

Press to stop recording.

#### 12

Press to start or restart playback.

#### 13 ■

Press to stop playback.

#### 14 ▲ OPEN/CLOSE

Press to open/close the disc tray.

#### 15 Front panel inputs

Pull the cover down where indicated to access the front panel input jacks. Especially convenient for connecting camcorders and other portable equipment.

142

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Rear panel connections

#### 1 VHF/UHF IN/OUT

Connect your TV antenna to the VHF/UHF IN jack. The signal is passed through to the VHF/UHF OUT jack for connection to your TV.

#### 2 COMPONENT VIDEO OUT

A high-quality video output for connecting to a TV or monitor with a component video input.

#### 3 Audio/video outputs 1 and 2

Two sets of audio/video outputs (stereo analog audio; S-video and standard (composite) video jack) that you can use to connect TVs or monitors.

#### 4 Audio/video inputs 1 and 3

Two sets of audio/video inputs (stereo analog audio; S-video and standard (composite) video jack) that you can use to connect to satellite receivers, TVs, VCR or other source component for recording.

#### 5 CONTROL IN

Use to control this recorder from the remote sensor of another Pioneer component with a CONTROL OUT terminal and bearing the Pioneer am mark. Connect the CONTROL OUT of the other component to the CONTROL IN of this recorder using a mini-plug cord.

#### **6 OPTICAL DIGITAL OUT**

5

A digital audio output for connecting to an AV amp/ receiver, Dolby Digital/DTS decoder or other equipment with optical digital input.

Connect to a power outlet using the supplied power cable after making all other connections.

## Front panel connections



On the left side of the front panel a flip-down cover hides a second audio/video input, consisting of an S-video and standard (composite) video jack, and stereo analog audio jacks.

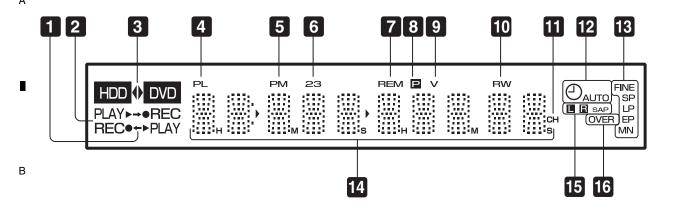
On the right side is the DV input/output i.LINK connector. This is for connection to a DV camcorder.

143

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## **Display**



#### 1 ← / →

Arrows indicate the copy direction between the HDD ( HDD ) and DVD ( DVD ).

#### ► PLAY/ • REC indicators

Lights during playback / recording; blinks when playback / recording is paused.

#### HDD ◀▶ DVD

The' 

'and ' 

'indicators light to indicate that the HDD or DVD is selected for recording/playback.

Lights when a VR mode disc is loaded and the recorder is in Play List mode.

Lights to indicate PM (after midday) for the clock display.

Shows the remote control mode (if nothing is displayed, the remote control mode is 1).

Lights when the character display is showing the remaining available recording time.

Lights when the component video output is set to progressive scan.

Lights when an unfinalized Video mode disc is loaded.

#### 10 R/RW

Indicates the type of recordable DVD loaded: DVD-R or DVD-RW.

#### 11 CH

Channel indicator for the built-in TV tuner.

### **12** ( )

Lights when a timer recording has been set. (Indicator blinks if the timer has been set to DVD but there isnít a recordable disc loaded or the timer has been set to HDD but the HDD is not recordable)

Lights when Auto Start Recording has been set, and during Auto Start Recording.

#### 13 Recording quality indicators

3

Lights when the recording mode is set to FINE (best quality).

Lights when the recording mode is set to SP (standard play).

Lights when the recording mode is set to LP(long play).

#### EP

Lights when the recording mode is set to EP (extended play).

#### MN

Lights when the recording mode is set to MN (manual recording level) mode.

#### 14 Character display

#### 15 Channel recording indicators

Indicates which channels are recorded when Dual Mono is selected.

Lights when the currently selected TV channel has a Second Audio Program channel.

#### 16 OVER

Lights when the analog audio input level is too high.

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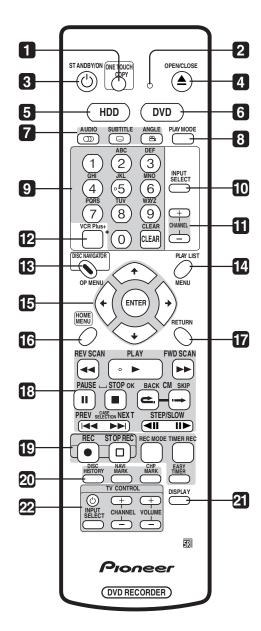
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#### Remote control



#### 1 ONE TOUCH COPY

Press to start One Touch Copy of the currently playing title to DVD or the HDD.

#### 2 Remote control indicator

Lights when setting up the remote control for use with a TV and when setting the remote control mode

#### 3 USTANDBY/ON

Press to switch the recorder on/into standby.

#### 4 ≜ OPEN/CLOSE

Press to open/close the disc tray.

Press to select the hard disk (HDD) for recording or playback.

#### 6 DVD

6

Press to select the DVD for recording or playback.

#### 7 DVD playback functions

#### AUDIO (33)

Changes the audio language or channel. (When the recorder is stopped, press to change the tuner audio.)

#### SUBTITLE ....

Displays/changes the subtitles included in multilingual DVD-Video discs.

#### ANGLE ₽

Switches camera angles on discs with multi-angle scenes.

#### 8 PLAY MODE

Press to display the Play Mode menu (for features such as search, repeat and program play).

#### 9 Alphanumeric buttons and CLEAR

Use the number buttons for track/chapter/title selection; channel selection, and so on. The same buttons can also be used to enter names for titles, discs and so on.

Use CLEAR to clear an entry and start again.

#### 10 INPUT SELECT

Press to change the input to use for recording.

#### 11 CHANNEL +/-

Press to change the channel of the built-in TV tuner.

#### 12 VCR Plus+

Press, then use the number buttons to enter a PlusCode® programming number for timer recording.

#### 13 DISC NAVIGATOR / TOP MENU

Press to display the Disc Navigator screen, or the top menu if a DVD-Video disc is loaded.

#### 14 PLAY LIST / MENU

Press to switch between Original and Play List content on VR mode discs, or display the disc menu if a DVD-Video disc is loaded.

### 15 ↑/↓/←/→ (cursor buttons)and ENTER

Used to navigate all on-screen displays. Press ENTER to select the currently highlighted option.

#### 16 HOME MENU

Press to display the Home Menu, from which you can navigate all the functions of the recorder.

#### 17 RETURN

Press to go back one level in the on-screen menu or display.

#### 18 Playback controls

#### ■■ REV SCAN / FWD SCAN ▶▶

Press to start reverse or forward scanning. Press again to change the speed.

#### ► PLAY

Press to start playback.

#### **II PAUSE**

Press to pause playback or recording.

Press to stop playback.

145

2 3

#### CM SKIP(commercial skip)

Press repeatedly to skip progressively forward on the

#### I◀◀ PREV / NEXT ▶▶I

Press to skip to the previous or next title/chapter/ track/folder; or to display the previous or next menu page.

#### **◄II STEP/SLOW II▶**

During playback, press to start slow-motion playback; while paused, press to show the previous or next video frame.

#### 19 Recording controls

#### REC

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Press to start recording. Press repeatedly to set the recording time in blocks of 30 mins.

#### ☐ STOP REC

Press to stop recording.

#### REC MODE

Press repeatedly to change the recording mode (picture quality).

#### TIMER REC

Press to set a timer recording from the standard Timer Recording screen.

#### **EASY TIMER**

Press to set a timer recording from the Easy Timer Recording screen.

#### **20 DISC HISTORY**

Press to display summary information (disc name, recording time left, etc.) from the last 30 recordable discs loaded.

#### **NAVI MARK**

Press to select a thumbnail picture for the current title for use in the Disc Navigator screen.

## CHP MARK

Press to insert a chapter marker when playing/recording a VR mode DVD-RW disc.

#### 21 DISPLAY

Displays/changes the on-screen information displays.

#### 22 TV CONTROL

After setting up, use these controls to control your TV.

146

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## ■ Jigs list

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Name	Jig No.	Remarks
Service Remote Control Unit	GGF1381	adjustment, diagnosis
DVD Test Disc (DVD-Video)	GGV1025	Check of DVD-Video
DVD Recorder Data Disc	GGV1179 (*)	diagnosis (ID data setting)
Flexible Cable (40P)	GGD1370	diagnosis of Drive Assy

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(\*): GGV1134 is now released, however GGV1179 will be released in JUNE/2004. Until GGV1179 is released, use GGV1134 data disc.

148

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